RoHS

COMPLIANT

HALOGEN FREE



Vishay Semiconductors

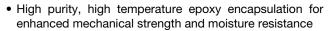
High Performance Schottky Rectifier, 65 A



PRIMARY CHARACTERISTICS					
I _{F(AV)} 65 A					
V _R	15 V				
V _F at I _F	0.46 V				
I _{RM} max.	870 mA at 100 °C				
T _J max.	125 °C				
E _{AS}	9 mJ				
Package	TO-247AC 3L				
Circuit configuration	Single				

FEATURES

- 125 °C T_J operation (V_R < 5 V)
- · Single diode configuration
- · Optimized for OR-ing applications
- Ultralow forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability



- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

DESCRIPTION

The VS-65PQ015... Schottky rectifier module has been optimized for ultralow forward voltage drop specifically for the OR-ing of parallel power supplies. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	65	А		
V_{RRM}		15	V		
I _{FSM}	t _p = 5 μs sine	1500	Α		
V _F	65 A _{pk} , T _J = 125 °C	0.46	V		
TJ	Range	-55 to +125	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VS-65PQ015-N3	UNITS	
Maximum DC rayaraa yaltaga	\/_	T _J = 100 °C	15	V	
Maximum DC reverse voltage	V_R	T _J = 125 °C	5	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST COND	ITIONS	VALUES	UNITS
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T _C = 83 °C,	50 % duty cycle at T _C = 83 °C, rectangular waveform		
Maximum peak one cycle	I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	1500	А
non-repetitive surge current		10 ms sine or 6 ms rect. pulse	V _{RRM} applied	400	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 2 A, L = 4.5 mH		9	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5 \times V_R$ typical		2	А



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
		65 A	T _{.1} = 25 °C	0.50	V
Forward voltage drop	V _{FM} ⁽¹⁾	130 A	11 = 23 0	0.71	
Forward voltage drop	VFM ('')	65 A	T 105 °C	0.46	
		130 A	− T _J = 125 °C	0.76	
	I _{RM} ⁽¹⁾	T _J = 125 °C	$V_R = 5 V$	1.2	Α
Reverse leakage current		T _J = 25 °C	M. DatadW	18	mA
		T _J = 100 °C	V _R = Rated V _R	870	
Threshold voltage	V _{F(TO)}	T - T movimum		0.137	mV
Forward slope resistance	r _t	$T_J = T_J$ maximum		4.9	mΩ
Maximum junction capacitance	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		4300	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		8	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

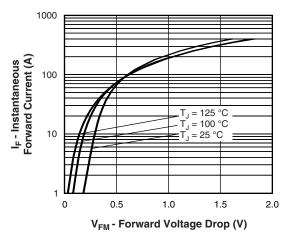
Note

 $^{(1)}~$ Pulse width $<300~\mu s,~duty~cycle < 2~\%$

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction temperature range	T_J		-55 to 125	°C	
Maximum storage temperature range	T _{Stg}		-55 to 150	10	
Maximum thermal resistance, junction to case	R _{thJC}	DC operation	0.8	°C/W	
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased	0.3	·C/VV	
Approximate weight			6	g	
Approximate weight			0.21	OZ.	
Mounting torque minimum		Nicos Is designated the conde	6 (5)	kgf · cm	
Mounting torque maximum		Non-lubricated threads	12 (10)	(lbf · in)	
Marking device		Case style TO-247AC 3L	65P0	2015	







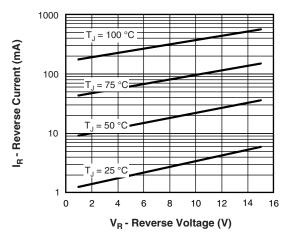


Fig. 1 - Maximum Forward Voltage Drop Characteristics

Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

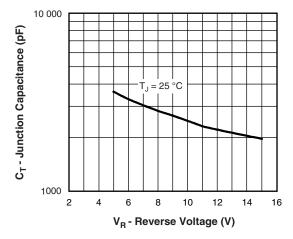


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

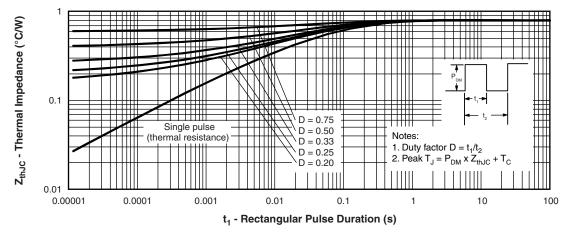


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics



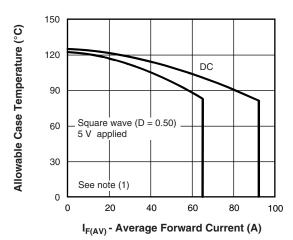


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

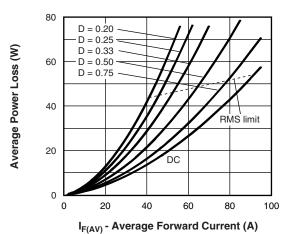


Fig. 6 - Forward Power Loss Characteristics

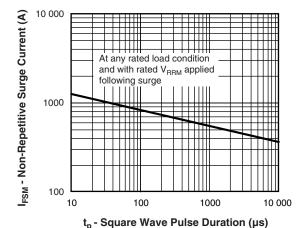


Fig. 7 - Maximum Non-Repetitive Surge Current

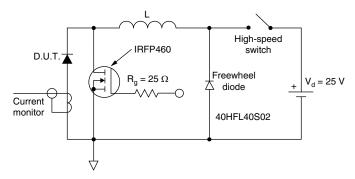


Fig. 8 - Unclamped Inductive Test Circuit

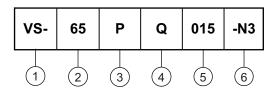
Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = 5 \text{ V} \\ \end{array}$



ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product

2 - Current rating (65 = 65 A)

3 - Package:

P = TO-247

4 - Schottky "Q" series

- Voltage code (015 = 15 V)

6 - Environmental digit

-N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

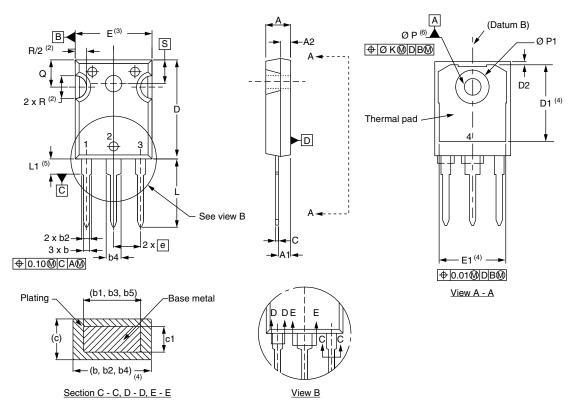
ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-65PQ015-N3	25	500	Antistatic plastic tube		

LINKS TO RELATED DOCUMENTS		
Dimensions	www.vishay.com/doc?96138	
Part marking information	www.vishay.com/doc?95007	
SPICE model	www.vishay.com/doc?97120	



TO-247AC 3L

DIMENSIONS in millimeters and inches



SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STINIDUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.17	1.37	0.046	0.054	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.34	0.065	0.092	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
С	0.38	0.89	0.015	0.035	
c1	0.38	0.84	0.015	0.033	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

SYMBOL	MILLIMETERS INCHES		MILLIMETERS		INCHES		NOTES
OTIVIDOL	MIN.	MAX.	MIN.	MAX.	NOTES		
D2	0.51	1.35	0.020	0.053			
E	15.29	15.87	0.602	0.625	3		
E1	13.46	-	0.53	-			
е	5.46	BSC	0.215	BSC			
ØK	0.2	254	0.0)10			
L	14.20	16.10	0.559	0.634			
L1	3.71	4.29	0.146	0.169			
ØΡ	3.56	3.66	0.14	0.144			
Ø P1	-	7.39	-	0.291			
Q	5.31	5.69	0.209	0.224			
R	4.52	5.49	0.178	0.216			
S	5.51 BSC		0.217	BSC			

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension Q



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Vishay

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