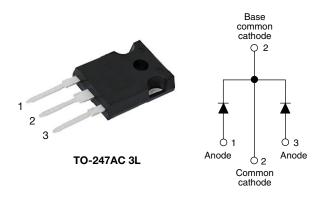
# High Performance Schottky Rectifier, 2 x 35 A



www.vishay.com

PRIMARY CHARACTERISTICS							
I <sub>F(AV)</sub> 2 x 35 A							
V <sub>R</sub>	30 V						
V <sub>F</sub> at I <sub>F</sub>	0.43 V						
I <sub>RM</sub> max.	450 mA at 125 °C						
T <sub>J</sub> max.	150 °C						
E <sub>AS</sub>	27 mJ						
Package	TO-247AC 3L						
Circuit configuration	Common cathode						

### FEATURES

- 150 °C T<sub>J</sub> operation
- Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance



COMPLIANT HALOGEN

- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### DESCRIPTION

The VS-72CPQ030... center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL CHARACTERISTICS VALUES									
I <sub>F(AV)</sub>	Rectangular waveform	70	А						
V <sub>RRM</sub>		30	V						
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	2180	А						
V <sub>F</sub>	35 $A_{pk}$ , $T_J = 125 \ ^{\circ}C$ (per leg)	0.43	V						
TJ	Range	-55 to +150	°C						

VOLTAGE RATINGS							
PARAMETER SYMBOL VS-72CPQ030-N3 U							
Maximum DC reverse voltage	V <sub>R</sub>	20					
Maximum working peak reverse voltage	V <sub>RWM</sub>	м 30					

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS			
Maximum average forward per leg	1	50 % duty cyclo at $T_{-} = 125$ %		35				
current, see fig. 5 per device	I <sub>F(AV)</sub>	50 % duty cycle at $T_C$ = 125 °C, rectangular waveform		70				
Maximum peak one cycle non-repetitive	I <sub>FSM</sub>	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	2180	A			
surge current per leg, see fig. 7		10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	600				
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 6 A, L = 1.5 mH		27	mJ			
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		6	А			

 Revision: 02-Apr-2019
 1
 Document Number: 96463

 For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com
 DiodesEurope@vishay.com

 THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000



www.vishay.com

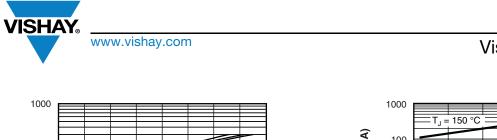
## Vishay Semiconductors

ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS					
		35 A	T, = 25 °C	0.51				
Maximum forward voltage drop per leg	V <sub>FM</sub> <sup>(1)</sup>	70 A	1j=25 0	0.61	V			
See fig. 1	VFM (*)	35 A	T.I = 125 °C	0.43				
		70 A	1j = 125 C	0.58				
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	1.9	mA			
See fig. 2		T <sub>J</sub> = 125 °C	VR - naleu VR	450				
Threshold voltage	V <sub>F(TO)</sub>			0.25	V			
Forward slope resistance	r <sub>t</sub>	$T_J = T_J$ maximum		4.7	mΩ			
Maximum junction capacitance per leg	CT	$V_R = 5 V_{DC}$ (test signal range	4600	pF				
Typical series inductance per leg	Ls	Measured lead to lead 5 m	7.5	nH				
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>	10 000	V/µs				

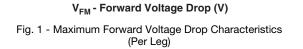
Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55 to 150	°C				
Maximum thermal resistance, junction to case per leg		DC operation See fig. 4	0.8					
Maximum thermal resistance, junction to case per package	– R <sub>thJC</sub>	DC operation	0.4	°C/W				
Typical thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, smooth and greased	0.25					
Approximate weight			6	g				
Approximate weight			0.21	oz.				
Mounting torque	num		6 (5)	kgf ⋅ cm				
maxir	num		12 (10)	(lbf · in)				
Marking device		Case style TO-247AC 3L	72CP	Q030				



1.6



T<sub>J</sub> = 150 °C T<sub>J</sub> = 125 °C

T<sub>1</sub> = 25 °C

I<sub>F</sub> - Instantaneous Forward Current (A)

100

10

1

0

0.2 0.4 0.6 0.8 1.0 1.2 1.4

I<sub>R</sub> - Reverse Current (mA) 100 T<sub>J</sub> = 125 °C 10 = 100 °C = 75 °C = 50 °C 0.1 25 °C 0.01 30 0 5 10 15 20 25 V<sub>R</sub> - Reverse Voltage (V)

Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

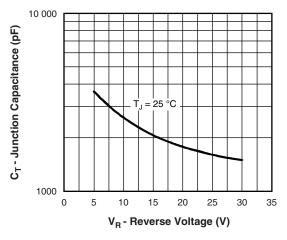


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

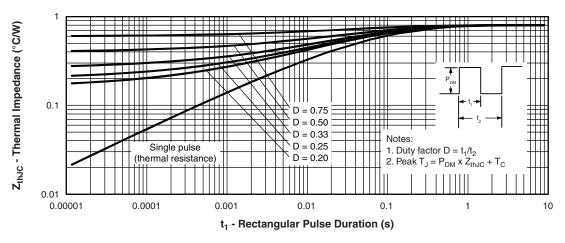
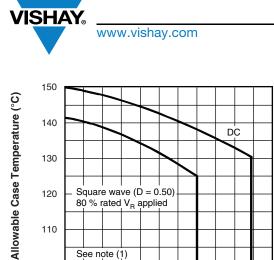


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)

## Vishay Semiconductors



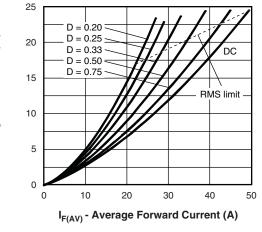


100

0 5

 Image: Constraint of the second se







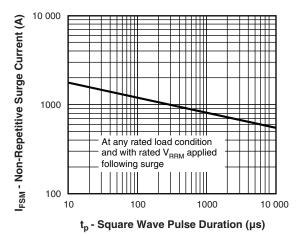


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

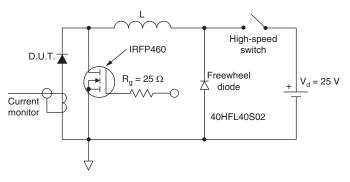


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \ \mathsf{x} \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \ \mathsf{x} \ \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$ 

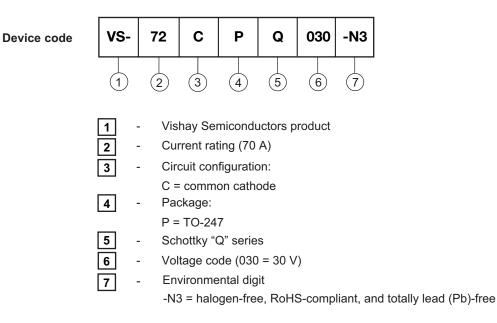
Revision: 02-Apr-2019

4

For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>



### **ORDERING INFORMATION TABLE**



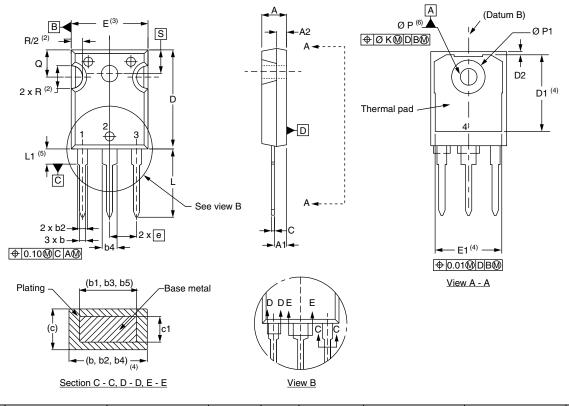
ORDERING INFORMATION (Example)								
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION					
VS-72CPQ030-N3	25	500	Antistatic plastic tube					

LINKS TO RELATED DOCUMENTS						
Dimensions www.vishay.com/doc?96138						
Part marking information	www.vishay.com/doc?95007					
SPICE model	www.vishay.com/doc?96597					



TO-247AC 3L

### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIMETERS		INC	HES	NOTES		SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STWDOL	MIN.	MAX.	MIN.	MAX.	NOTES
A	4.65	5.31	0.183	0.209			D2	0.51	1.35	0.020	0.053	
A1	2.21	2.59	0.087	0.102			E	15.29	15.87	0.602	0.625	3
A2	1.17	1.37	0.046	0.054			E1	13.46	-	0.53	-	
b	0.99	1.40	0.039	0.055			е	5.46	BSC	0.215	5 BSC	
b1	0.99	1.35	0.039	0.053			ØК	0.2	254	0.0	)10	
b2	1.65	2.39	0.065	0.094			L	14.20	16.10	0.559	0.634	
b3	1.65	2.34	0.065	0.092			L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135			ØΡ	3.56	3.66	0.14	0.144	
b5	2.59	3.38	0.102	0.133			Ø P1	-	7.39	-	0.291	
С	0.38	0.89	0.015	0.035			Q	5.31	5.69	0.209	0.224	
c1	0.38	0.84	0.015	0.033			R	4.52	5.49	0.178	0.216	
D	19.71	20.70	0.776	0.815	3		S	5.51	BSC	0.217	' BSC	
D1	13.08	-	0.515	-	4							

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5M-1994

(2) Contour of slot optional

(3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

(4) Thermal pad contour optional with dimensions D1 and E1

<sup>(5)</sup> Lead finish uncontrolled in L1

<sup>(6)</sup> Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")

<sup>(7)</sup> Outline conforms to JEDEC<sup>®</sup> outline TO-247 with exception of dimension Q

Revision: 20-Jun-17

1



Vishay

# Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Vishay products are not designed for use in life-saving or life-sustaining applications or any application in which the failure of the Vishay product could result in personal injury or death unless specifically qualified in writing by Vishay. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

© 2025 VISHAY INTERTECHNOLOGY, INC. ALL RIGHTS RESERVED

Revision: 01-Jan-2025

1