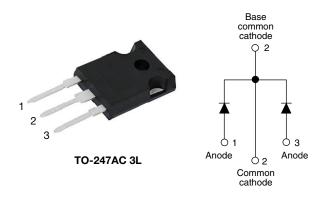
**Vishay Semiconductors** 

## High Performance Schottky Rectifier, 2 x 20 A



www.vishay.com

PRIMARY CHARACTERISTICS								
I <sub>F(AV)</sub>	2 x 20 A							
V <sub>R</sub>	45 V							
V <sub>F</sub> at I <sub>F</sub>	0.49 V							
I <sub>RM</sub> max.	80 mA at 100 °C							
T <sub>J</sub> max.	150 °C							
E <sub>AS</sub>	20 mJ							
Package	TO-247AC 3L							
Circuit configuration	Common cathode							

### **FEATURES**

- 150 °C T<sub>J</sub> operation
- · Very low forward voltage drop
- · High frequency operation
- purity, High high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance



COMPLIANT

- HALOGEN FREE
- · Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified according to JEDEC<sup>®</sup>-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### DESCRIPTION

The VS-STPS40L45CW... center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies.

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL	CHARACTERISTICS	VALUES	UNITS					
I <sub>F(AV)</sub>	Rectangular waveform	40	А					
V <sub>RRM</sub>		45	V					
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	1240	А					
V <sub>F</sub>	20 $A_{pk}$ , $T_J$ = 125 °C (per leg, typical)	0.42	V					
TJ		-55 to +150	°C					

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-STPS40L45CW-N3	UNITS	
Maximum DC reverse voltage	VR	45	V	
Maximum working peak reverse voltage	V <sub>RWM</sub>	45	v	

ABSOLUTE MAXIMUM RATINGS										
PARAMETER		SYMBOL	TEST COND	VALUES	UNITS					
Maximum average forward	per device	1	50 % duty cycle at T <sub>C</sub> = 122 °C	rootangular wavoform	40					
current, see fig. 5	per leg	I <sub>F(AV)</sub>	$30\%$ duty cycle at $T_{\rm C} = 122$	20	А					
Maximum peak one cycle non-repetitive surge current per leg, see fig. 7			5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	1240	A				
		I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	350					
Non-repetitive avalanche energy per leg		E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 3 A, L = 4.4 mH		20	mJ				
Repetitive avalanche currer	nt per leg	I <sub>AR</sub>	Current decaying linearly to zero Frequency limited by T <sub>J</sub> maximu		3	А				

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ELECTRICAL SPECIFICATIONS										
PARAMETER	SYMBOL	TEST CONDIT	IONS	TYP.	MAX.	UNITS				
		20 A	T.I = 25 °C	0.48	0.53					
Maximum forward voltage drop per leg See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	40 A	$I_{\rm J} = 25$ C	0.61	0.69	v				
	VFM \''	20 A	T.I = 125 °C	0.42	0.49					
		40 A	1j = 125 C	0.60	0.70	1				
Reverse leakage current per leg	I <sub>RM</sub> (1)	T <sub>J</sub> = 25 °C	$V_{B} = Rated V_{B}$	-	1.5	m (				
See fig. 2		$T_J = 100 \ ^\circ C$	VR = naleu VR	20	80	mA				
Threshold voltage	V <sub>F(TO)</sub> 0.27		27	V						
Forward slope resistance	r <sub>t</sub>	$T_{J} = T_{J}$ maximum			8.72					
Maximum junction capacitance per leg	CT	$V_{\rm R}$ = 5 $V_{\rm DC}$ (test signal range 10	-	1500	pF					
Typical series inductance per leg	LS	Measured lead to lead 5 mm fi	7.5	-	nH					
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>	000	V/µs						

### Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS					
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55 to 150	°C					
Maximum thermal resistance, junction to case per leg	P	DC operation See fig. 4	1.6						
Maximum thermal resistance, junction to case per package	R <sub>thJC</sub>	DC operation	0.8	°C/W					
Typical thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, smooth and greased	0.24						
Approvimete weight			6	g					
Approximate weight			0.21	oz.					
mi	nimum		6 (5)	kgf ⋅ cm (lbf ⋅ in)					
Mounting torque ma m	aximu	Non-lubricated threads	12 (10)						
Marking device		Case style TO-247AC 3L	STPS40	)L45CW					



## VS-STPS40L45CW-N3

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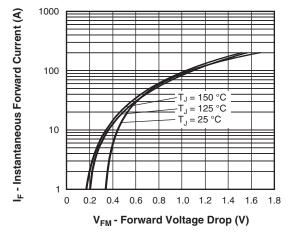


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

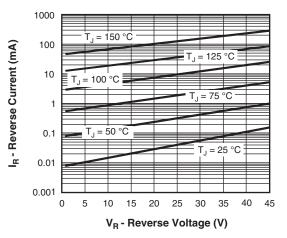


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

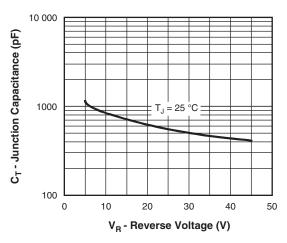
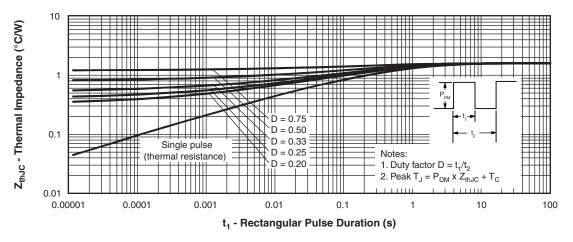
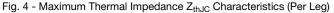


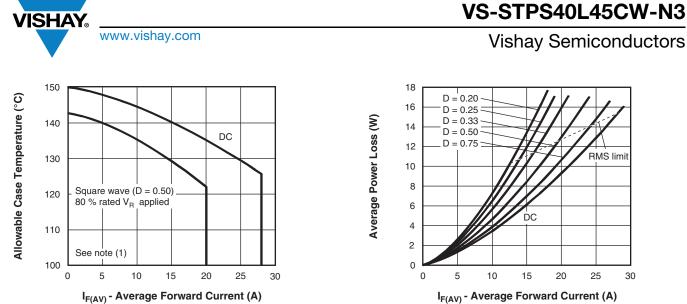
Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

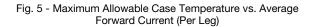




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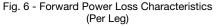
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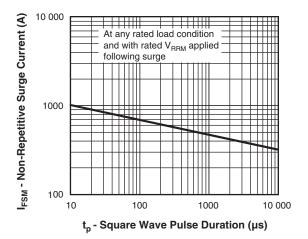


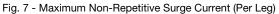


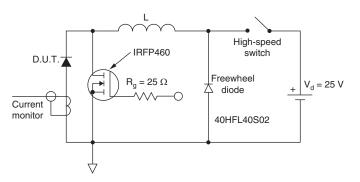


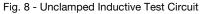
RMS limit











#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see fig. 6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$ 

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# VS-STPS40L45CW-N3

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### **ORDERING INFORMATION TABLE**

Device code	VS-	STPS	40	L	45	cw	-N3
	1	2	3	4	5	6	7
	1 2 3 4 5 6	- Sch - Cui - L = - Vol	nottky S rrent rat low for	micondu TPS ser ings (40 ward vol de (45 =	ies = 40 A) tage		
	7	- Env		247 ntal digi gen-free		-compli	ant, and

ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-STPS40L45CW-N3	25	500	Antistatic plastic tube						

LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?96138					
Part marking information	www.vishay.com/doc?95007					





**Vishay Semiconductors** 

TO-247AC 3L

### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIM	IETERS	INC	HES	NOTES	NOTES		MILLIN	IETERS	INC	HES	NOTES
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES		SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
A	4.65	5.31	0.183	0.209			D2	0.51	1.35	0.020	0.053	
A1	2.21	2.59	0.087	0.102			E	15.29	15.87	0.602	0.625	3
A2	1.17	1.37	0.046	0.054			E1	13.46	-	0.53	-	
b	0.99	1.40	0.039	0.055			е	5.46	BSC	0.215	5 BSC	
b1	0.99	1.35	0.039	0.053			ØК	0.2	254	0.0	)10	
b2	1.65	2.39	0.065	0.094			L	14.20	16.10	0.559	0.634	
b3	1.65	2.34	0.065	0.092			L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135			ØΡ	3.56	3.66	0.14	0.144	
b5	2.59	3.38	0.102	0.133			Ø P1	-	7.39	-	0.291	
С	0.38	0.89	0.015	0.035			Q	5.31	5.69	0.209	0.224	
c1	0.38	0.84	0.015	0.033			R	4.52	5.49	0.178	0.216	
D	19.71	20.70	0.776	0.815	3		S	5.51	BSC	0.217	' BSC	
D1	13.08	-	0.515	-	4							

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5M-1994

(2) Contour of slot optional

(3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

(4) Thermal pad contour optional with dimensions D1 and E1

<sup>(5)</sup> Lead finish uncontrolled in L1

<sup>(6)</sup> Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")

<sup>(7)</sup> Outline conforms to JEDEC<sup>®</sup> outline TO-247 with exception of dimension Q

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