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Hyperfast Rectifier, 15 A FRED Pt<sup>®</sup> G5



### LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS							
I <sub>F(AV)</sub> 15 A							
V <sub>R</sub>	1200 V						
V <sub>F</sub> at I <sub>F</sub> at 125 °C	1.7 V						
t <sub>rr</sub>	37 ns						
T <sub>J</sub> max.	175 °C						
Package	TO-220AC 2L						
Circuit configuration	Single						

### FEATURES

- Hyperfast and optimized Q<sub>rr</sub>
- Best in class forward voltage drop and switching RoHS
  losses trade off
  HALOGEN
- Optimized for high speed operation
- 175 °C maximum operating junction temperature
- Polyimide passivation
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### **DESCRIPTION / APPLICATIONS**

Featuring a unique combination of low conduction and switching losses, this rectifier is the right choice for high frequency converters, both soft switched / resonant.

Specifically designed to improve efficiency of PFC and output rectification stages of EV / HEV battery charging stations, booster stage of solar inverters and UPS applications, these devices are perfectly matched to operate with MOSFETs or high speed IGBTs.

### **MECHANICAL DATA**

Case: TO-220AC 2L

Molding compound meets UL 94 V-0 flammability rating

**Terminals:** matte tin plated leads, solderable per J-STD-002

Polarity: as per marking device details

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Repetitive peak reverse voltage	V <sub>RRM</sub>		1200	V				
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 110 °C, D = 0.50	15					
Repetitive peak forward current	I <sub>FRM</sub>	T <sub>C</sub> = 110 °C, D = 0.50, f = 20 kHz	30	А				
Non-repetitive peak surge current	I <sub>FSM</sub>	$T_C = 45 \text{ °C}, t_p = 10 \text{ ms}, \text{ sine wave}$	125					
Operating junction and storage temperature	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C				

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	1200	-	-			
Forward voltage	V	I <sub>F</sub> = 15 A	-	1.9	2.5	V		
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 15 A, T <sub>J</sub> = 125 °C	-	1.7	-			
Deverse leakerse eurrent		$V_{R} = V_{R}$ rated	-	-	50			
Reverse leakage current	I <sub>R</sub>	$T_J = 125 \text{ °C}, V_R = V_R \text{ rated}$	-	-	500	μA		
Junction capacitance	CT	V <sub>R</sub> = 200 V	-	10	-	pF		
Series inductance	L <sub>S</sub>	Measured to lead 5 mm from package body	-	8	-	nH		

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### Vishay Semiconductors

<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J = 25$ °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS		
		I <sub>F</sub> = 1.0 A, dI <sub>F</sub> /dt =	$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 100 \text{ A}/\mu\text{s}, V_R = 30 \text{ V}$			50			
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	95	-	ns		
		T <sub>J</sub> = 125 °C		-	146	-			
Peak recovery current		T <sub>J</sub> = 25 °C	I <sub>F</sub> = 10 A dI <sub>F</sub> /dt = 600 A/μs V <sub>R</sub> = 400 V	-	14	-	A		
reak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 125 °C		-	19	-			
Poverse receivery charge	0	T <sub>J</sub> = 25 °C		-	545	-	nC		
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	1200	-			
Reverse recovery time	+	T <sub>J</sub> = 25 °C		-	75.5	-	ns		
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	100	-			
Doole recovery current		T <sub>J</sub> = 25 °C	I <sub>F</sub> = 15 A dI <sub>F</sub> /dt = 1000 A/μs V <sub>B</sub> = 800 V	-	23	-	A		
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 125 °C		-	35	-			
Boyoroo roooyony oborgo	0	T <sub>J</sub> = 25 °C		-	935	-			
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	1985	-	nC		

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Thermal resistance, junction-to-case	R <sub>thJC</sub>		-	-	1.7	°C/W			
Weight			-	2.0	-	g			
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)			
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55	-	175	°C			
Marking device		Case style TO-220AC 2L	E5TH1512						

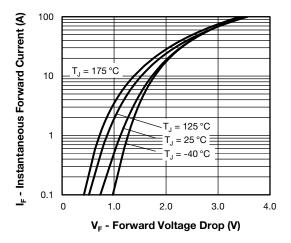


Fig. 1 - Forward Voltage Drop Characteristics

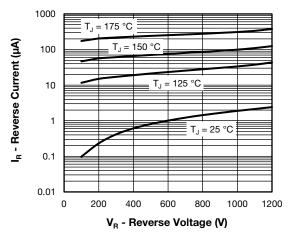


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



# VS-E5TH1512-M3

### **Vishay Semiconductors**

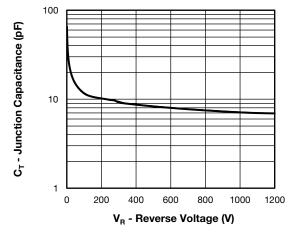


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

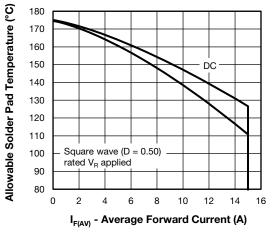


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

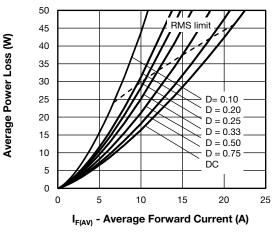


Fig. 5 - Forward Power Loss Characteristics

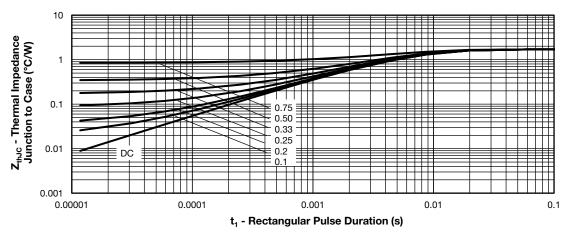


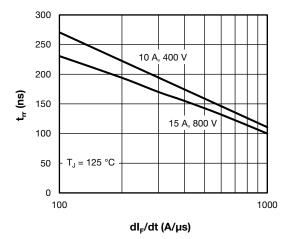
Fig. 6 - Transient Thermal Impedance, Junction to Case

Revision: 16-May-2022

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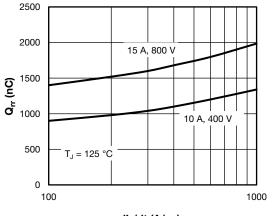




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Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt



 $dI_F/dt$  (A/µs) Fig. 8 - Typical Stored Charge vs.  $dI_F/dt$ 

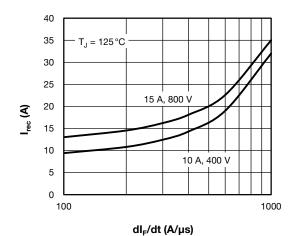


Fig. 9 - Typical Stored Charge vs. dl<sub>F</sub>/dt





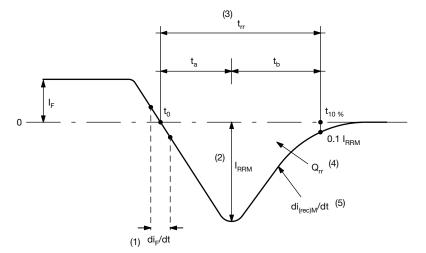


Fig. 10 - Reverse Recovery Waveform and Definitions

#### Notes

- $^{(1)}$  di<sub>F</sub>/dt rate of change of current through zero crossing
- (2) I<sub>RRM</sub> peak reverse recovery current
- $^{(3)}$  t<sub>rr</sub> reverse recovery time measured from t<sub>0</sub>, crossing point of negative going I<sub>F</sub>, to point t<sub>10%</sub>, 0.1 I<sub>RRM</sub>
- $^{(4)}\,$  Qrr area under curve defined by  $t_0$  and  $t_{10}$   $_{\%}$

$$Q_{rr} = \int_{t_0}^{t_{10\%}} I(t) dt$$

 $^{(5)}$  di\_{(rec)}M/dt - peak rate of change of current during  $t_b$  portion of  $t_{rr}$ 

#### **ORDERING INFORMATION TABLE**

Device code	VS-	E	5	т	н	15	12	-МЗ
		2	3	4	5	6	7	8
	1 ·	- Visł	nay Sem	nicondu	ctors pr	oduct		
	2 ·	E=	single o	diode				
	3 -	- 5 =	FRED g	jeneratio	on 5			
	4 -	· Pac	kage:					
		-	TO-220					
	5 -	• H=	hyperfa	ast reco	very			
	6	- Cur	rent rati	ng (15 =	= 15 A)			
	7	- Vol	tage rati	ing (12 =	= 1200 \	/)		
	8 -	- Env	rironmer	ntal digit	:			
		-M3	3 = halo	gen-free	e, RoHS	-compli	ant, and	d termin

ORDERING INFORMATION (Example)						
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION				
VS-E5TH1512-M3	50	Antistatic plastic tubes				
VS-E3TH1512-W3	50	Antistatic plastic tubes				

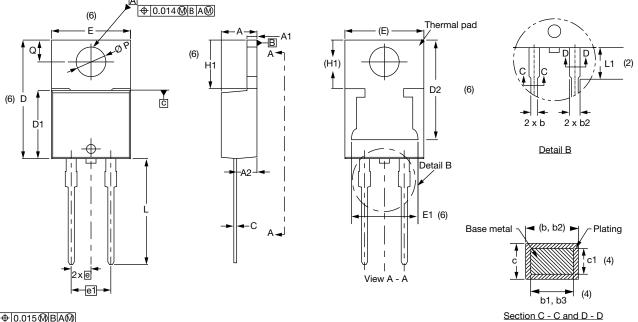
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**TO-220AC 2L** 

### **DIMENSIONS** in millimeters and inches



⊕0.015@BA@



SYMBOL	MBOI MILLIMETE		INC	HES	NOTES
STMBOL	MIN.	MIN. MAX. MIN		MAX.	NOTES
A	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.50	2.92	0.098	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.35	0.585	0.604	3
D1	8.38	9.02	0.330	0.355	

Conforms to JEDEC	® outline TO-220AC

SYMBOL	MILLIMETERS INCHES			NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	11.68	13.30	0.460	0.524	6, 7
E	10.11	10.51	0.398	0.414	3, 6
E1	6.86	8.89	0.270	0.350	6
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØР	3.54	3.91	0.139	0.154	
Q	2.60	3.00	0.102	0.118	

#### Notes

 $^{(1)}\,$  Dimensioning and tolerancing as per ASME Y14.5M-1994

<sup>(2)</sup> Lead dimension and finish uncontrolled in L1

<sup>(4)</sup> Dimension b1, b3, and c1 apply to base metal only

(5) Controlling dimensions: inches

- <sup>(6)</sup> Thermal pad contour optional within dimensions E, H1, D2, and E1
- <sup>(7)</sup> Outline conforms to JEDEC<sup>®</sup> TO-220, except D2

Revision: 22-Feb-2024

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<sup>(3)</sup> Dimension D, D1, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body



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Revision: 01-Jan-2025

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