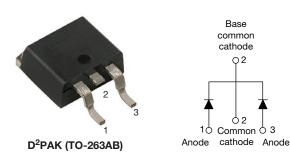
Vishay Semiconductors

High Performance Schottky Rectifier, 2 x 15 A



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SHAY

PRIMARY CHARACTERISTICS								
I _{F(AV)}	2 x 15 A							
V _R	35 V, 40 V, 45 V							
V _F at I _F	0.50 V							
I _{RM} typ.	70 mA at 125 °C							
T _J max.	150 °C							
E _{AS}	20 mJ							
Package	D ² PAK (TO-263AB)							
Circuit configuration	Common cathode							

FEATURES

- 150 °C T_J operation
- · Center tap configuration
- · Very low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- · Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- AEC-Q101 qualified, meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The VS-25CTQ... center tap Schottky rectifier series has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MECHANICAL DATA

Case: D²PAK (TO-263AB)

Molding compound meets UL 94 V-0 flammability rating

Terminals: matte tin plated leads, solderable per J-STD-002

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	MBOL CHARACTERISTICS								
I _{F(AV)}	Rectangular waveform	30	A						
V _{RRM}	Range	35 to 45	V						
I _{FSM}	t _p = 5 μs sine	990	A						
V _F	15 A_{pk} , $T_J = 125 \ ^{\circ}C$ (per leg)	0.50	V						
TJ	Range	-55 to +150	С°						

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-25CTQ045SLHM3	UNITS
Maximum DC reverse voltage	V _R	45	М
Maximum working peak reverse voltage	V _{RWM}	45	v

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VS-25CTQ045SLHM3



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ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST COND	VALUES	UNITS					
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T_{C} = 102 °C	30						
Maximum peak one cycle		I _{FSM} 5 μs sine or 3 μs rect. pulse 10 ms sine or 6 ms rect. pulse Following any rated load condition and with rated V _{RRM} applied		990	А				
non-repetitive surge current per leg See fig. 7	I _{FSM}			250					
Non-repetitive avalanche energy per leg	E _{AS}	$T_J = 25 \ ^{\circ}C, \ I_{AS} = 3 \ A, \ L = 4.40$	20	mJ					
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zer Frequency limited by T _J maxim	3	А					

ELECTRICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS						
Maximum forward voltage drop per leg See fig. 1		15 A	T _{.1} = 25 °C	0.56					
	V _{FM} ⁽¹⁾	30 A	1j=25 C	0.71	v				
	VFM ()	15 A	T.I = 125 °C	0.50					
		30 A	1j=125 C	0.64					
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	1.75	mA				
per leg		T _J = 125 °C	$v_{\rm R} = naleu v_{\rm R}$	110					
Typical reverse leakage current	I _{RM} ⁽¹⁾	T _J = 125 °C	V _R = Rated V _R	70	mA				
Maximum junction capacitance per leg	CT	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		900	pF				
Typical series inductance per leg	LS	Measured lead to lead 5 m	8.0	nH					
Maximum voltage rate of change	dV/dt	Rated V _R	Rated V _R 1						

Note

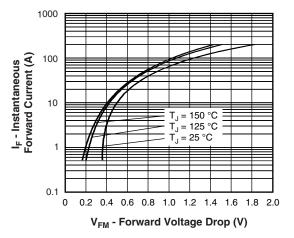
 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

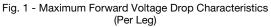
THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS					
Maximum junction and storage temperature range	T _J , T _{Stg}		-55 to 150	°C					
Maximum thermal resistance, junction to case per leg	P	DC operation See fig. 4	3.25						
Maximum thermal resistance, junction to case per package	— R _{thJC}	DC operation	1.63	°C/W					
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased	0.50						
Approximate weight			2	g					
Approximate weight			0.07	oz.					
Mounting torque			6 (5)	kgf · cm					
Mounting torque maximum			12 (10)	(lbf · in)					
Marking device		Case style D ² PAK (TO-263AB)	25CTQ0)45SH					



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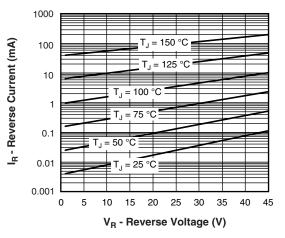


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

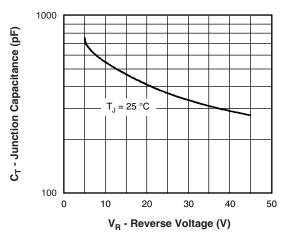


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

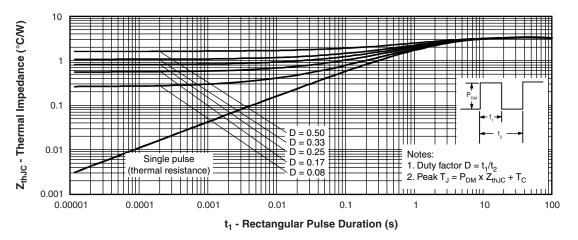


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

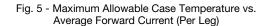
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160 150 25CTQ R_{thJC} (DC) = 3.25 °C/W 140 130 120 DC 110 100 5 10 15 20 0 25 I_{F(AV)} - Average Forward Current (A)



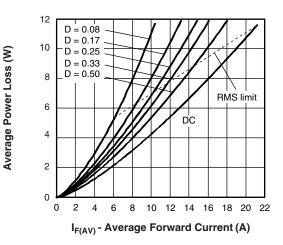


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

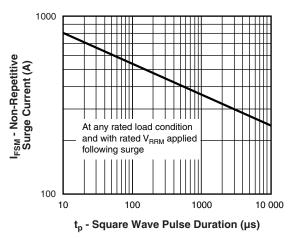


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

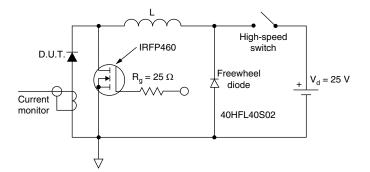


Fig. 8 - Unclamped Inductive Test Circuit

Note

Allowalde Case Temperature (°C)

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
- Pd = forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); Pd_{BEV} = inverse power loss = $V_{B1} \times I_B (1 - D)$; I_B at $V_{B1} = 80$ % rated V_B

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VS-25CTQ045SLHM3

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ORDERING INFORMATION TABLE

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VISHAY

Device code	VS-	25	С	т	Q	045	S	L	н	М3
		(2)	(3)	(4)	(5)	(6)	$\overline{(7)}$	(8)	(9)	(10)
	<u> </u>	\bigcirc	\bigcirc	nicondu	\cup	Ŭ	\bigcirc	\bigcirc	\bigcirc	\bigcirc
	2 -		-	ng (25 A		oduci				
	3 - Circuit configuration: C = common cathode									
	4 - T = TO-220 or D ² PAK / TO-262									
	5 -	- Sch	nottky "O	Q" series	5					
	6 -	- Vol	tage rati	ng (045	= 45 V)					
	7 - S = D ² PAK (TO-263AB)									
	8 -	- L=	tape an	d reel (l	eft orier	nted) - fo	or differ	ent orie	ntation,	contact
	9 -	• Н=	AEC-Q	101 qua	alified					
	10 -	- M3	= halog	en-free,	RoHS-	complia	nt, and	termina	ation lea	ıd (Pb)-f

ORDERING INFORMATION								
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION					
VS-25CTQ045SLHM3	800	800	13" diameter reel					

LINKS TO RELATED DOCUMENTS							
Dimensions www.vishay.com/doc?95046							
Part marking information	www.vishay.com/doc?95444						
Packaging information	www.vishay.com/doc?96317						
SPICE model	www.vishay.com/doc?95285						

Outline Dimensions



D²PAK

DIMENSIONS in millimeters and inches

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SHA



SYMBOL	MILLIMETERS		INCHES		NOTES	SYMBOL	MILLIM	IETERS	INC	HES	NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTED	STWDUL	MIN.	MAX.	MIN.	MAX.	NOTES	
А	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

⁽⁵⁾ Datum A and B to be determined at datum plane H

⁽⁶⁾ Controlling dimension: inch

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-263AB

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