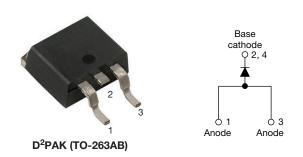


High Voltage Surface-Mount Input Rectifier Diode, 25 A



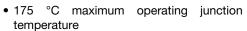
LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTE	RISTICS				
I _{F(AV)} 25 A					
V _R	1200 V				
V _F at I _F	1.14 V				
I _{FSM}	255 A				
T _J max.	175 °C				
Package	D ² PAK (TO-263AB)				
Circuit configuration	Single				

FEATURES

 Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C





- · Glass passivated pellet chip junction
- AEC-Q101 qualified
- Meets JESD 201 class 1A whisker test
- Flexible solution for reliable AC power rectification
- High surge, low V_F rugged blocking diode for DC charging stations
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- · Input rectification
- On-board and off-board EV / HEV battery chargers

DESCRIPTION

The VS-25ETS12SLHM3 rectifier High Voltage Series has been optimized for very low forward voltage drop, with moderate leakage.

MECHANICAL DATA

Case: D²PAK (TO-263AB)

Molding compound meets UL 94 V-0 flammability rating **Terminals:** matte tin plated leads, solderable per

J-STD-002

OUTPUT CURRENT IN TYPICAL APPLICATIONS								
APPLICATIONS	PLICATIONS SINGLE-PHASE BRIDGE THREE-PHASE BRIDGE UNITS							
Capacitive input filter T _A = 55 °C, T _J = 125 °C common heatsink of 1 °C/W	20	23	А					

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL CHARACTERISTICS VALUES UN								
I _{F(AV)}	Sinusoidal waveform	25	А					
V _{RRM}		1200	V					
I _{FSM}		255	А					
V _F	10 A, T _J = 25 °C	1.0	V					
T _J		-40 to +175	°C					

VOLTAGE RATINGS									
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 175 °C mA						
VS-25ETS12SLHM3	1200	1300	3						





ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum average forward current	I _{F(AV)}	T _C = 125 °C, 180° conduction half sine wave	25				
Maximum peak one cycle		10 ms sine pulse, rated V_{RRM} applied, at $T_J = 175\ ^{\circ}\text{C}$	215	А			
non-repetitive surge current	IFSM	10 ms sine pulse, no voltage reapplied, at $T_J = 175~^{\circ}\text{C}$	255				
Maximum 12t for fusing	l ² t	10 ms sine pulse, rated V_{RRM} applied, at $T_J = 175\ ^{\circ}\text{C}$	231	A ² s			
Maximum I ² t for fusing	I-I	10 ms sine pulse, no voltage reapplied, at $T_J = 175$ °C	326	A-S			
Maximum I ² √t for fusing	I ² √t	$t = 0.1$ ms to 10 ms, no voltage reapplied, at T_J = 175 °C	3260	A²√s			

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST (VALUES	UNITS	
Maximum forward voltage drop	V _{FM}	25 A, T _J = 25 °C		1.14	V
Forward slope resistance	r _t	T _{.1} = 175 °C	12	mΩ	
Threshold voltage	V _{F(TO)}	1j=175 C	0.83	V	
		T _J = 25 °C		0.1	
Maximum reverse leakage current	I_{RM}	T _J = 150 °C	V_R = rated V_{RRM}	1.0	mA
		T _J = 175 °C		3.0	

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range	T _J , T _{Stg}		-40 to +175	°C			
Maximum thermal resistance, junction to case	R _{thJC}	DC operation	0.9				
Maximum thermal resistance, junction to ambient	R _{thJA} (1)	For D ² PAK version	62	°C/W			
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth, and greased	0.5				
Approximate weight			2	g			
Approximate weight			0.07	OZ.			
Marking device		Case style: D ² PAK (TO-263AB)	25ETS	12SH			

Note

 $^{^{(1)}}$ When mounted on 1" square (650 mm²) PCB of FR-4 or G-10 material 4 oz. (140 μ m) copper 40 °C/W

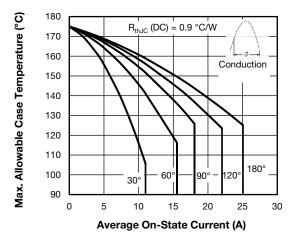


Fig. 1 - Current Rating Characteristics

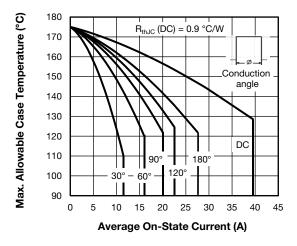


Fig. 2 - Current Rating Characteristics

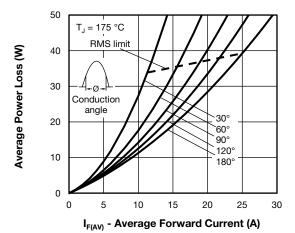


Fig. 3 - Forward Power Loss Characteristics

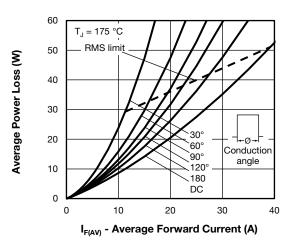


Fig. 4 - Forward Power Loss Characteristics

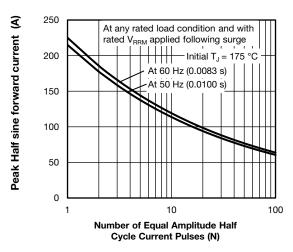


Fig. 5 - Maximum Non-Repetitive Surge Current

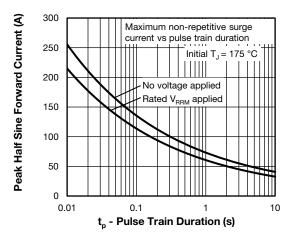


Fig. 6 - Maximum Non-Repetitive Surge Current

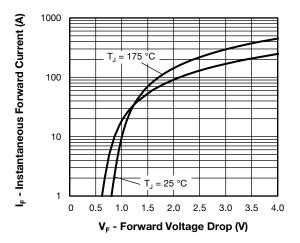


Fig. 7 - Forward Voltage Drop Characteristics

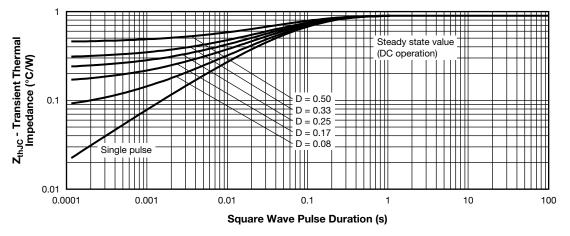
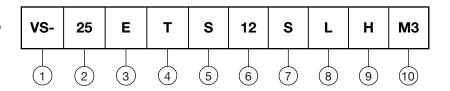


Fig. 8 - Thermal Impedance Z_{thJC} Characteristics



ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product

Current rating (25 = 25 A)

3 - Circuit configuration

E = single diode

4 - Package:

9

 $T = D^2PAK$

5 - Type of silicon:

S = standard recovery rectifier

7 - S = surface mountable

 L = tape and reel (left oriented), for different orientation, contact factory

H = AEC-Q101 qualified

- Environmental digit:

M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

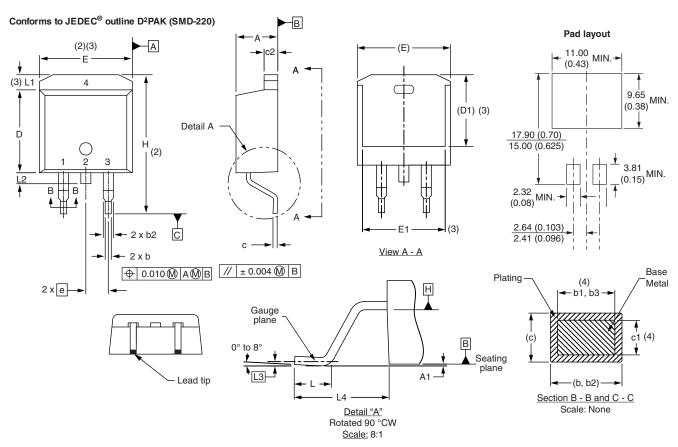
ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-25ETS12SLHM3	800	800	13" diameter reel				

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95046			
Part marking information	www.vishay.com/doc?95444			
Packaging information	www.vishay.com/doc?96317			
SPICE model	www.vishay.com/doc?95409			



D²PAK

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		HES NOTES		SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOIES	NOTES	STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			Е	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100) BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch
- (7) Outline conforms to JEDEC® outline TO-263AB



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Vishay

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