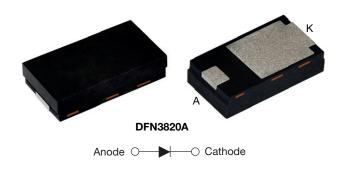


Ultrafast Rectifier, 1 A FRED Pt®



LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I _{F(AV)}	1 A			
V _R	200 V			
V _F at I _F	0.72 V			
t _{rr} (typ.)	15 ns			
I _{FSM}	32 A			
T _J max.	175 °C			
Package	DFN3820A			
Circuit configuration	Single			

FEATURES

- Very low profile typical height of 0.88 mm
- · Ideal for automated placement
- Wettable flanks allows easy inspection with AOI (automated optical inspection). No X-ray necessary



AUTOMOTIVE GRADE

- Low forward voltage drop, low power losses
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- For PFC, CRM snubber operation
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in high frequency inverters, DC/DC converters, freewheeling diodes, clamping and snubber, polarity protection, dual voltage injector drivers, piezo drivers, ECU, Antilock Braking Systems (ABS), HID and LED lighting

MECHANICAL DATA

Case: DFN3820A

Molding compound meets UL 94 V-0 flammability rating

Terminals: matte tin plated leads, solderable per

J-STD-002, meets JESD 201 class 2 whisker test

Polarity: color band denotes cathode end

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Peak repetitive reverse voltage	V_{RRM}		200	V	
Average rectified forward current	I _{F(AV)}	T _M = 168 °C	1	^	
Non-repetitive peak surge current	I _{FSM}	$T_J = 25$ °C, 10 ms sine pulse	32	^	
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR} , V_{R}	I _R = 100 μA	200	-	-	
Forward voltage	M	I _F = 1 A	-	0.88	0.97	V
Forward voltage	V _F	I _F = 1 A, T _J = 150 °C	-	0.72	0.76	
Reverse leakage current I		V _R = V _R rated	-	-	1	
	IR	$T_J = 150 ^{\circ}\text{C}, V_R = V_R \text{ rated}$	-	-	50	μA
Junction capacitance	C _T	V _R = 200 V	-	5	-	pF



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS
		$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}$	A, $I_{rr} = 0.25 A$	-	15	23	
Reverse recovery time	t _{rr}	T _J = 25 °C		-	9	-	ns
		T _J = 125 °C	$I_F = 1 \text{ A}$ $dI_F/dt = 500 \text{ A/}\mu\text{s}$ $V_R = 200 \text{ V}$	-	13	-	
Pook rocovery current	I _{RRM}	T _J = 25 °C		-	2.1	-	Α
Peak recovery current I _F		T _J = 125 °C		-	3.1	-] ^
Reverse recovery charge Q _{rr}	0	T _J = 25 °C		-	11	-	nC
	Q _{rr}	T _J = 125 °C		-	22	-	IIC

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	175	°C
Thermal resistance, junction to mount	R _{thJM} ⁽¹⁾		-	6.5	8.5	
Thermal resistance, junction to ambient	R _{thJA}	Device mounted on FR4 PCB, 2 oz. standard footprint	-	140	-	°C/W
Weight			-	0.023	-	g
Marking device		Case style DFN3820A		11	1 2	

Note

⁽¹⁾ Thermal resistance junction to mount follows JEDEC® 51-14 transient dual interface test method (TDIM)

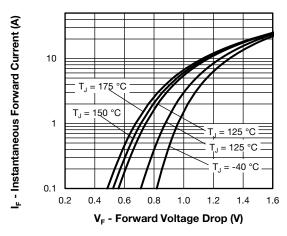


Fig. 1 - Typical Forward Voltage Drop Characteristics

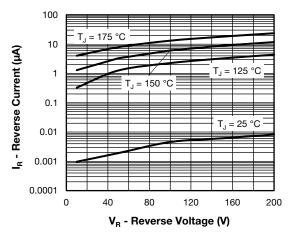


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



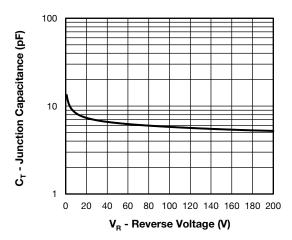


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

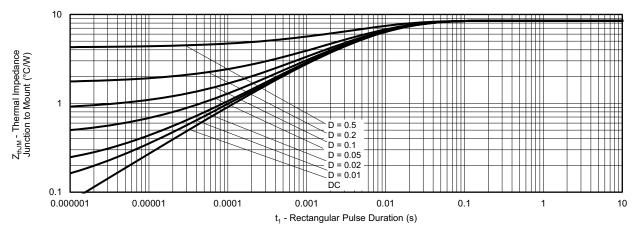


Fig. 4 - Maximum Transient Thermal Impedance, Junction to Mount

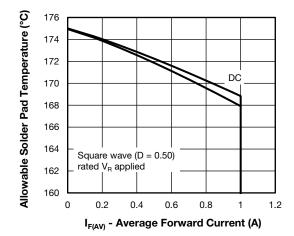


Fig. 5 - Maximum Allowable Mount Temperature vs. Average Forward Current

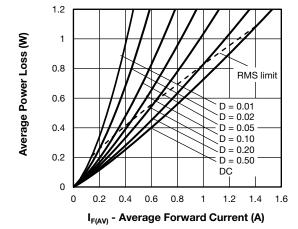
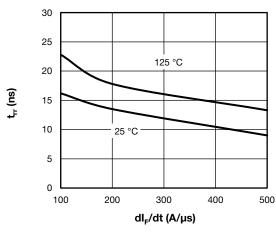


Fig. 6 - Forward Power Loss Characteristics

Note

Formula used: T_M = T_J - (Pd + Pd_{REV}) x R_{thJM}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 5); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = rated V_R







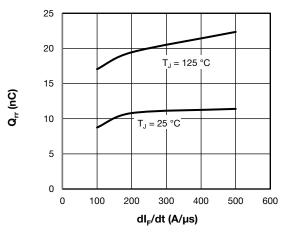


Fig. 8 - Typical Stored Charge vs. dl_F/dt

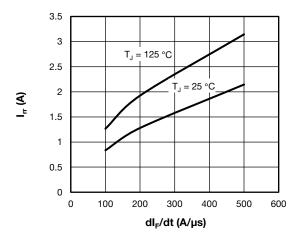
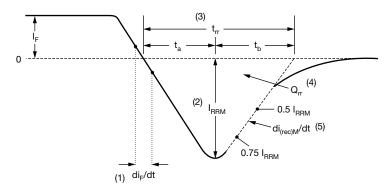


Fig. 9 - I_{rr} vs. dI/dt



- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_F$ to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) \mathbf{Q}_{rr} area under curve defined by \mathbf{t}_{rr} and \mathbf{I}_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

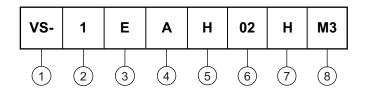
(5) di_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (1 = 1 A)

Circuit configuration:

E = single diode

4 - A = DFN3820A package

5 - Process type,

H = ultrafast recovery

6 - Voltage code (02 = 200 V)

7 - H = AEC-Q101 qualified

8 - M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

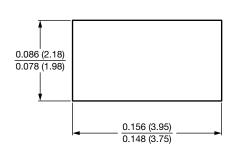
ORDERING INFORMATION (Example)					
PREFERRED P/N	PREFERRED PACKAGE CODE	BASE QUANTITY	PACKAGING DESCRIPTION		
VS-1EAH02HM3/H	Н	3500	7" diameter plastic tape and reel		
VS-1EAH02HM3/I	I	14 000	13" diameter plastic tape and reel		

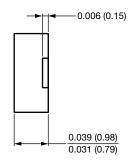
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?97066			
Part marking information	www.vishay.com/doc?97065			
Packaging information	www.vishay.com/doc?98488			
SPICE model	www.vishay.com/doc?97095			

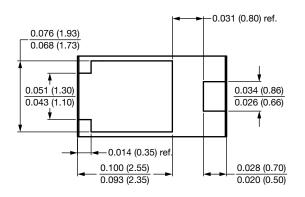


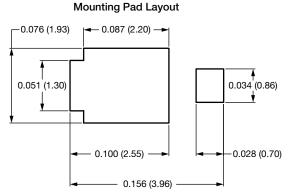
DFN3820A, FRED Pt®

DIMENSIONS in inches (millimeters)











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Vishay

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