

### Vishay Semiconductors

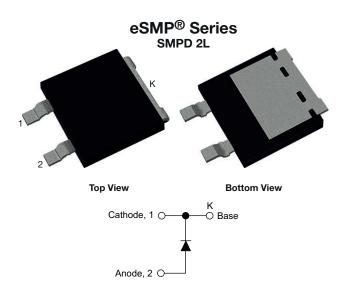
RoHS

COMPLIANT

HALOGEN

FREE

# 650 V Power SiC Gen 3 Merged PIN Schottky Diode, 10 A



#### **LINKS TO ADDITIONAL RESOURCES**



PRIMARY CHARACTERISTICS				
I <sub>F</sub>	10 A			
V <sub>R</sub>	650 V			
V <sub>F</sub> at I <sub>F</sub> at 25 °C, typ.	1.30 V			
T <sub>J</sub> max.	175 °C			
I <sub>R</sub> at V <sub>R</sub> at 175 °C	19 μΑ			
Q <sub>C</sub> (V <sub>R</sub> = 400 V)	29 nC			
Package	SMPD 2L			
Circuit configuration	Single			

#### **FEATURES**

- Creepage and clearance distance 3.6 mm minimum
- Very low profile typical height of 1.7mm
- Majority carrier diode using Schottky technology on SiC wide band gap material
- Improved V<sub>F</sub> and efficiency by thin wafer technology
- Positive V<sub>F</sub> temperature coefficient for easy paralleling
- · Virtually no recovery tail and no switching losses
- · Temperature invariant switching behavior
- 175 °C maximum operating junction temperature
- MPS structure for high ruggedness to forward current surge events
- Meets JESD 201 class 2 whisker test
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **DESCRIPTION / APPLICATIONS**

Wide band gap SiC based 650 V Schottky diode, designed for high performance and ruggedness.

Optimum choice for high speed hard switching and efficient operation over a wide temperature range, it is also recommended for all applications suffering from Silicon ultrafast recovery behavior.

Typical applications include AC/DC PFC and DC/DC ultra high frequency output rectification in FBPS and LLC converters.

### **MECHANICAL DATA**

Case: SMPD 2L

Molding compound meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, RoHS-compliant

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise specified)					
PARAMETER	SYMBOL	SYMBOL TEST CONDITIONS		UNITS	
Peak repetitive reverse voltage	$V_{RRM}$		650	V	
Continuous forward current	I <sub>F</sub>	T <sub>M</sub> = 147 °C (DC)	10	Α	
DC blocking voltage	$V_{DC}$		650	V	
Repetitive peak surge current	I <sub>FRM</sub>	T <sub>M</sub> = 25 °C, f = 50 Hz, square wave, DC = 25 %	49	Α	
Non-repetitive peak forward surge current	I <sub>FSM</sub>	$T_M = 25$ °C, $t_p = 10$ ms, half sine wave	60	А	
		$T_M = 110$ °C, $t_p = 10$ ms, half sine wave	58		
	P <sub>tot</sub> (1)	T <sub>M</sub> = 25 °C	81	W	
Power dissipation	tot \	T <sub>M</sub> = 110 °C	35	VV	
rowei dissipation	P <sub>tot</sub> (2)	T <sub>M</sub> = 25 °C	106	W	
		T <sub>M</sub> = 110 °C	46		
l <sup>2</sup> t value	∫i <sup>2</sup> dt	T <sub>M</sub> = 25 °C	18	A <sup>2</sup> s	
i-t value		T <sub>M</sub> = 110 °C	17	7 ~~5	
Operating junction and storage temperatures	T <sub>J</sub> <sup>(3)</sup> , T <sub>Stg</sub>		-55 to +175	Ŝ	

#### Notes

- (1) Based on maximum Rth
- (2) Based on typical Rth
- $^{(3)}$  The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$



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<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
		I <sub>F</sub> = 10 A	-	1.3	1.5		
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 10 A, T <sub>J</sub> = 150 °C	-	1.46	1.85	V	
		I <sub>F</sub> = 10 A, T <sub>J</sub> = 175 °C	-	1.52	-		
Reverse leakage current	I <sub>R</sub>	$V_R = V_R$ rated	-	0.7	55		
		V <sub>R</sub> = V <sub>R</sub> rated, T <sub>J</sub> = 150 °C	-	11	125	μΑ	
		V <sub>R</sub> = V <sub>R</sub> rated, T <sub>J</sub> = 175 °C	-	19	-		
Total capacitance	С	V <sub>R</sub> = 1 V, f = 1 MHz	-	445	=.	pF	
		V <sub>R</sub> = 400 V, f = 1 MHz	-	43	-	PΓ	
Total capacitive charge	Q <sub>C</sub>	V <sub>R</sub> = 400 V, f = 1 MHz	-	29	-	nC	

THERMAL - MECHANICAL SPECIFICATIONS (T <sub>A</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Thermal resistance, junction-to-mount	$R_{thJM}$		-	1.4	1.8	°C/W
Marking device				3C10	ED07T	

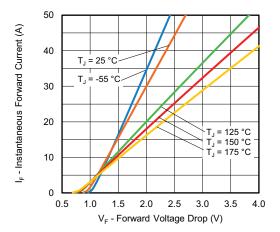


Fig. 1 - Typical Forward Voltage Drop Characteristics

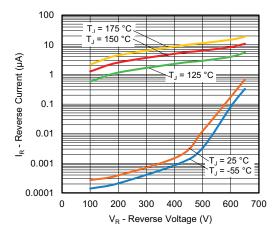


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

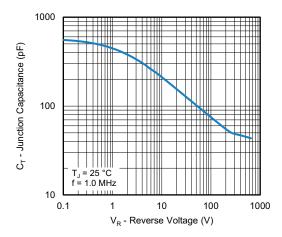


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

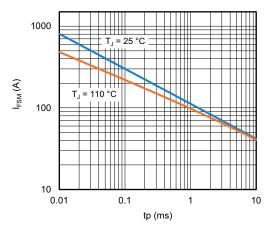


Fig. 4 - Non-Repetitive Peak Forward Surge Current vs. Pulse Duration (Square Wave)



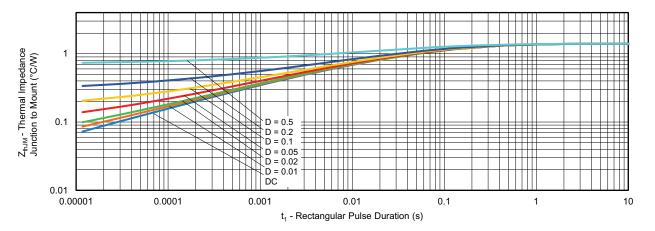


Fig. 5 - Typical Thermal Impedance Z<sub>thJM</sub> Characteristics

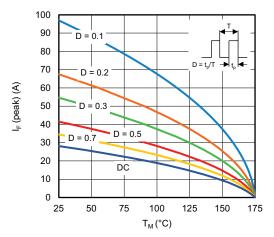


Fig. 6 - Peak Forward Current vs. Maximum Allowable Mount Temperature

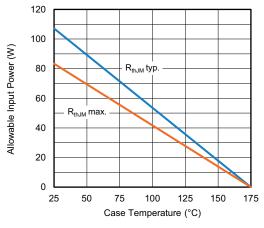


Fig. 7 - Forward Power Loss Characteristics

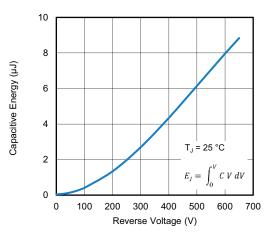


Fig. 8 - Typical Capacitive Energy vs. Reverse Voltage

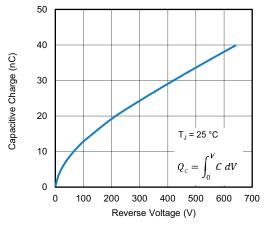


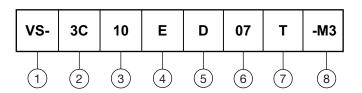
Fig. 9 - Typical Capacitive Charge vs. Reverse Voltage



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### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Vishay Semiconductors product

2 - 3C = SiC diode, Generation 3

Current rating (10 = 10 A)

- E = single diode

5 - Package SMPD 2L

Voltage rating: (07 = 650 V)

7 - T = true 2 pin

8 - Environmental digit:

-M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION						
ORDERING P/N	UNIT WEIGHT	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
VS-3C10ED07T-M3/I	0.52 g	I	2000/reel	13" diameter plastic tape and reel		

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?97059</u>				
Part marking information	www.vishay.com/doc?97105			
Packaging information	www.vishay.com/doc?88869			



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Vishay

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