\* Copyright 2023 © Vishay Intertechnology, Inc. \* \* All rights reserved. \*\*\*\*\*\*\*\*\*\*\*\*\*\* \*May 3, 2023 \*DD Vishay Semicoductor Italiana SPA \*File Name: VS-65APS-EPS12L \*This document is intended as a SPICE modeling guideline and does not \*constitute a commercial product datasheet. Designers should refer to the \*appropriate datasheet of the same number for guaranteed specification \*limits. \*Model fit device at 25°C \*DEVICE=VS-65APS-EPS12L, D \*\*\*\*\*\*\*\*\*\*\*\*\*\* .MODEL VS-65APS-EPS12L D + IS=2.4702E-9 + N=1.4874 + RS=2.2544E-3 + IKF=19.146 + CJO=847.80E-12 + M=.46631 + VJ=.6802 + ISR=61.920E-9 + NR=4.9950 + BV=1.3900E3 + IBV=100.00E-6 \*\*\*\*\*\*\*\*\*\*\*\*\*\* .ENDS Thermal Model Subcircuit .SUBCKT VS-65EPS-APS12L 5 1 CTHERM1 5 4 1.40E-02 CTHERM2 4 3 1.80E-01 CTHERM3 3 2 2.08E+00 CTHERM4 2 1 1.14E+03 RTHERM1 5 4 2.15E-02 3 RTHERM2 4 8.14E-02 3 2 RTHERM3 1.42E-01 RTHERM4 1 1.00E-07

.ENDS VS-65APS-EPS12L

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