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# SOT-227 Silicon Carbide Schottky Barrier Diode, 1200 V, 120 A



SOT-227
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PRIMARY CHARACTERISTICS							
V <sub>R</sub>	1200 V						
V <sub>F</sub> (typical) at 60 A, per diode	1.42 V						
Q <sub>C</sub> (typical), per diode	332.5 nC						
I <sub>F(DC)</sub> per module at T <sub>C</sub> = 136 °C	120 A						
Type	Modules - diode, SiC Schottky						
Package	SOT-227						
Circuit configuration	Two separate diodes, parallel pin-out						

#### **FEATURES**

Virtually no recovery tail and no switching losses



 Majority carrier diode using Schottky technology on SiC wide band gap material ROHS COMPLIANT

- Improved V<sub>F</sub> and efficiency by thin wafer technology
- High speed switching, low switching losses
- · Positive temperature coefficient, for easy paralleling
- · Electrically isolated base plate
- Large creepage distance between terminal
- · Simplified mechanical designs, rapid assembly
- Designed and qualified for industrial level
- UL approved file E78996
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **DESCRIPTION / APPLICATIONS**

Wide band gap SiC based 1200 V Schottky diode, designed for high performance and ruggedness.

Optimum choice for high speed hard switching and efficient operation over a wide temperature range, it is also recommended for all applications suffering from Silicon ultrafast recovery behavior.

Typical applications include AC/DC PFC and DC/DC ultra high frequency output rectification in FBPS and LLC converters

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Cathode to anode voltage	V <sub>R</sub>		1200	V	
Continuous forward current per diode	I <sub>F</sub>	T <sub>C</sub> = 136 °C	60	۸	
Single pulse forward current per diode	I <sub>FSM</sub>	T <sub>J</sub> = 25 °C, 6 ms square pulse	385	Α	
Maximum power dissipation per module	P <sub>D</sub>	T <sub>C</sub> = 136 °C	243	W	
RMS isolation voltage	V <sub>ISOL</sub>	Any terminal to case, t = 1 min	2500	V	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C	

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	$V_{BR}$	I <sub>R</sub> = 100 μA	1200	-	-	
Forward voltage	W	I <sub>F</sub> = 60 A	-	1.42	1.59	V
Forward voltage	$V_{FM}$	I <sub>F</sub> = 60 A, T <sub>J</sub> = 150 °C	-	1.86	-	
		V <sub>R</sub> = 1200 V	-	4.9	180	
Reverse leakage current	I <sub>RM</sub>	T <sub>J</sub> = 125 °C, V <sub>R</sub> = 1200 V	-	15.1	-	μA
		T <sub>J</sub> = 150 °C, V <sub>R</sub> = 1200 V	-	22	-	
Junction capacitance	$C_{T}$	V <sub>R</sub> = 1200 V, f = 1 MHz	-	206	-	pF



<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Total capacitive charge	$Q_{C}$	V <sub>R</sub> = 800 V	-	332.5	-	nC

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Thermal resistance junction to case, per diode	D		-	-	0.31	
Thermal resistance junction to case, per module	R <sub>thJC</sub>		-	-	0.16	°C/W
Thermal resistance case to heatsink, per module	R <sub>thCS</sub>	Flat, greased surface	-	0.05	-	
Weight			-	30	-	g
Manustina tanana		Torque per diode	-	-	1.1 (9.7)	Nm (lbf.in)
Mounting torque		Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)
Case style				SOT	Γ-227	

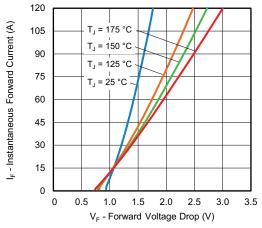


Fig. 1 - Typical Forward Voltage Drop Characteristics

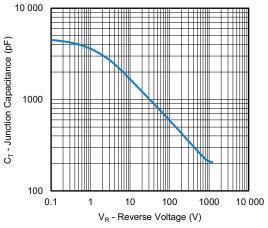


Fig. 3 - Junction Capacitance vs. Reverse Voltage

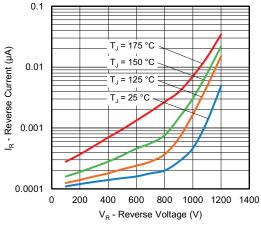


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

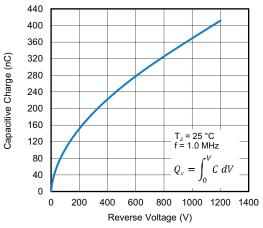


Fig. 4 - Typical Capacitive Charge vs. Reverse Voltage

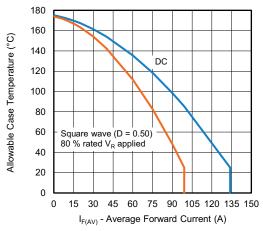


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

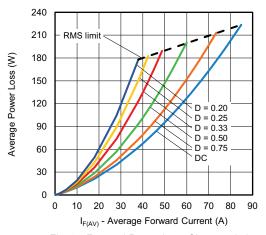


Fig. 6 - Forward Power Loss Characteristics

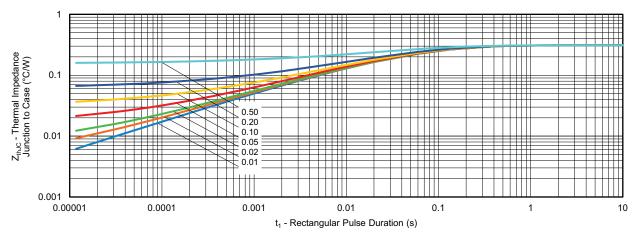
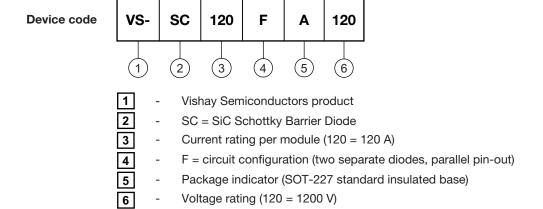


Fig. 7 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

### **ORDERING INFORMATION TABLE**



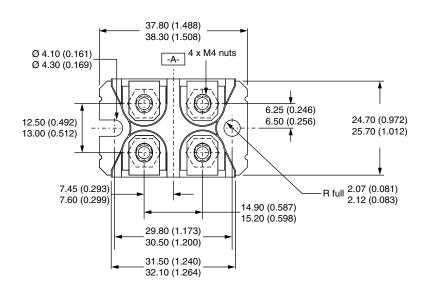


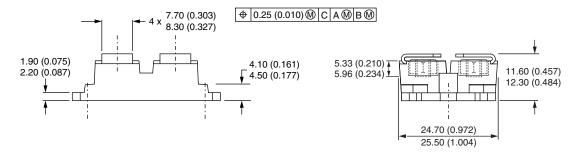
CIRCUIT CONFIGURATION					
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING			
Two separate diodes, parallel pin-out	F	Lead Assignment  4 0 0 3 4 1 0 0 2 1			

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95423				
Packaging information	www.vishay.com/doc?95425				

## SOT-227 Generation 2

## **DIMENSIONS** in millimeters (inches)





### Note

· Controlling dimension: millimeter



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