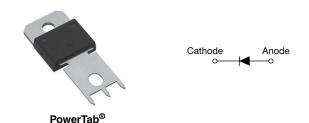
# FRED Pt<sup>®</sup> Ultrafast Soft Recovery Diode, 150 A



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### LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	150 A				
V <sub>R</sub>	600 V				
V <sub>F</sub> at I <sub>F</sub>	1.08 V				
I <sub>FSM</sub>	1200 A				
t <sub>rr</sub> (typ.)	50 ns				
T <sub>J</sub> max.	175 °C				
Snap factor	0.5				
Package	PowerTab <sup>®</sup>				
Circuit configuration	Single				

### FEATURES

- Ultrafast recovery time
- 175 °C max. operating junction temperature
- Screw mounting only
- $\bullet$  Designed and qualified according to  ${\sf JEDEC}^{\circledast}{\sf -}{\sf JESD}$  47
- PowerTab<sup>®</sup> package
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### BENEFITS

- Reduced RFI and EMI
- Higher frequency operation
- Reduced snubbing
- Reduced parts count

#### **DESCRIPTION/APPLICATIONS**

These diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems. The softness of the recovery eliminates the need for a snubber in most applications. These devices are ideally suited for HF welding, power converters and other applications where switching losses are not significant portion of the total losses.

#### MECHANICAL DATA

Case: PowerTab®

Molding compound meets UL 94 V-0 flammability rating **Terminal:** nickel plated, screwable

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS			
Cathode to anode voltage	V <sub>R</sub>		600	V			
Continuous forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 89 °C	150	^			
Single pulse forward current	I <sub>FSM</sub>	T <sub>C</sub> = 25 °C	1200				
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C			

<b>ELECTRICAL SPECIFICATIONS</b> ( $T_J = 25 \text{ °C}$ unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 200 μA		-	-		
		I <sub>F</sub> = 150 A	-	1.27	1.63	V	
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 150 A, T <sub>J</sub> = 125 °C	-	1.15	1.43		
		I <sub>F</sub> = 150 A, T <sub>J</sub> = 175 °C	-	1.08	1.32		
Reverse leakage current I <sub>R</sub>		V <sub>R</sub> = V <sub>R</sub> rated	-	-	8	μA	
		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	-	0.5	mA	
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 600 V - 70 -		-	pF		
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body - 3.5 - n		nH			

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COMPLIANT

HALOGEN

FREE



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## Vishay Semiconductors

<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J$ = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CON	DITIONS	MIN.	TYP.	MAX.	UNITS	
		$I_F = 1.0 \text{ A}, \ dI_F/dt = 100 \text{ A}$	-	50	-			
Reverse recovery time	+	$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 200 \text{ J}$	-	40	-	20		
Reverse recovery time t <sub>rr</sub>	۲r	T <sub>J</sub> = 25 °C		-	100	-	ns	
		T <sub>J</sub> = 125 °C		-	210	-		
Dock recovery ourrent	1	T <sub>J</sub> = 25 °C	I <sub>F</sub> = 50 A V <sub>B</sub> = 200 V	-	10.5	-	А	
Peak recovery current I <sub>RRM</sub>	IRRM	T <sub>J</sub> = 125 °C	dl <sub>F</sub> /dt = 200 A/μs	-	22	-	A	
Reverse recovery charge Q <sub>rr</sub>	0	T <sub>J</sub> = 25 °C		-	550	-	nC	
	Qrr	T <sub>J</sub> = 125 °C		-	2350	-	ΠC	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Thermal resistance, junction to case	R <sub>thJC</sub>		-	-	0.35	K/W
Typical thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth, and greased	-	0.2	-	r∖/vv
Weight			-	-	5.02	g
Mounting torque			1.2 (10)	-	2.4 (20)	N · m (lbf · in)
Marking device		Case style PowerTab®	EBU15006			

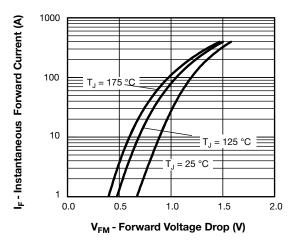


Fig. 1 - Maximum Forward Voltage Drop Characteristics

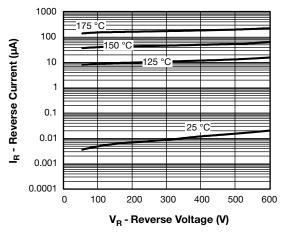


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



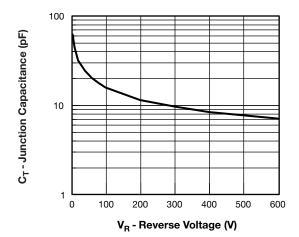


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

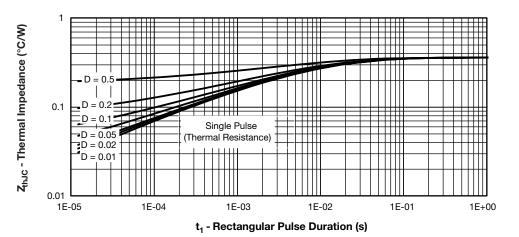
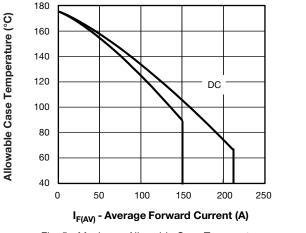


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics



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Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

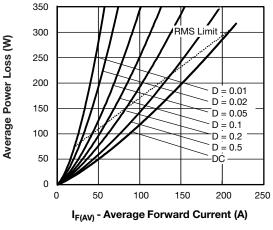


Fig. 6 - Forward Power Loss Characteristics

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# VS-EBU15006-N4

### **Vishay Semiconductors**

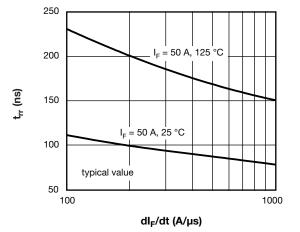


Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

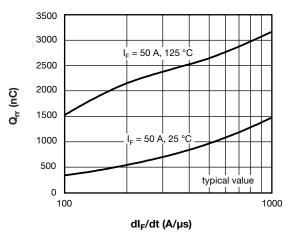


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

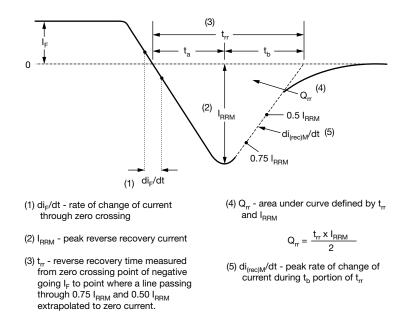


Fig. 9 - Reverse Recovery Waveform and Definitions



### **ORDERING INFORMATION TABLE**

Device code	VS-	Е	В	U	150	06	-N4
	1	2	3	4	5	6	7
	1 .	- Visł	nay Sem	niconduc	ctors pro	oduct	
	2 -	· Sing	gle diod	е			
	3 -	· Pov	verTab®	)			
	4 -	Ultr	Ultrafast recovery				
	5	- Cur	Current rating (150 = 150 A)				
	6	- Voli	tage rati	ng (06 =	= 600 V)		
	7			ntal digit en-free,		complia	nt, and t

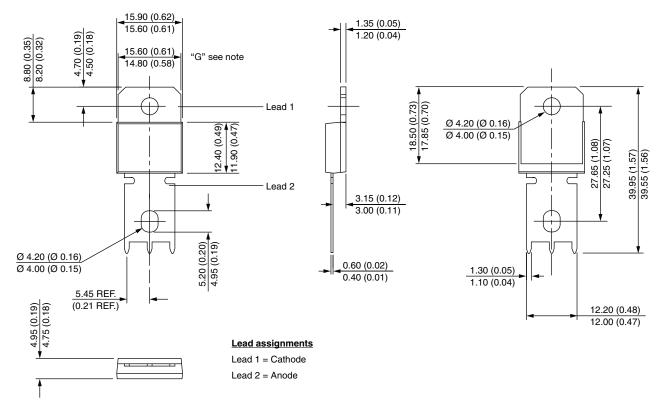
ORDERING INFORMATION (Example)					
PREFERRED P/N BASE QUANTITY PACKAGING DESCRIPTION					
VS-EBU15006-N4	25/tube	Antistatic plastic tube			

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95240				
Part marking information	www.vishay.com/doc?95467				
Application note	www.vishay.com/doc?95179				
SPICE model	www.vishay.com/doc?97099				



**PowerTab**<sup>®</sup>

### **DIMENSIONS** in millimeters (inches)



Note:

Outline conform to JEDEC® TO-275, except for dimension "G" only



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