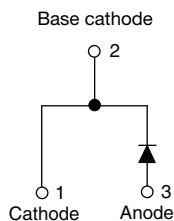


Hyperfast Rectifier, 60 A FRED Pt® Gen 5



FEATURES

- Hyperfast - very low Q_{rr}
- Designed to optimize hard switching losses
- Optimized for very high speed operations
- 175 °C maximum operating junction temperature
- Polyimide passivation
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



RoHS
COMPLIANT
HALOGEN
FREE

LINKS TO ADDITIONAL RESOURCES



3D Models



Application Notes

PRIMARY CHARACTERISTICS

$I_{F(AV)}$	60 A
V_R	1200 V
V_F at I_F at 150 °C	2.40 V
t_{rr}	30 ns
T_J max.	175 °C
Package	TO-247AD 2L
Circuit configuration	Single

DESCRIPTION / APPLICATIONS

Featuring a unique combination of low conduction and switching losses, this rectifier is the right choice for high frequency converters, both soft switched / resonant. Specifically designed to improve efficiency of PFC and output rectification stages of EV / HEV battery charging stations, booster stage of solar inverters and UPS applications, these devices are perfectly matched to operate with MOSFETs or high speed IGBTs.

MECHANICAL DATA

Case: TO-247AD 2L

Molding compound meets UL 94 V-0 flammability rating

Terminals: matte tin plated leads, solderable per J-STD-002

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Repetitive peak reverse voltage	V_{RRM}		1200	V
Average rectified forward current	$I_{F(AV)}$	$T_C = 89\text{ °C}$, $D = 0.50$	60	A
Non-repetitive peak surge current	I_{FSM}	$T_C = 25\text{ °C}$, $t_p = 10\text{ ms}$, sine wave	250	
Repetitive peak forward current	I_{FRM}	$T_C = 89\text{ °C}$, $D = 0.50$, $f = 20\text{ kHz}$	120	
Operating junction and storage temperature	T_J , T_{Stg}		-55 to +175	°C

ELECTRICAL SPECIFICATIONS ($T_J = 25\text{ °C}$ unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR} , V_R	$I_R = 100\text{ }\mu\text{A}$	1200	-	-	V
Forward voltage	V_F	$I_F = 60\text{ A}$	-	3.30	-	
		$I_F = 60\text{ A}$, $T_J = 150\text{ °C}$	-	2.40	2.80	
Reverse leakage current	I_R	$V_R = V_R$ rated	-	-	50	μA
		$T_J = 125\text{ °C}$, $V_R = V_R$ rated	-	-	500	
Junction capacitance	C_T	$V_R = 200\text{ V}$	-	32	-	pF
Series inductance	L_S	Measured to lead 5 mm from package body	-	8	-	nH



DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25\text{ }^{\circ}\text{C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Reverse recovery time	t_{rr}	$I_F = 1.0\text{ A}$, $di_F/dt = 100\text{ A}/\mu\text{s}$, $V_R = 30\text{ V}$	-	30	-	ns
		$T_J = 25\text{ }^{\circ}\text{C}$	-	82	-	
		$T_J = 125\text{ }^{\circ}\text{C}$	-	130	-	
Peak recovery current	I_{RRM}	$T_J = 25\text{ }^{\circ}\text{C}$	-	14	-	A
		$T_J = 125\text{ }^{\circ}\text{C}$	-	27	-	
Reverse recovery charge	Q_{rr}	$T_J = 25\text{ }^{\circ}\text{C}$	-	615	-	nC
		$T_J = 125\text{ }^{\circ}\text{C}$	-	2240	-	
Reverse recovery time	t_{rr}	$T_J = 25\text{ }^{\circ}\text{C}$	-	74	-	ns
		$T_J = 125\text{ }^{\circ}\text{C}$	-	114	-	
Peak recovery current	I_{RRM}	$T_J = 25\text{ }^{\circ}\text{C}$	-	24	-	A
		$T_J = 125\text{ }^{\circ}\text{C}$	-	43	-	
Reverse recovery charge	Q_{rr}	$T_J = 25\text{ }^{\circ}\text{C}$	-	1110	-	nC
		$T_J = 125\text{ }^{\circ}\text{C}$	-	3345	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Thermal resistance, junction-to-case	R_{thJC}		-	-	0.4	$^{\circ}\text{C}/\text{W}$
Weight			-	5.5	-	g
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Maximum junction and storage temperature range	T_J , T_{Stg}		-55	-	175	$^{\circ}\text{C}$
Marking device		Case style: TO-247AD 2L	E5PW6012L			

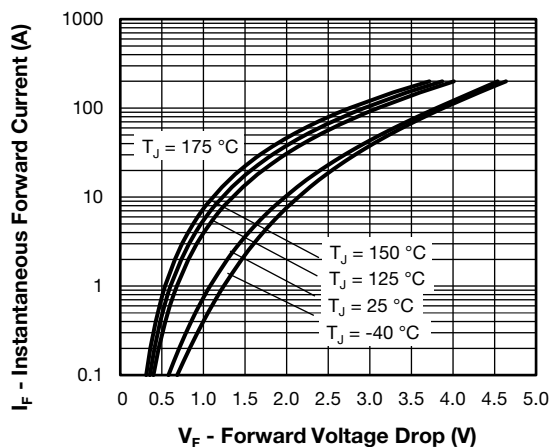


Fig. 1 - Typical Forward Voltage Drop Characteristics

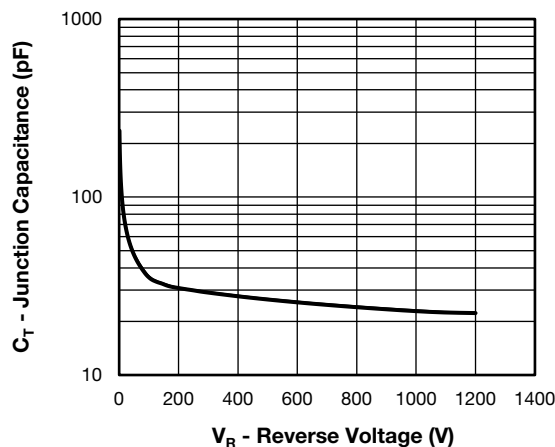


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

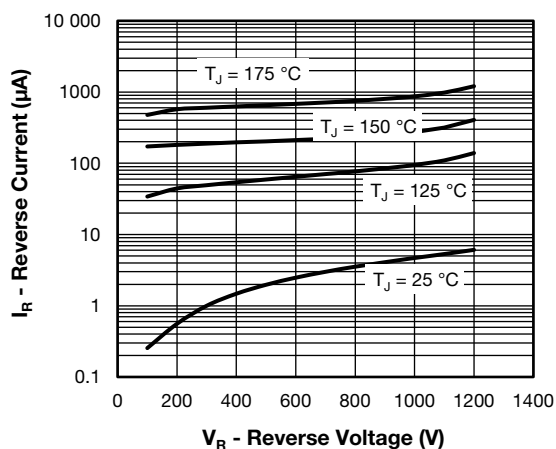


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

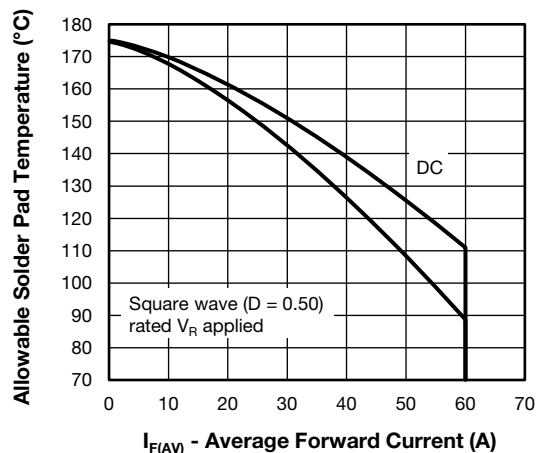


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

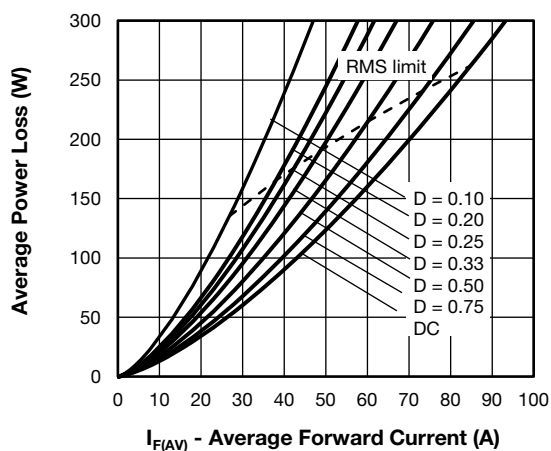
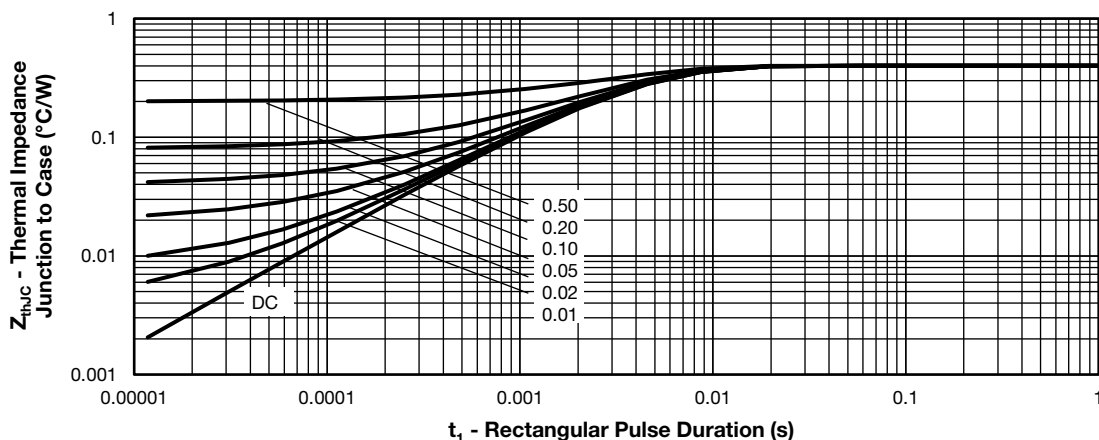
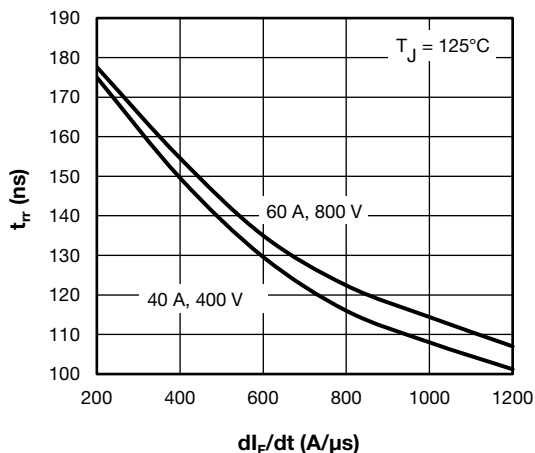
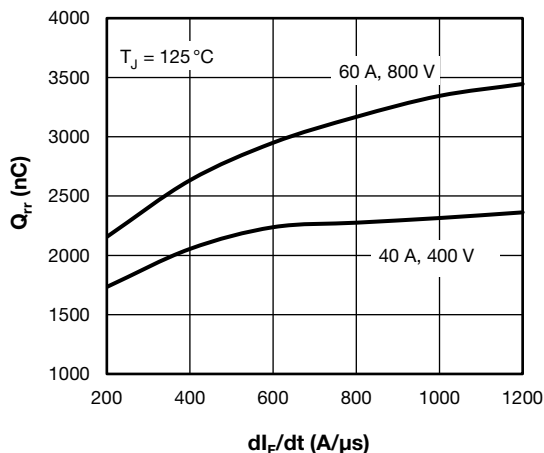
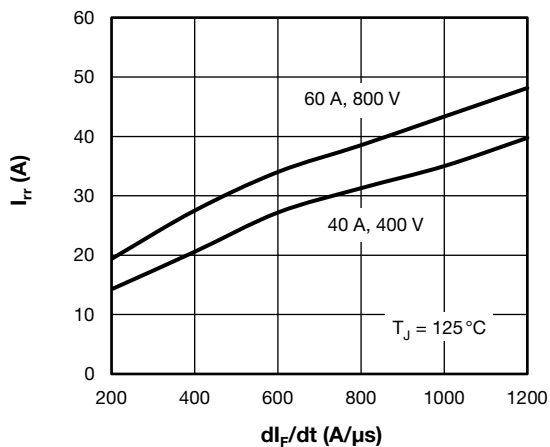


Fig. 5 - Forward Power Loss Characteristics


Fig. 6 - Thermal Impedance Z_{thJC} - Characteristics

Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

Fig. 8 - Typical Stored Charge vs. dI_F/dt

Fig. 9 - Typical Recovery Current vs. dI_F/dt

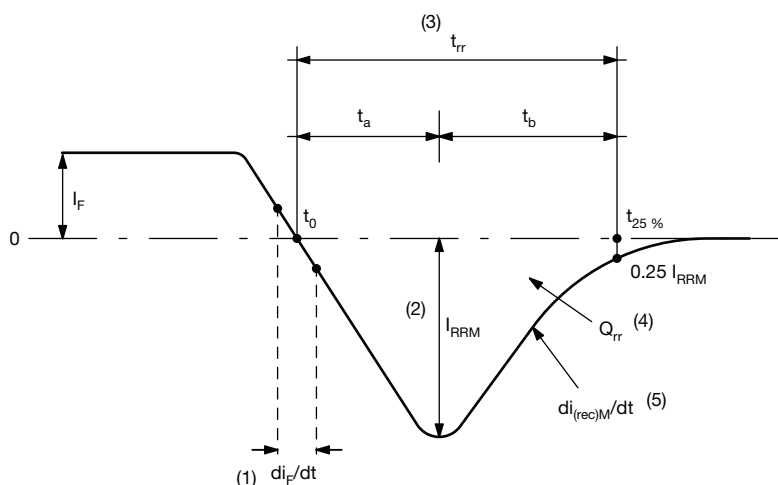


Fig. 10 - Reverse Recovery Waveform and Definitions

Notes

- (1) di_F/dt - rate of change of current through zero crossing
(2) I_{RRM} - peak reverse recovery current
(3) t_{rr} - reverse recovery time measured from t_0 , crossing point of negative going I_F , to point $t_{25\%}$, $0.25 I_{RRM}$
(4) Q_{rr} - area under curve defined by t_0 and $t_{25\%}$

$$Q_{rr} = \int_{t_0}^{t_{25\%}} I(t) dt$$

- (5) $di_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

ORDERING INFORMATION TABLE

Device code	VS-	E	5	P	W	60	12	L	-N3
	1	2	3	4	5	6	7	8	9

- 1 - Vishay Semiconductors product
2 - E = single diode
3 - 5 = Fred generation 5
4 - Package:
P = TO-247 package
5 - W = warp hyperfast recovery
6 - Current rating (60 = 60 A)
7 - Voltage rating (12 = 1200 V)
8 - Package: L = long lead (TO-247AD)
9 - Environmental digit:
-N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)			
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION
VS-E5PW6012L-N3	25	500	Antistatic plastic tube



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