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- *DD Vishay Semiconductor Italiana SPA
- *File Name: VS-ETL3006S.txt
- *This document is intended as a SPICE modeling guideline and does not
- *constitute a commercial product datasheet. Designers should refer to the
- *appropriate datasheet of the same number for guaranteed specification *limits.
- *Model fit device @25°C-150°C
- *DEVICE=VS-ETL3006S, Diode

.MODEL VS-ETL3006S D

- + IS= 7.053e-07
- + N= 1.971
- + RS= 3.215e-03
- + IKF= 31.52
- + CJO= 3.746e-10
- + M = 0.426
- + VJ= 0.591
- + ISR= 1.001e-20
- + NR= 4.995
- + BV=690.0
- + IBV= 1.75e-04
- + TT= 1.121e-07
- *Add subsequent parameters for temperature behavior:
- + XTI=3.5
- + TIKF=0.024
- + TRS1=0.0027
- *use follow TT and Vp with LTSpice at 25°C
- *+ TT=225e-9
- *+ Vp=0.135

THERMAL MODEL SUBCIRCUIT

- *RC Foster model
- .SUBCKT zth-ETL3006S Tj Tc

RTHERM1	Tj	1	6.899e-02
RTHERM2	1	2	3.376e-01
RTHERM3	2	3	1.238e-02
RTHERM4	3	Tc	5.915e-01
CTHERM1	Tj	1	1.667e-03
CTHERM2	1	2	1.220e-02
CTHERM3	2	3	1.447e-04
CTHERM4	3	Tc	9.360e-02

.ENDS zth-ETL3006S



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