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* DD Vishay Semiconductor Italiana SPA

* File Name: VS-8ESH06.txt

* This document is intended as a SPICE modeling guideline and does not
* constitute a commercial product datasheet. Designers should refer to the
* appropriate datasheet of the same number for guaranteed specification
* limits.

* Model fit device @25°C-150°C

* DEVICE=VS-8ESH06, Diode

.MODEL VS-8ESH06 D

+ IS= 2.549e-04

+ N= 4.672

+ RS= 7.164e-03

+ IKF= 7.67

+ CJO= 5.865e-11

+ M= 0.3

+ VJ= 0.716

+ ISR= 1.001e-20

+ NR= 4.995

+ BV=690.0

+ IBV= 5.00e-04

+ TT= 4.078e-08

* Add subsequent parameters for temperature behavior:

+ XTI=24

+ TIKF=0.056

+ TRS1=0.0045

* use follow TT and Vp with LTSpice at 25°C

*+ TT=38.5e-9

*+ Vp=0.45

* DEVICE=VS-8ESH06, Diode

* THERMAL MODEL SUBCIRCUIT

* RC Foster model

.SUBCKT zth-8ESH06_zth Tj Tc

R THERM1	Tj	1	1.199e+00
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R THERM2	1	2	1.265e-02
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R THERM3	2	3	1.141e+00
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R THERM4	3	Tc	4.371e-02
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C THERM1	Tj	1	3.147e-02
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C THERM2	1	2	9.207e-05
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CTHERM3	2	3	3.333e-03
CTHERM4	3	Tc	6.575e-04

.ENDS zth-8ESH06_zth



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