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- \*January 13, 2025
- \*DD Vishay Semiconductor Italiana SPA
- \*File Name: VS-10ETS08-12 zth.txt
- \*This document is intended as a SPICE modeling guideline and does not
- \*constitute a commercial product datasheet. Designers should refer to the
- \*appropriate datasheet of the same number for guaranteed specification
- \*limits.
- \*DEVICE=VS-10ETS08-12, Diode
- \* Technology: DIODE
- \* Device: DIODE
- \* Description:
- \* Type: Typical (nom)
- \* Model established: 07.01.2025
- \* Wafer:
- \* Remarks:
- \* Revision: 07.01.2025
- \* Simulation Model:
- \* Simulator: SpiceMod /LTSPICE

.MODEL VS-10ETS08-12 D

- + IS=1.0000E-9
- + N=1.5154
- + RS=7.9961E-3
- + IKF=.95308
- + CJO=136.56E-12
- + M=.42354
- + VJ=.59991

- + ISR=44.319E-9
- + NR=4.9950
- + BV=1.3800E3
- + IBV=100.00E-6

\*\*\*\*\*\*\*\*\*\*\*\*\*\*

- \*THERMAL MODEL SUBCIRCUIT
- \*RC Foster model

## .SUBCKT zth-10ETS08-12 Tj Tc

RTHERM1	Tj	1	6.743e-01
RTHERM2	1	2	3.786e-02
RTHERM3	2	3	4.289e-01
RTHERM4	3	Tc	1.059e-02
CTHERM1	Tj	1	6.578e-02
CTHERM2	1	2	1.647e-03
CTHERM3	2	3	6.830e-03
CTHERM4	3	Tc	1.539e-04

.ENDS zth-10ETS08-12

<sup>\*</sup>DEVICE=VS-10ETS08-12, Diode



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