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\*DD Vishay Semiconductor Italiana SPA

\*File Name: VS-10ETS08-12FP zth.txt

\*This document is intended as a SPICE modeling guideline and does not

\*constitute a commercial product datasheet. Designers should refer to the

\*appropriate datasheet of the same number for guaranteed specification

\*limits.

\*DEVICE=VS-10ETS08-12FP, Diode

\* Technology: DIODE

\* Device: DIODE

\* Description:

\* Type: Typical (nom)

\* Model established: 07.01.2025

\* Wafer:

\* Remarks:

\* Revision: 07.01.2025

\* Simulation Model:

\* Simulator: SpiceMod /LTSPICE

.MODEL VS-10ETS08-12FP D

+ IS=1.0000E-9

+ N=1.5154

+ RS=7.9961E-3

+ IKF=.95308

+ CJO=136.56E-12

+ M=.42354

+ VJ=.59991

+ ISR=44.319E-9  
+ NR=4.9950  
+ BV=1.3800E3  
+ IBV=100.00E-6  
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\*DEVICE=VS-10ETS08-12FP, Diode

\*THERMAL MODEL SUBCIRCUIT

\*RC Foster model

.SUBCKT zth-10ETS08-12FP Tj Tc

R THERM1	Tj	1	5.146e-02
R THERM2	1	2	6.099e-01
R THERM3	2	3	1.207e-02
R THERM4	3	4	9.616e-01
R THERM5	4	Tc	4.986e-01

C THERM1	Tj	1	1.578e-03
C THERM2	1	2	1.563e-01
C THERM3	2	3	1.414e-04
C THERM4	3	4	2.510e+00
C THERM5	4	Tc	7.482e-03

.ENDS zth-10ETS08-12FP



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