Vishay Semiconductors

Hyperfast Rectifier, 30 A FRED Pt[®] G5



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LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I _{F(AV)}	30 A			
V _R	1200 V			
V _F at I _F at 150 °C	2.35 V			
t _{rr}	25 ns			
T _J max.	175 °C			
Package	TO-247AD 2L			
Circuit configuration	Single			

FEATURES

- Hyperfast very low Qrr
- · Designed to optimize hard switching losses
- Optimized for very high speed operations
- 175 °C maximum operating junction temperature
- Polyimide passivation



 Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

Featuring a unique combination of low conduction and switching losses, this rectifier is the right choice for high frequency converters, both soft switched / resonant. Specifically designed to improve efficiency of PFC and output rectification stages of EV / HEV battery charging stations, booster stage of solar inverters and UPS applications, these devices are perfectly matched to operate with MOSFETs or high speed IGBTs.

MECHANICAL DATA

Case: TO-247AD 2L

Molding compound meets UL 94 V-0 flammability rating

Terminals: matte tin plated leads, solderable per J-STD-002

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Repetitive peak reverse voltage	V _{RRM}		1200	V
Average rectified forward current	I _{F(AV)}	T _C = 95 °C, D = 0.50	30	
Non-repetitive peak surge current	I _{FSM}	T_{C} = 25 °C, t_{p} = 10 ms, sine wave	120	А
Repetitive peak forward current	I _{FRM}	T _C = 95 °C, D = 0.50, f = 20 kHz	60	
Operating junction and storage temperature	T _J , T _{Stg}		-55 to +175	°C

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V_{BR}, V_{R}	I _R = 100 μA	1200	-	-		
Forward voltage	V _F	I _F = 30 A	-	3.30	-	V	
		I _F = 30 A, T _J = 150 °C	-	2.35	2.80		
Deverse le cliege comment		$V_{R} = V_{R}$ rated	-	-	50		
Reverse leakage current	IR	$T_J = 125 \text{ °C}, V_R = V_R \text{ rated}$	-	-	500	μA	
Junction capacitance	CT	V _R = 200 V	-	17	-	pF	
Series inductance	L _S	Measured to lead 5 mm from package body	-	8	-	nH	

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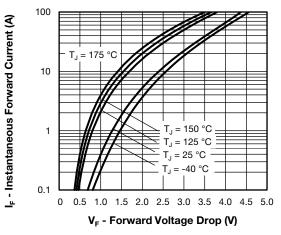


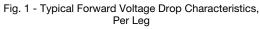
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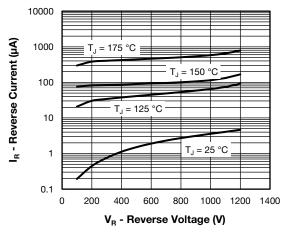
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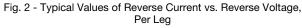
DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS		TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, \text{ d}_F/\text{d}t = 100$) A/µs, V _R = 30 V	-	25	-	
Reverse recovery time	t _{rr}	T _J = 25 °C		-	56	-	ns
		T _J = 125 °C	I _F = 20 A	-	108	-	
Pool rocovery ourrent		T _J = 25 °C	$d_{\rm IF} = 20 \text{ A}$ $d_{\rm IF}/dt = 600 \text{ A/}\mu\text{s}$ $V_{\rm R} = 400 \text{ V}$ $I_{\rm RR} = 25 \text{ °C}$	-	11	-	А
Peak recovery current I _{RRM}	IRRM	T _J = 125 °C		-	19.5	-	A
Reverse recovery charge	0	T _J = 25 °C		-	310	-	nC
Reverse recovery charge	Q _{rr}	T _J = 125 °C		-	1200	-	
Powerse recovery time	t _{rr}	T _J = 25 °C	I _F = 30 A dI _F /dt = 1000 A/μs V _R = 800 V I _{RR} = 25 °C	-	60	-	ns
Reverse recovery time		T _J = 125 °C		-	95	-	
Peak recovery current I _{RRM}		T _J = 25 °C		-	20	-	Α
	IRRM	T _J = 125 °C		-	32	-	
	0	T _J = 25 °C		-	640	-	
Reverse recovery charge	Q _{rr}	T _J = 125 °C		-	1900	-	nC

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Thermal resistance, junction-to-case	R _{thJC}		-	-	0.8	°C/W
Weight			-	5.5	-	g
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	175	°C
Marking device		Case style: TO-247AD 2L		E5PW	3012L	









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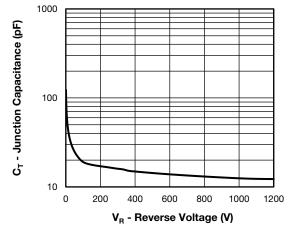


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage, Per Leg

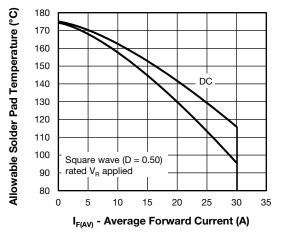


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current, Per Leg

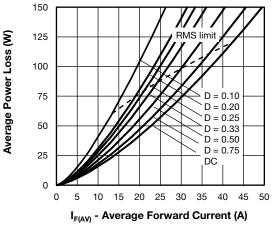


Fig. 5 - Forward Power Loss Characteristics, Per Leg

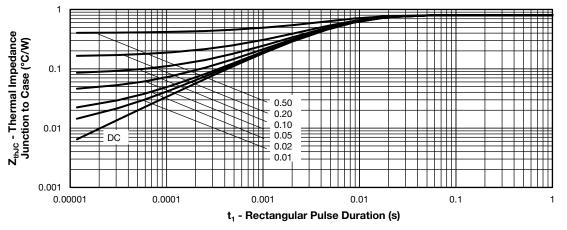


Fig. 6 - Transient Thermal Impedance, Junction to Case, Per Leg

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VS-E5PW3012L-N3

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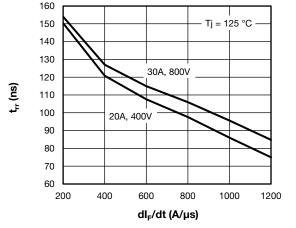


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt, Per Leg

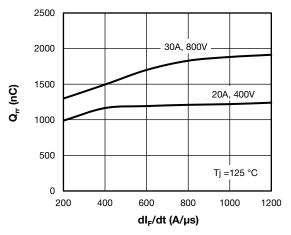


Fig. 8 - Typical Reverse Recovery Charge vs. dl_F/dt, Per Leg

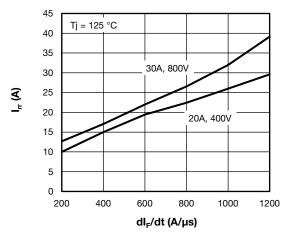


Fig. 9 - Typical Reverse Recovery Current vs. dl_F/dt, Per Leg





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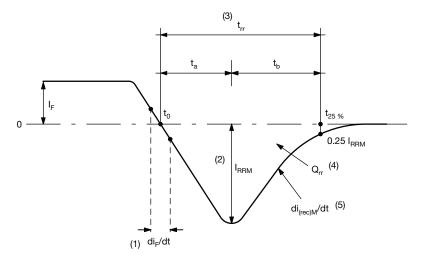


Fig. 10 - Reverse Recovery Waveform and Definitions

Notes

- (1) di_F/dt rate of change of current through zero crossing
- ⁽²⁾ I_{RRM} peak reverse recovery current
- ⁽³⁾ t_{rr} reverse recovery time measured from t₀, crossing point of negative going I_F, to point t_{25 %}, 0.25 I_{RRM}
- $^{(4)}~Q_{rr}$ area under curve defined by t_0 and $t_{25~\%}$

$$Q_{rr} = \int_{t_0}^{t_{25\,\%}} I(t)dt$$

 $^{(5)}$ di_(rec)M/dt - peak rate of change of current during t_b portion of t_{rr}

ORDERING INFORMATION TABLE

W **Device code** VS-Ε 5 Ρ 30 12 -N3 L 2 3 (5) 6 (7)8 4 9 1 Vishay Semiconductors product 2 E = single diode 3 5 = Fred generation 5 4 5 6 7 Package: P = TO-247 package W = warp hyperfast recovery Current rating (30 = 30 A)Voltage rating (12 = 1200 V) 8 Package: L = long lead (TO-247AD) Environmental digit: 9 -N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER TUBE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION					
VS-E5PW3012L-N3	25	500	Antistatic plastic tube			

LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95536				
Part marking information	www.vishay.com/doc?95648			
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