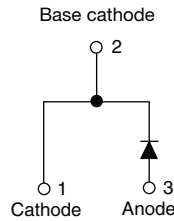
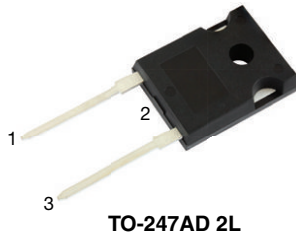


# 650 V Gen 4 Power Silicon Carbide Schottky Diode, 60 A



## FEATURES

- Positive  $V_F$  temperature coefficient for easy paralleling
- Virtually no recovery tail and no switching losses
- Temperature invariant switching behavior
- 175 °C maximum operating junction temperature
- Meets JESD 201 class 1A whisker test
- Solder bath temperature 275 °C maximum, 10 s per JESD 22-B106
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**

## LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS	
$I_F$	60 A
$V_R$	650 V
$V_F$ at $I_F$ at 25 °C, typ.	1.39 V
$T_J$ max.	175 °C
$I_R$ at $V_R$ at 175 °C	300 $\mu$ A
$Q_C$ ( $V_R = 400$ V)	155 nC
Package	TO-247AD 2L
Circuit configuration	Single

## DESCRIPTION / APPLICATIONS

Wide band gap SiC based 650 V Schottky diode, designed for high performance and ruggedness.

Optimized for extreme high speed hard switching across a wide temperature range. This SiC diode is ideal for applications with high  $dI/dt$  such as high efficiency PFC and ultra-high frequency output rectifiers in AC/DC and DC/DC converters.

## MECHANICAL DATA

**Case:** TO-247AD 2L

Molding compound meets UL 94 V-0 flammability rating  
Base P/N -M3 - halogen-free, RoHS-compliant

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

**Mounting torque:** 10 in-lbs maximum

MAXIMUM RATINGS ( $T_A = 25$ °C unless otherwise specified)				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	$V_{RRM}$		650	V
Continuous forward current	$I_F^{(1)}$	$T_C = 146$ °C (DC)	60	A
	$I_F^{(2)}$	$T_C = 135$ °C (DC)	60	A
DC blocking voltage	$V_{DC}$		650	V
Repetitive peak forward current	$I_{FRM}$	$T_C = 25$ °C, $f = 50$ Hz, square wave, DC = 25 %	243	A
Non-repetitive peak forward surge current	$I_{FSM}$	$T_C = 25$ °C, $t_p = 10$ ms, half sine wave	360	A
		$T_C = 110$ °C, $t_p = 10$ ms, half sine wave	320	
Power dissipation	$P_{tot}^{(1)}$	$T_C = 25$ °C	517	W
		$T_C = 110$ °C	224	
	$P_{tot}^{(2)}$	$T_C = 25$ °C	375	W
		$T_C = 110$ °C	162	
$I^2t$ value	$\int i^2 dt$	$T_C = 25$ °C	648	A <sup>2</sup> s
		$T_C = 110$ °C	512	
Operating junction and storage temperatures	$T_J^{(3)}, T_{Stg}$		-55 to +175	°C

## Notes

(1) Based on typical  $R_{th}$

(2) Based on maximum  $R_{th}$

(3) The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$



ELECTRICAL SPECIFICATIONS (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 60 A	-	1.39	1.55	V
		I <sub>F</sub> = 60 A, T <sub>J</sub> = 150 °C	-	1.58	1.80	
		I <sub>F</sub> = 60 A, T <sub>J</sub> = 175 °C	-	1.65	-	
Reverse leakage current	I <sub>R</sub>	V <sub>R</sub> = V <sub>R</sub> rated	-	14	400	μA
		V <sub>R</sub> = V <sub>R</sub> rated, T <sub>J</sub> = 150 °C	-	137	800	
		V <sub>R</sub> = V <sub>R</sub> rated, T <sub>J</sub> = 175 °C	-	300	-	
Total capacitance	C	V <sub>R</sub> = 1 V, f = 1 MHz	-	2630	-	pF
		V <sub>R</sub> = 400 V, f = 1 MHz	-	220	-	
Total capacitive charge	Q <sub>C</sub>	V <sub>R</sub> = 400 V, f = 1 MHz	-	155	-	nC

THERMAL AND MECHANICAL SPECIFICATIONS (T <sub>A</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Thermal resistance, junction-to-case	R <sub>thJC</sub>		-	0.29	0.4	°C/W
Marking device			4C60EP07L			

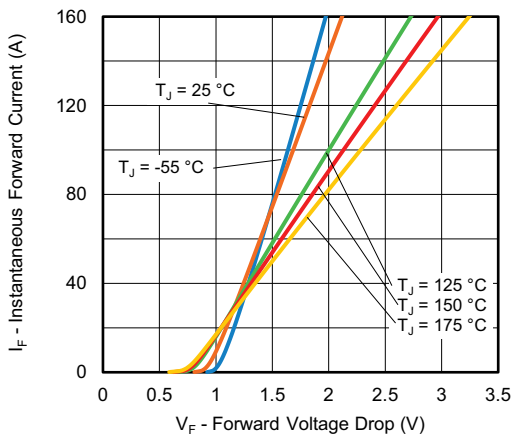


Fig. 1 - Typical Forward Voltage Drop Characteristics

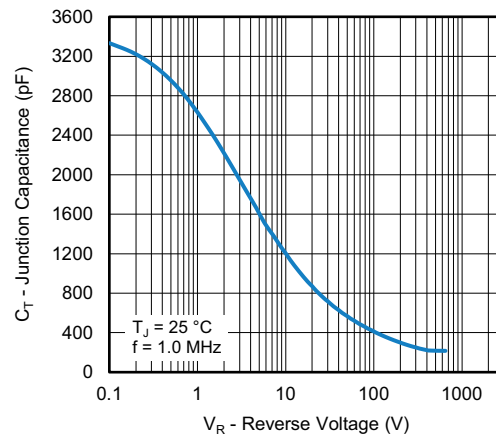


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

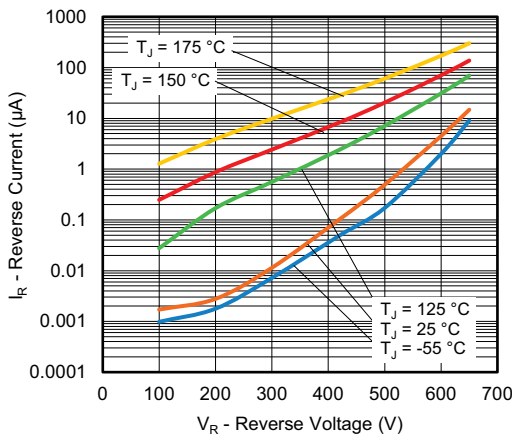


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

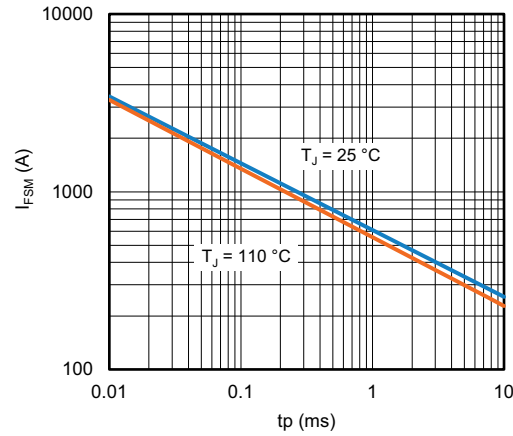


Fig. 4 - Non-Repetitive Peak Forward Surge Current vs. Pulse Duration (Square Wave)

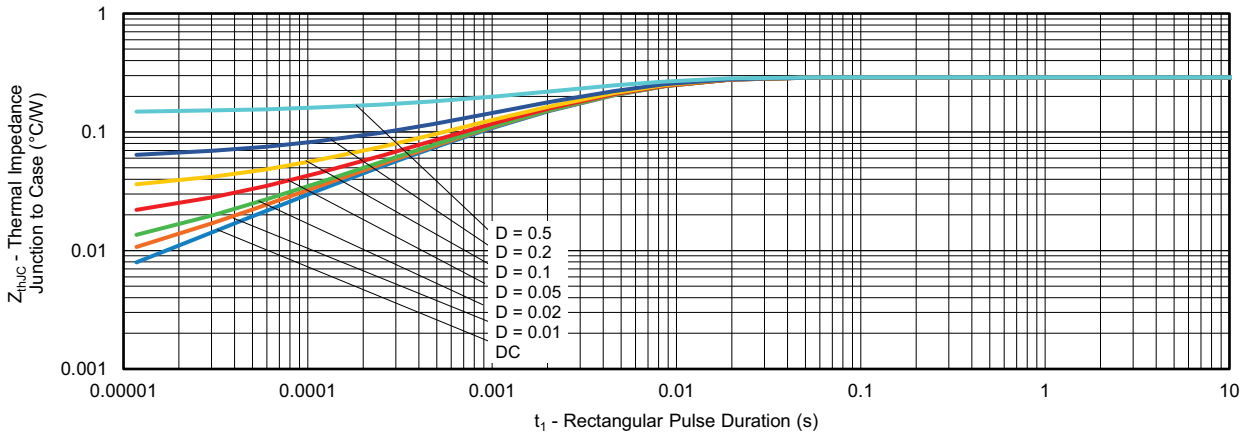


Fig. 5 - Typical Thermal Impedance  $Z_{thJC}$  - Characteristics

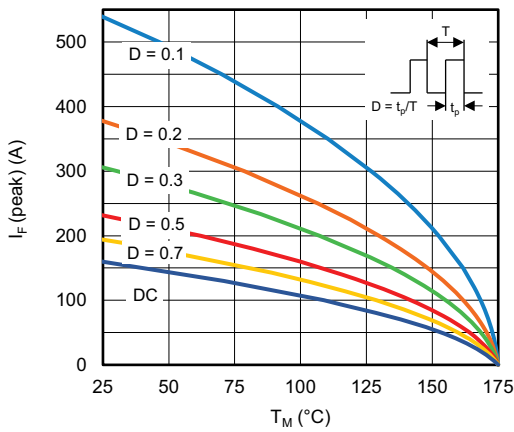


Fig. 6 - Peak Forward Current vs. Maximum Allowable Case Temperature

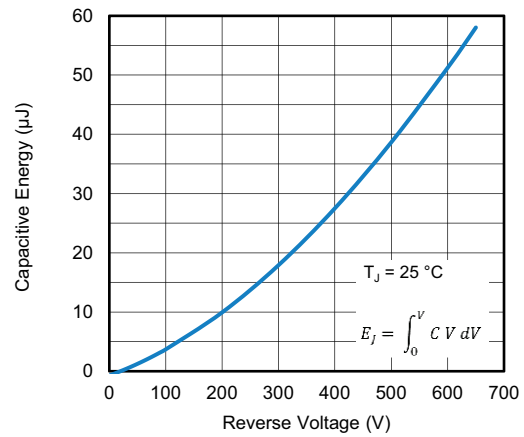


Fig. 8 - Typical Capacitive Energy vs. Reverse Voltage

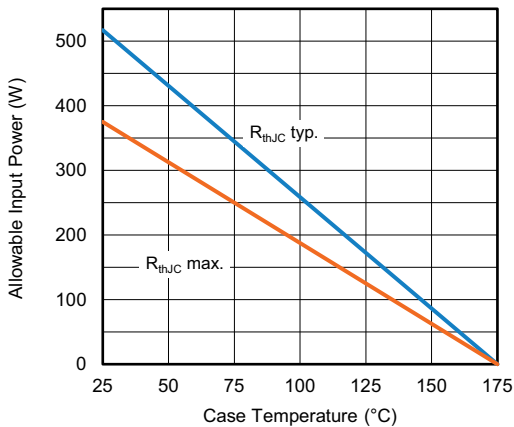


Fig. 7 - Forward Power Loss Characteristics

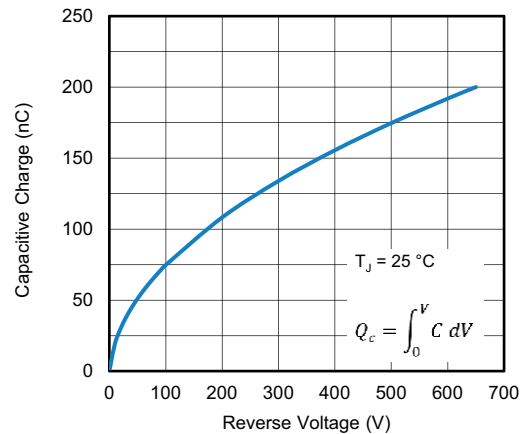
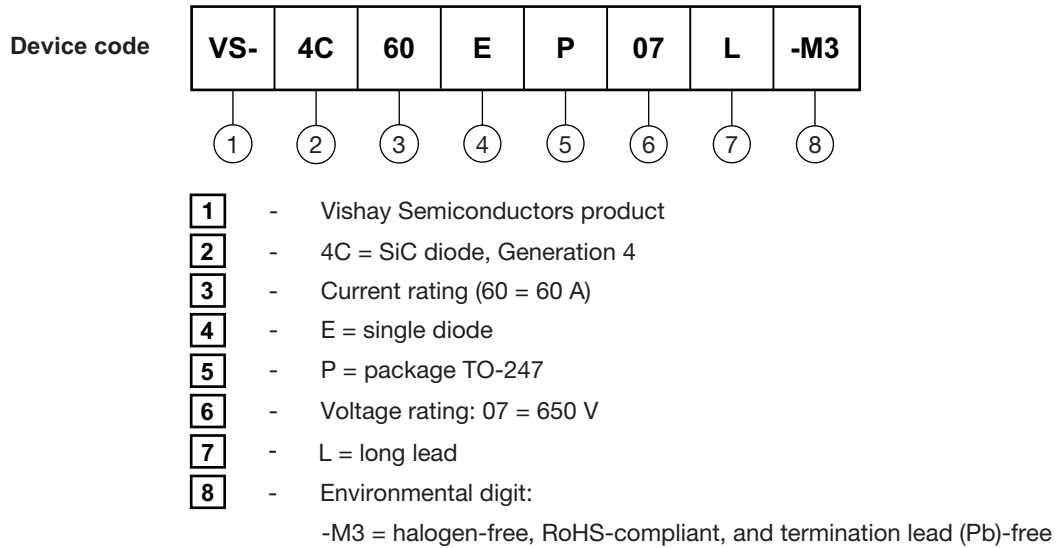


Fig. 9 - Typical Capacitive Charge vs. Reverse Voltage



## ORDERING INFORMATION TABLE



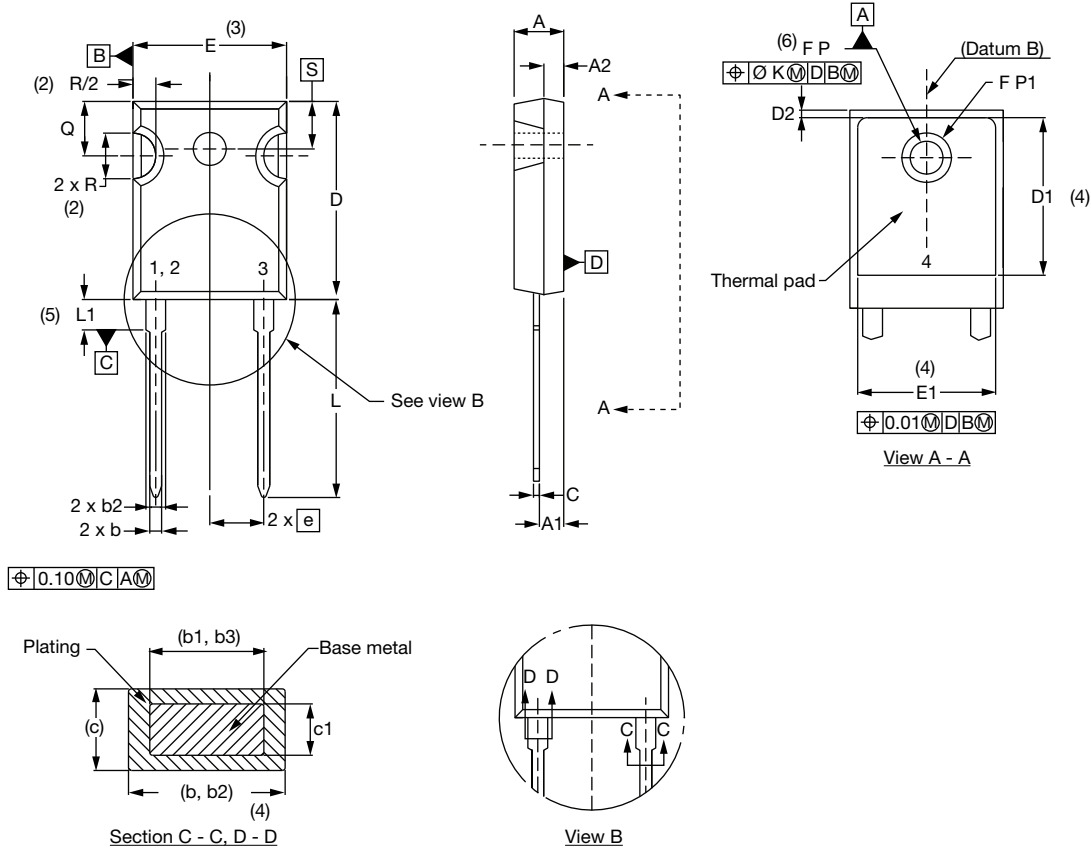
ORDERING INFORMATION			
PREFERRED P/N	UNIT WEIGHT	BASE QUANTITY	PACKAGING DESCRIPTION
VS-4C60EP07L-M3	5.5 g	25 / tube	Antistatic plastic tubes

LINKS TO RELATED DOCUMENTS	
Dimensions	<a href="http://www.vishay.com/doc?95536">www.vishay.com/doc?95536</a>
Part marking information	<a href="http://www.vishay.com/doc?95648">www.vishay.com/doc?95648</a>



# TO-247AD 2L

**DIMENSIONS** in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES	SYMBOL	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.			MIN.	MAX.	MIN.	MAX.	
A	4.65	5.31	0.183	0.209		E	15.29	15.87	0.602	0.625	3
A1	2.21	2.59	0.087	0.102		E1	13.46	-	0.53	-	
A2	1.50	2.49	0.059	0.098		e	5.46 BSC		0.215 BSC		
b	0.99	1.40	0.039	0.055		Ø K	0.254		0.010		
b1	0.99	1.35	0.039	0.053		L	19.81	20.32	0.780	0.800	
b2	1.65	2.39	0.065	0.094		L1	3.71	4.29	0.146	0.169	
b3	1.65	2.34	0.065	0.092		Ø P	3.56	3.66	0.14	0.144	
c	0.38	0.89	0.015	0.035		Ø P1	-	6.98	-	0.275	
c1	0.38	0.84	0.015	0.033		Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3	R	4.52	5.49	0.178	0.216	
D1	13.08	-	0.515	-	4	S	5.51 BSC		0.217 BSC		
D2	0.51	1.35	0.020	0.053							

**Notes**

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension A min., D, E min., Q min., S, and note 4



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