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*February 10, 2026

*DD Vishay Semiconductor Italiana SPA

*File Name: VS-4C30EP07L-E3P-4C60CP07L.txt

*This document is intended as a SPICE modeling guideline and does not

*constitute a commercial product datasheet. Designers should refer to the

*appropriate datasheet of the same number for guaranteed specification

*limits.

* Model fit device @25°C-125°C

* DEVICE= VS-4C30EP07L-E3P-4C60CP07L, SiC Diode

.MODEL VS-4C30EP07L-E3P-4C60CP07L D

+ IS=9.681400E-11

+ N=1.487328E+00

+ RS=9.791781E-03

+ IKF=2.000000E+01

+ EG=3.260000E+00

+ CJO=1.736570E-09

+ M=4.669661E-01

+ VJ=1.200000E+00

+ FC=5.000000E-01

+ ISR=5.000000E-11

+ NR=4.995000E+00

+ BV=8.241500E+02

+ IBV=5.000000E-03

+ TT=1.000000E-08

+ XTI=-6.731891E+01

+ TIKF=-4.291322E-09

+ TRS1=-1.132028E-03

+ TRS2=7.627528E-05

*THERMAL MODEL SUBCIRCUIT

*RC Foster model

.SUBCKT zth- VS-4C30EP07L-E3P-4C60CP07L Tj Tc

R THERM1	Tj	1	7.42482E-02
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R THERM2	1	2	1.78285E-01
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R THERM3	2	Tc	3.20367E-01
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C THERM1	Tj	1	1.41639E-03
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C THERM2	1	2	6.82905E-03
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C THERM3	2	Tc	3.20628E-02
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.ENDS zth- VS-4C30EP07L-E3P-4C60CP07L



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