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*DD Vishay Semiconductor Italiana SPA

*File Name: VS-4C10ET12T-S2L.txt

*This document is intended as a SPICE modeling guideline and does not

*constitute a commercial product datasheet. Designers should refer to the

*appropriate datasheet of the same number for guaranteed specification

*limits.

* Model fit device @25°C-125°C

* DEVICE= VS-4C10ET12T-S2L, SiC Diode

.MODEL VS-4C10ET12T-S2L D

+ IS=9.110667E-11

+ N=1.461970E+00

+ RS=3.868953E-02

+ IKF=1.999982E+01

+ EG=3.260000E+00

+ CJO=7.923265E-10

+ M=4.800714E-01

+ VJ=1.200000E+00

+ FC=5.000000E-01

+ ISR=5.000000E-11

+ NR=4.995000E+00

+ BV=1.521440E+03

+ IBV=5.000000E-03

+ TT=1.000000E-08

+ XTI=-6.814229E+01

+ TIKF=-2.908979E-07

+ TRS1=2.892771E-04

+ TRS2=9.421467E-05

*THERMAL MODEL SUBCIRCUIT

*RC Foster model

.SUBCKT zth- VS-4C10ET12T-S2L Tj Tc

R THERM1	Tj	1	2.94719E-01
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R THERM2	1	2	3.2197E-01
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R THERM3	2	3	3.51202E-01
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R THERM4	3	Tc	1.42482E-01
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C THERM1	Tj	1	2.18435E-03
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C THERM2	1	2	1.63941E-02
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C THERM3	2	3	1.89340E-01
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C THERM4	3	Tc	2.78491E-01
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.ENDS zth- VS-4C10ET12T-S2L



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