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*DD Vishay Semiconductor Italiana SPA

*File Name: VS-4C30ET12T-S2L.txt

*This document is intended as a SPICE modeling guideline and does not

*constitute a commercial product datasheet. Designers should refer to the

*appropriate datasheet of the same number for guaranteed specification

*limits.

* Model fit device @25°C-125°C

* DEVICE= VS-4C30ET12T-S2L, SiC Diode

.MODEL VS-4C30ET12T-S2L D

+ IS=1.506110E-10

+ N=1.466832E+00

+ RS=1.182038E-02

+ IKF=2.000000E+01

+ EG=3.260000E+00

+ CJO=2.422610E-09

+ M=4.877022E-01

+ VJ=1.200000E+00

+ FC=5.000000E-01

+ ISR=8.000000E-11

+ NR=4.995000E+00

+ BV=1.488520E+03

+ IBV=5.000000E-03

+ TT=1.000000E-08

+ XTI=-6.940592E+01

+ TIKF=4.496835E-11

+ TRS1=-4.388747E-04

+ TRS2=1.035625E-04

*THERMAL MODEL SUBCIRCUIT

*RC Foster model

.SUBCKT zth- VS-4C30ET12T-S2L Tj Tc

R THERM1	Tj	1	9.46382E-02
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R THERM2	1	2	1.09748E-01
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R THERM3	2	Tc	3.08154E-01
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C THERM1	Tj	1	6.89154E-03
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C THERM2	1	2	3.93249E-02
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C THERM3	2	Tc	2.24080E-01
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.ENDS zth- VS-4C30ET12T-S2L



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