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*February 10, 2026

*DD Vishay Semiconductor Italiana SPA

*File Name: VS-4C20ET12T-S2L.txt

*This document is intended as a SPICE modeling guideline and does not

*constitute a commercial product datasheet. Designers should refer to the

*appropriate datasheet of the same number for guaranteed specification

*limits.

* Model fit device @25°C-125°C

* DEVICE= VS-4C20ET12T-S2L, SiC Diode

.MODEL VS-4C20ET12T-S2L D

+ IS=1.198980E-10

+ N=1.487407E+00

+ RS=1.867652E-02

+ IKF=2.000000E+01

+ EG=3.260000E+00

+ CJO=1.564410E-09

+ M=4.794345E-01

+ VJ=1.200000E+00

+ FC=5.000000E-01

+ ISR=8.000000E-11

+ NR=4.995000E+00

+ BV=1.496520E+03

+ IBV=5.000000E-03

+ TT=1.000000E-08

+ XTI=-6.627062E+01

+ TIKF=-3.447435E-08

+ TRS1=3.889040E-04

+ TRS2=9.990890E-05

*THERMAL MODEL SUBCIRCUIT

*RC Foster model

.SUBCKT zth- VS-4C20ET12T-S2L Tj Tc

R THERM1	Tj	1	1.98765E-01
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R THERM2	1	2	2.16027E-01
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R THERM3	2	Tc	3.86332E-01
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C THERM1	Tj	1	3.48211E-03
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C THERM2	1	2	2.82208E-02
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C THERM3	2	Tc	1.57664E-01
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.ENDS zth- VS-4C20ET12T-S2L



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