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*DD Vishay Semiconductor Italiana SPA

*File Name: VS-3C02EJ12.txt

*This document is intended as a SPICE modeling guideline and does not

*constitute a commercial product datasheet. Designers should refer to the

*appropriate datasheet of the same number for guaranteed specification

*limits.

*Model fit device at 25°C to 125°C

* DEVICE=VS-3C02EJ12, Diode

.MODEL VS-3C02EJ12 D

+ IS=2.399530E-10

+ N=1.719167E+00

+ RS=1.867378E-01

+ IKF=1.887336E+01

+ EG=3.260000E+00

+ CJO=1.565972E-10

+ M=5.040122E-01

+ VJ=1.517228E+00

+ FC=5.000000E-01

+ ISR=1.500000E-09

+ NR=4.995000E+00

+ BV=1.450000E+03

+ IBV=5.390000E-01
+ TT=1.000000E-08
+ XTI=-7.174648E+01
+ TIKF=-5.880552E-04
+ TRS1=8.253677E-03
+ TRS2=-1.171301E-07

* Estimated package capacitance: 3.22 pF

*THERMAL MODEL SUBCIRCUIT

*RC Foster model

.SUBCKT zth-VS-3C02EJ12 Tj Tc

R THERM1	Tj	1	9.63862E-01
R THERM2	1	2	2.67212E+00
R THERM3	2	Tc	8.55925E+00

C THERM1	Tj	1	1.21836E-04
C THERM2	1	2	3.93282E-03
C THERM3	2	Tc	1.06496E-02

.ENDS zth-VS-3C02EJ12



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