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*DD Vishay Semiconductor Italiana SPA

*File Name: VS-3C01EJ12.txt

*This document is intended as a SPICE modeling guideline and does not

*constitute a commercial product datasheet. Designers should refer to the

*appropriate datasheet of the same number for guaranteed specification

*limits.

*Model fit device at 25°C to 125°C

* DEVICE=VS-3C01EJ12, Diode

.MODEL VS-3C01EJ12 D

+ IS=1.110625E-10

+ N=1.611238E+00

+ RS=3.916210E-01

+ IKF=1.489533E+01

+ EG=3.260000E+00

+ CJO=8.059708E-11

+ M=4.974836E-01

+ VJ=1.512080E+00

+ FC=5.000000E-01

+ ISR=3.700000E-09

+ NR=4.995000E+00

+ BV=1.450000E+03

+ IBV=7.600000E-01
+ TT=1.000000E-08
+ XTI=-7.994000E+01
+ TIKF=-6.642883E-03
+ TRS1=1.358296E-02
+ TRS2=-6.050000E-05

* Estimated package capacitance: 2.01 pF

*THERMAL MODEL SUBCIRCUIT

*RC Foster model

.SUBCKT zth-VS-3C01EJ12 Tj Tc

| | | | |
|----------|----|----|-------------|
| R THERM1 | Tj | 1 | 1.29034E+00 |
| R THERM2 | 1 | 2 | 2.71974E+00 |
| R THERM3 | 2 | Tc | 8.13165E+00 |

| | | | |
|----------|----|----|-------------|
| C THERM1 | Tj | 1 | 1.13664E-04 |
| C THERM2 | 1 | 2 | 2.1881E-03 |
| C THERM3 | 2 | Tc | 9.00202E-03 |

.ENDS zth-VS-3C01EJ12



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