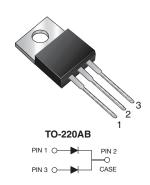


## Vishay General Semiconductor

# Dual High Voltage TMBS® (Trench MOS Barrier Schottky) Rectifier

Ultra Low  $V_F = 0.36 \text{ V}$  at  $I_F = 5 \text{ A}$ 



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	2 x 20 A			
$V_{RRM}$	100 V			
I <sub>FSM</sub>	250 A			
V <sub>F</sub> at I <sub>F</sub> = 20 A (125 °C)	0.58 V			
T <sub>J</sub> max.	150 °C			
Package	TO-220AB			
Circuit configuration	Common cathode			

#### **FEATURES**

Trench MOS Schottky technology

· Low forward voltage drop, low power losses

High efficiency operation

 Solder bath temperature 275 °C maximum, 10 s, per JESD 22-B106

 Material categorization: for definitions of compliance please see <a href="https://www.vishav.com/doc?99912">www.vishav.com/doc?99912</a>

#### **TYPICAL APPLICATIONS**

For use in high frequency converters, switching power supplies, freewheeling diodes, OR-ing diode, DC/DC converters, and reverse battery protection.

#### **MECHANICAL DATA**

Case: TO-220AB

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and

commercial grade

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER		SYMBOL	V41103C	UNIT
Maximum repetitive peak reverse voltage		$V_{RRM}$	100	V
Maximum average forward rectified current (fig. 1)	per device		40	А
	per diode	I <sub>F(AV)</sub>	20	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I <sub>FSM</sub>	250	Α
Operating junction temperature range		T <sub>J</sub> <sup>(1)</sup>	-40 to +150	°C
Storage temperature range		T <sub>STG</sub>	-55 to +150	

#### Note

<sup>(1)</sup> The heat generated must be less than the thermal conductivity from junction to ambient: dP<sub>D</sub>/dT<sub>J</sub> <1/ R<sub>8,IA</sub>



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I <sub>F</sub> = 5 A	T <sub>J</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.45	-	V	
	I <sub>F</sub> = 10 A			0.52	-		
	I <sub>F</sub> = 20 A			0.63	0.70		
	I <sub>F</sub> = 5 A	T <sub>J</sub> = 125 °C		0.36	-		
	I <sub>F</sub> = 10 A			0.46	-		
	I <sub>F</sub> = 20 A			0.58	0.63		
Reverse current at rated V <sub>R</sub> per diode	V - 70 V	T <sub>J</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	0.02	-		
	$V_R = 70 \text{ V}$	T <sub>J</sub> = 125 °C		1 (2)	11	-	mA
	V <sub>R</sub> = 100 V	T <sub>J</sub> = 25 °C		-	1.9	TIIA	
		T <sub>J</sub> = 125 °C		26	45	]	
Typical junction capacitance	4 V, 1MHz		CJ	2500	-	pF	

#### **Notes**

 $^{(2)}$  Pulse test: 300  $\mu$ s pulse width, 1 % duty cycle

(3) Pulse test: Pulse width ≤ 5 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V41103C	UNIT	
Typical thermal resistance per device	R <sub>0</sub> JC (1)	1.0	°C/W	

#### Note

(4) Thermal resistance junction-to-case to follow JEDEC® 51-14 transient dual interface test method (TDIM)

OERDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
V41103C-M3/P	1.88	Р	50/tube	Tube		



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### **RATINGS AND CHARACTERISTICS CURVES** (T<sub>A</sub> = 25 °C unless otherwise noted)

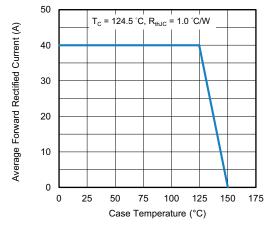


Fig. 1 - Forward Current Derating Curve

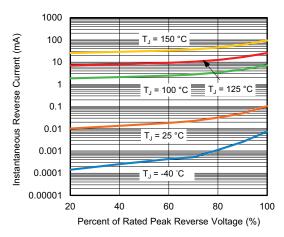


Fig. 4 - Typical Reverse Characteristics Per Diode

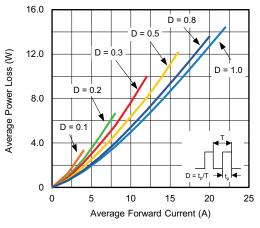


Fig. 2 - Forward Power Loss Characteristics Per Diode

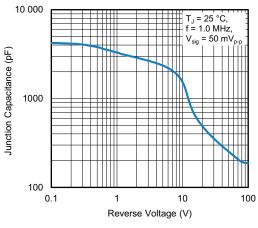


Fig. 5 - Typical Junction Capacitance

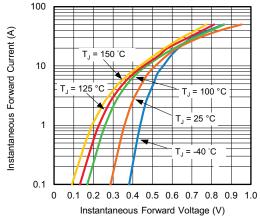


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

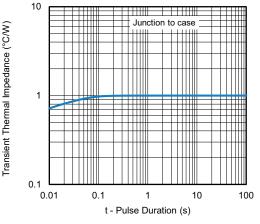
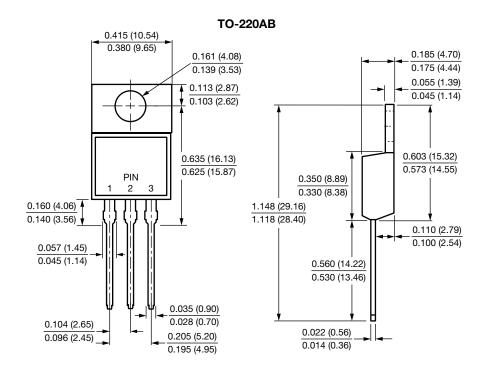


Fig. 6 - Typical Transient Thermal Impedance Per Device



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### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)





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