# V10PW60C

Vishay General Semiconductor

## High Current Density Surface-Mount TMBS<sup>®</sup> (Trench MOS Barrier Schottky) Rectifier

Ultra Low  $V_F = 0.38$  V at  $I_F = 2.5$  A



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### SlimDPAK (TO-252AE)

### LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	2 x 5 A			
V <sub>RRM</sub>	60 V			
I <sub>FSM</sub>	80 A			
$V_{F}$ at $I_{F} = 5 \text{ A} (T_{J} = 125 \text{ °C})$	0.48 V			
T <sub>J</sub> max.	150 °C			
Package	SlimDPAK (TO-252AE)			
Circuit configuration Common cathode				

## FEATURES

- Very low profile typical height of 1.3 mm
- Trench MOS Schottky technology
- Ideal for automated placement
- Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available
   Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### **TYPICAL APPLICATIONS**

For use in low voltage high frequency DC/DC converters, freewheeling diodes, and polarity protection applications.

## **MECHANICAL DATA**

Case: SlimDPAK (TO-252AE)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meets JESD 201 class 2 whisker test

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	V10PW60C	UNIT	
Device marking code		V10PW60C			
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	60	V		
Maximum average forward rectified current (Fig. 1)	per device	I <sub>F(AV)</sub> <sup>(1)</sup>	10	А	
	per diode		5	А	
Peak forward surge current 8.3 ms single half sine-was superimposed on rated load per diode	I <sub>FSM</sub> 80		А		
Operating junction temperature range	T <sub>J</sub> <sup>(2)</sup>	T <sub>J</sub> <sup>(2)</sup> -40 to +150			
Storage temperature range	T <sub>STG</sub>	-55 to +150	°C		

#### Notes

<sup>(1)</sup> With infinite heatsink

 $^{(2)}$  The heat generated must be less than the thermal conductivity from junction to ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ 





COMPLIANT HALOGEN

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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_J$ = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage per diode	I <sub>F</sub> = 2.5 A	T <sub>J</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.47	-	V
	I <sub>F</sub> = 5 A			0.54	0.61	
	I <sub>F</sub> = 2.5 A	T <sub>J</sub> = 125 °C		0.38	-	
	I <sub>F</sub> = 5 A			0.48	0.56	
Reverse current per diode	V <u>60</u> .V	T <sub>J</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	-	0.6	mA
	$V_{\rm R} = 60 \text{ V}$ $T_{\rm J} =$	T <sub>J</sub> = 125 °C		5	20	
Typical junction capacitance per diode	4.0 V, 1 MHz		CJ	570	-	pF

#### Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

 $^{(2)}~$  Pulse test: pulse width  $\leq 5~ms$ 

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER	SYMBOL	V10PW60C	UNIT		
Typical thermal resistance per device	R <sub>0JA</sub> (1)(2)	65	°C/W		
	R <sub>0JM</sub> <sup>(3)</sup>	2.1			

#### Notes

 $^{(1)}$  The heat generated must be less than thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ 

 $^{(2)}$  Free air, mounted on recommended copper pad area; thermal resistance  $R_{\theta JA}$  - junction to ambient

<sup>(3)</sup> Mounted on infinite heat sink; thermal resistance  $R_{\theta JM}$  - junction-to-mount

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
V10PW60C-M3/I	0.20	I	4500	13" diameter plastic tape and reel		
V10PW60CHM3/I (1)	0.20	l	4500	13" diameter plastic tape and reel		

Note

(1) AEC-Q101 qualified



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## RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)

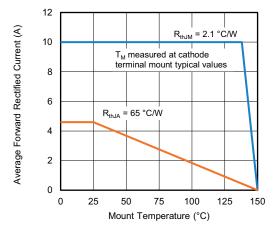


Fig. 1 - Maximum Forward Current Derating Curve

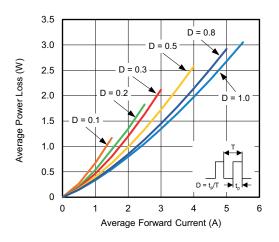


Fig. 2 - Forward Power Loss Characteristics Per Diode

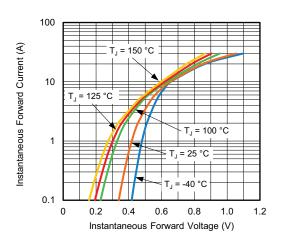


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

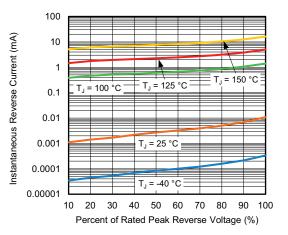


Fig. 4 - Typical Reverse Leakage Characteristics Per Diode

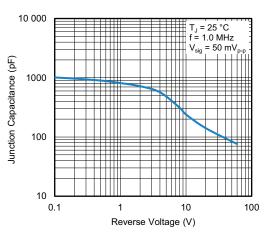
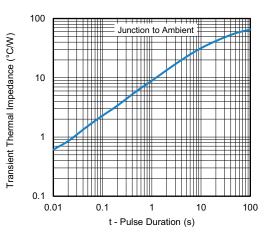


Fig. 5 - Typical Junction Capacitance Per Diode





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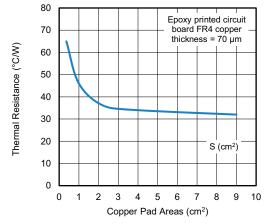
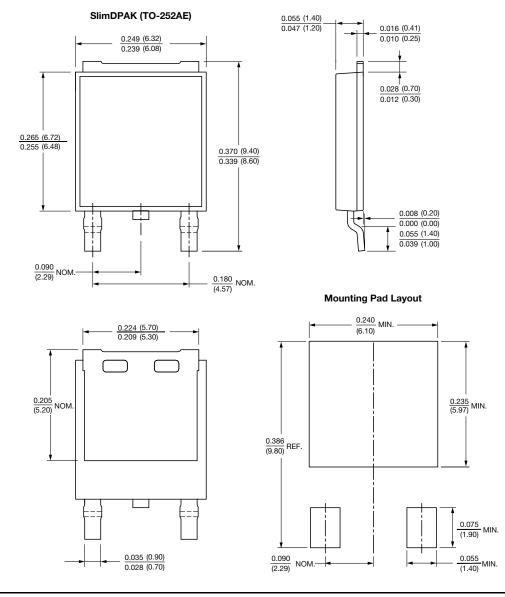


Fig. 7 - Typical Resistance Junction to Ambient vs. Copper Pad Areas



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