Available

ROHS COMPLIANT

HALOGEN

FREE

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Vishay General Semiconductor

# Surface-Mount TMBS<sup>®</sup> (Trench MOS Barrier Schottky) Rectifier



Cathode O Anode

## LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	3.0 A			
V <sub>RRM</sub>	60 V			
I <sub>FSM</sub>	60 A			
$V_F$ at $I_F = 3.0$ A	0.48 V			
T <sub>J</sub> max.	150 °C			
Package	SMP (DO-220AA)			
Circuit configuration	Single			

### FEATURES

- Low profile package
- Trench MOS Schottky technology
- Low power losses, high efficiency
- Low forward voltage drop
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available - Automotive ordering code; base P/NHM3
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### **TYPICAL APPLICATIONS**

For use in low voltage, high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

### **MECHANICAL DATA**

Case: SMP (DO-220AA)

Molding compound meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test, HM3 suffix meets JESD 201 class 2 whisker test

Polarity: color band denotes the cathode end

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V3PL63	UNIT	
Device marking code		3LF		
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	60	V	
Maximum DC forward current	I <sub>F(AV)</sub> <sup>(1)</sup>	3	А	
	I <sub>F(AV)</sub> <sup>(2)</sup>	2.1	A	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	60	А	
Operating junction temperature range	T <sub>J</sub> <sup>(3)</sup>	-40 to +150	°C	
Storage temperature range	T <sub>STG</sub>	-55 to +150	°C	

Notes

<sup>(1)</sup> Mounted on 10 mm x 10 mm PCB pad area

<sup>(2)</sup> Free air, mounted on recommended copper pad area

 $^{(3)}$  The heat generated must be less than the thermal conductivity from junction-to-ambient: dP<sub>D</sub>/dT<sub>J</sub> < 1/R<sub>0JA</sub>

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**V3PL63** 



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise noted)						
PARAMETER	TEST CO	TEST CONDITIONS		TYP.	MAX.	UNIT
Instantaneous forward voltage	I <sub>F</sub> = 1.5 A		V <sub>F</sub> <sup>(1)</sup>	0.46	-	V
	I <sub>F</sub> = 3 A			0.53	0.59	
	I <sub>F</sub> = 1.5 A	- T <sub>J</sub> = 125 °C		0.38	-	
	I <sub>F</sub> = 3 A			0.48	0.55	
Reverse current	V <sub>B</sub> = 60 V	T <sub>J</sub> = 25 °C T <sub>J</sub> = 125 °C	I <sub>R</sub> <sup>(2)</sup>	-	0.07	- mA
	v <sub>R</sub> = 60 v	T <sub>J</sub> = 125 °C		2.5	5	
Typical junction capacitance	4.0 V, 1 MF	4.0 V, 1 MHz		460	-	pF

#### Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: pulse width  $\leq$  5 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise specified)				
PARAMETER	SYMBOL	V3PL63	UNIT	
Typical thermal resistance	R <sub>0JA</sub> (1)(2)	125	°C/W	
	R <sub>θJM</sub> <sup>(3)</sup>	15		

#### Notes

<sup>(1)</sup> Free air, mounted on recommended PCB, 2 oz. pad area; thermal resistance  $R_{\theta JA}$  - junction-to-ambient

<sup>(2)</sup> The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ 

 $^{(3)}$  Mounted on 10 mm x 10 mm copper pad area PCB; thermal resistance  $R_{\theta JM}$  - junction-to-mount

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
V3PL63-M3/H	0.024	Н	3000	7" diameter plastic tape and reel		
V3PL63-M3/I	0.024	I	10 000	13" diameter plastic tape and reel		
V3PL63HM3/H <sup>(1)</sup>	0.024	Н	3000	7" diameter plastic tape and reel		
V3PL63HM3/I <sup>(1)</sup>	0.024	l	10 000	13" diameter plastic tape and reel		

#### Note

(1) AEC-Q101 qualified



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## **RATINGS AND CHARACTERISTICS CURVES** (T<sub>A</sub> = 25 °C unless otherwise noted)

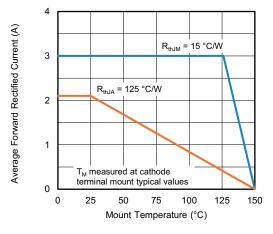


Fig. 1 - Maximum Forward Current Derating Curve

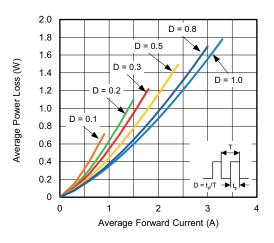


Fig. 2 - Forward Power Loss Characteristics

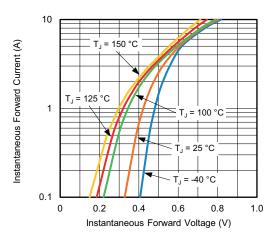


Fig. 3 - Typical Instantaneous Forward Characteristics

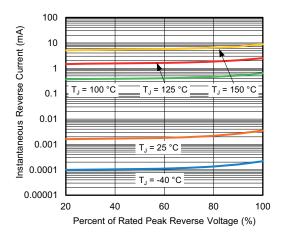


Fig. 4 - Typical Reverse Characteristics

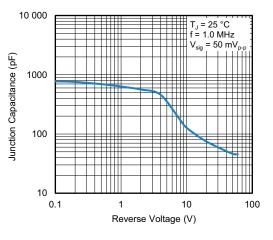


Fig. 5 - Typical Junction Capacitance

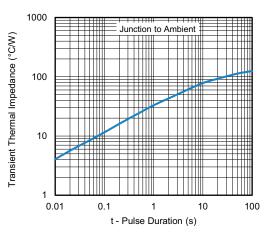


Fig. 6 - Typical Transient Thermal Impedance

Revision: 08-Mar-2022

3

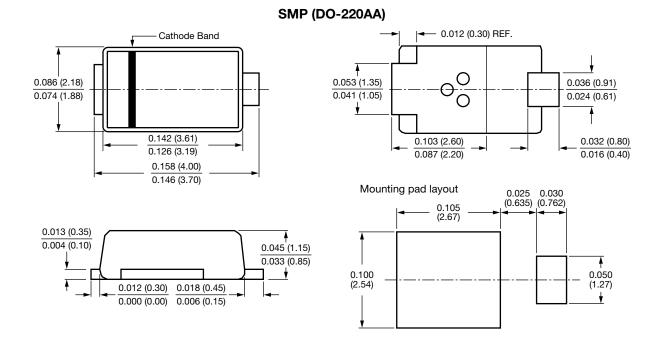
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## **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)





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Revision: 01-Jan-2025

1