# V35PWL63

Vishay General Semiconductor

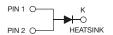
## High Current Density Surface-Mount TMBS<sup>®</sup> (Trench MOS Barrier Schottky) Rectifier

Ultra Low  $V_F = 0.28$  V at  $I_F = 5$  A



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### SlimDPAK (TO-252AE)



### LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	35 A			
V <sub>RRM</sub>	60 V			
I <sub>FSM</sub>	260 A			
$V_F$ at $I_F$ = 35 A ( $T_J$ = 125 °C)	0.56 V			
T <sub>J</sub> max.	150 °C			
Package	SlimDPAK (TO-252AE)			
Circuit configuration	Single			

## FEATURES

- Very low profile typical height of 1.3 mm
- Trench MOS Schottky technology
- Ideal for automated placement
- Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available
  Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### **TYPICAL APPLICATIONS**

For use in low voltage high frequency DC/DC converters, freewheeling diodes, and polarity protection applications.

## **MECHANICAL DATA**

Case: SlimDPAK (TO-252AE)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meets JESD 201 class 2 whisker test

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	V35PWL63	UNIT	
Device marking code		V35PWL63		
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	60	V	
Maximum average forward rectified current (Fig. 1)	I <sub>F(AV)</sub> <sup>(1)</sup>	35	A	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	260	A	
Operating junction temperature range	T <sub>J</sub> <sup>(2)</sup>	-40 to +150	°C	
Storage temperature range	T <sub>STG</sub>	-55 to +150	°C	

Notes

(1) With infinite heatsink

<sup>(2)</sup> The heat generated must be less than the thermal conductivity from junction to ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ 

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COMPLIANT

HALOGEN

V35PWL63



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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_J = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	TEST CO	TEST CONDITIONS		TYP.	MAX.	UNIT
Instantaneous forward voltage	I <sub>F</sub> = 5.0 A	T <sub>J</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.40	-	V
	I <sub>F</sub> = 17.5 A			0.49	-	
	I <sub>F</sub> = 35 A			0.60	0.68	
	I <sub>F</sub> = 5.0 A	T <sub>J</sub> = 125 °C		0.28	-	
	I <sub>F</sub> = 17.5 A			0.42	-	
	I <sub>F</sub> = 35 A			0.56	0.63	
Reverse current	V <sub>B</sub> = 60 V	$T_{\rm J} = 25 ^{\circ}{\rm C}$	I <sub>R</sub> <sup>(2)</sup>	-	0.7	mA
	v <sub>R</sub> = 00 v	T <sub>J</sub> = 125 °C		20	50	
Typical junction capacitance	4.0 V, 1 MHz		CJ	4700	-	pF

#### Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: pulse width  $\leq$  5 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)			
PARAMETER SYM		V35PWL63	UNIT
Typical thermal resistance	R <sub>0JA</sub> <sup>(1)(2)</sup>	65	°C/W
	R <sub>0JM</sub> <sup>(3)</sup>	1.5	C/W

#### Notes

 $^{(1)}$  The heat generated must be less than thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ 

 $^{(2)}\,$  Free air, mounted on recommended copper pad area; thermal resistance  $R_{\theta JA}$  - junction to ambient

 $^{(3)}$  Mounted on infinite heat sink; thermal resistance  $R_{\theta JM}$  - junction-to-mount

ORDERING INFORMATION (Example)					
PREFERRED P/N	EFERRED P/N UNIT WEIGHT (g) PREFERRED PACKAGE CODE		BASE QUANTITY	DELIVERY MODE	
V35PWL63-M3/I	0.20	I	4500	13" diameter plastic tape and reel	
V35PWL63HM3/I <sup>(1)</sup>	0.20	I	4500	13" diameter plastic tape and reel	

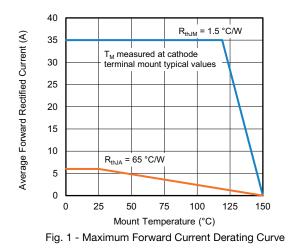
#### Note

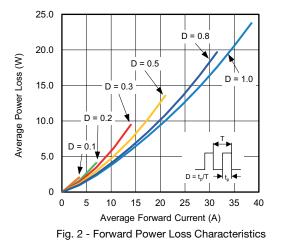
(1) AEC-Q101 qualified

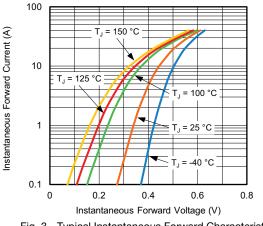


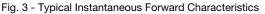
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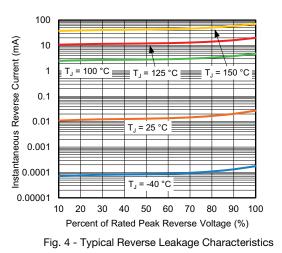
## RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)

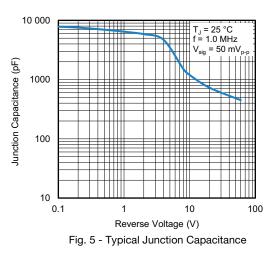












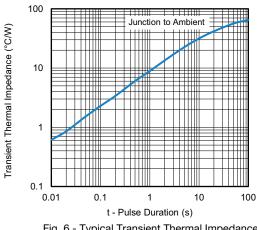


Fig. 6 - Typical Transient Thermal Impedance

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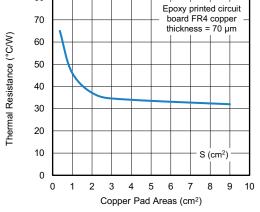
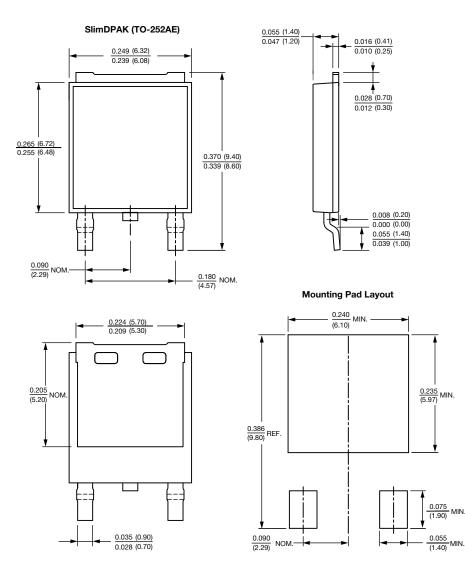


Fig. 7 - Typical Resistance Junction to Ambient vs. Copper Pad Areas

## **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)



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