AUTOMOTIVE

COMPLIANT

HALOGEN FREE



# Vishay General Semiconductor

# Surface-Mount PAR® Transient Voltage Suppressors

High Temperature Stability and High Reliability Conditions





#### **LINKS TO ADDITIONAL RESOURCES**









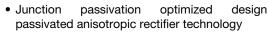


PRIMARY CHARACTERISTICS					
V <sub>BR</sub> 12 V to 51 V					
$V_{WM}$	10.2 V to 43.6 V				
P <sub>PPM</sub> (10 x 1000 μs)	600 W				
T <sub>J</sub> max.	185 °C				
Polarity	Unidirectional				
Package	DFN3820A				
Circuit configuration	Single				

## **FEATURES**

- Low-profile package typical height of 0.88 mm
- Leadless DFN package with side-wettable flanks suitable for customer AOI (Automatic Optical Inspection)





- T<sub>J</sub> = 185 °C capability suitable for high reliability and automotive requirement
- Unidirectional
- · Excellent clamping capability
- Peak pulse power: 600 W (10/1000 μs)
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified
  - Automotive ordering code: base P/NHM3
- Compatible to SMP (DO-220AA) package case outline
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

## **TYPICAL APPLICATIONS**

Use in sensitive electronics protection against voltage transients induced by inductive load switching and lightning on ICs, MOSFET, signal lines of sensor units for automotive.

## **MECHANICAL DATA**

Case: DFN3820A

Molding compound meets UL 94 V-0 flammability rating Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

HM3 suffix meets JESD 201 class 2 whisker test

Polarity: color band denotes cathode end, heatsink is anode

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	VALUE	UNIT			
Peak pulse power dissipation with a 10/1000 µs waveform (fig. 1) (1)	P <sub>PPM</sub>	600	W			
Peak pulse current with a 10/1000 µs waveform (fig. 3) (1)	I <sub>PPM</sub>	See table next page	А			
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	-65 to +185	°C			

#### Note

 $^{(1)}$  Non-repetitive current pulse, per fig. 3 and derated above  $T_A = 25$  °C per fig. 2



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C, unless otherwise noted)											
DEVICE TYPE	DEVICE MARKING CODE	BREAKDOWN VOLTAGE V <sub>BR</sub> <sup>(1)</sup> AT I <sub>T</sub> (V)		VOLTAGE CURRENT IT		OFF LE	MAXIMUM REVERSE LEAKAGE AT V <sub>WM</sub>	MAXIMUM REVERSE LEAKAGE AT V <sub>WM</sub> T <sub>J</sub> = 150 °C	MAXIMUM PEAK PULSE SURGE CURRENT I <sub>PPM</sub> (2)	MAXIMUM CLAMPING VOLTAGE AT I <sub>PPM</sub> V <sub>C</sub>	TYPICAL TEMP. COEFFICIENT OF V <sub>BR</sub> <sup>(3)</sup>
		MIN.	NOM.	MAX.			(µA)	(μ <b>A</b> )	(A)	(V)	(%/°C)
T6N12A	ABP	11.4	12.0	12.6	1.0	10.2	2.0	6.0	35.9	16.7	0.070
T6N13A	ABQ	12.4	13.0	13.7	1.0	11.1	2.0	5.0	33.0	18.2	0.072
T6N15A	ABR	14.3	15.0	15.8	1.0	12.8	1.0	5.0	28.3	21.2	0.076
T6N16A	ABS	15.2	16.0	16.8	1.0	13.6	1.0	5.0	26.7	22.5	0.078
T6N18A	ABT	17.1	18.0	18.9	1.0	15.3	1.0	5.0	23.5	25.5	0.080
T6N20A	ABV	19.0	20.0	21.0	1.0	17.1	1.0	5.0	21.7	27.7	0.082
T6N22A	ABW	20.9	22.0	23.1	1.0	18.8	1.0	5.0	19.6	30.6	0.084
T6N24A	ABY	22.8	24.0	25.2	1.0	20.5	1.0	5.0	18.1	33.2	0.085
T6N27A	ABZ	25.7	27.0	28.4	1.0	23.1	1.0	5.0	16.0	37.5	0.087
T6N30A	ACF	28.5	30.0	31.5	1.0	25.6	1.0	5.0	14.5	41.4	0.088
T6N33A	ACG	31.4	33.0	34.7	1.0	28.2	1.0	5.0	13.1	45.7	0.089
T6N36A	ACH	34.2	36.0	37.8	1.0	30.8	1.0	5.0	12.0	49.9	0.090
T6N39A	ACL	37.1	39.0	41.0	1.0	33.3	1.0	5.0	11.1	53.9	0.091
T6N43A	ACM	40.9	43.0	45.2	1.0	36.8	1.0	5.0	10.1	59.3	0.092
T6N47A	ACN	44.7	47.0	49.4	1.0	40.2	1.0	10.0	9.3	64.8	0.092
T6N51A	ACP	48.5	51.0	53.6	1.0	43.6	1.0	10.0	8.6	70.1	0.093

#### **Notes**

- $^{(1)}$  Pulse test:  $t_p \leq 50 \ ms$
- (2) Surge current waveform per fig. 3 and derated per fig. 2
- $^{(3)}$  To calculate V<sub>BR</sub> vs. junction temperature, use the following formula: V<sub>BR</sub> at T<sub>J</sub> = V<sub>BR</sub> at 25 °C x (1 +  $\alpha$ T x (T<sub>J</sub> 25))
- (4) All terms and symbols are consistent with ANSI/IEEE C62.35

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL TYP. MAX. UNIT					
Thermal resistance	R <sub>θJA</sub> <sup>(1)</sup>	140	175	°C/W		
Thermal resistance	$R_{\theta JM}^{(2)}$	5	6.5	°C/W		

#### Notes

- (1) Thermal resistance junction-to-ambient to follow JEDEC® 51-2A, device mounted on FR4 PCB, 2 oz. standard footprint
- (2) Thermal resistance junction-to-mount to follow JEDEC® 51-14 using transient dual interface test method (TDIM)

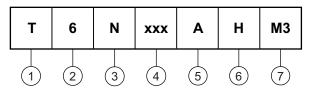
IMMUNITY TO STATIC ELECTRICAL DISCHARGE TO THE FOLLOWING STANDARDS ( $T_A = 25~^{\circ}\text{C}$ unless otherwise noted)						
STANDARD	TEST TYPE	TEST CONDITIONS	SYMBOL	VALUE		
IEC 61000-4-2	Contact discharge	C = 150 pF, R = 330 Ω	ESD	30 kV		
	Air discharge	- 0 = 150 pr, K = 350 Ω		30 kV		



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## **ORDERING INFORMATION TABLE**

**Device code** 



1 - Vishay PAR® TVS product

Peak pulse power rating (6 = 600 W)

Package type (N = DFN package)

Nominal breakdown voltage

5 - Breakdown voltage tolerance and polarity (A ± 5 %, unidirectional)

- Quality grade (H = AEC-Q101 qualified, otherwise = industry grade)

 Material / Environment category (M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free)

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
T6N12AHM3/H (1)	0.023	Н	3500	7" diameter plastic tape and reel		
T6N12AHM3/I (1)	0.023	I	14 000	13" diameter plastic tape and reel		

#### Note

(1) AEC-Q101 qualified

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# **RATINGS AND CHARACTERISTICS CURVES** (T<sub>A</sub> = 25 °C, unless otherwise noted)

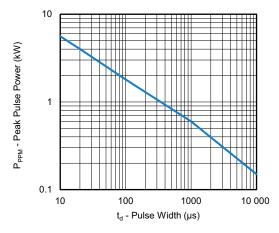


Fig. 1 - Peak Pulse Power Rating Curve

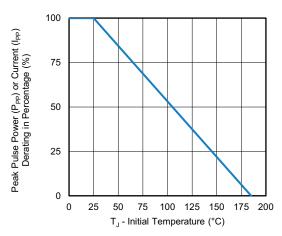


Fig. 2 - Pulse Power or Current vs. Initial Junction Temperature

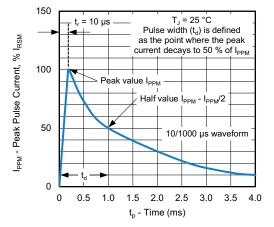


Fig. 3 - Pulse Waveform

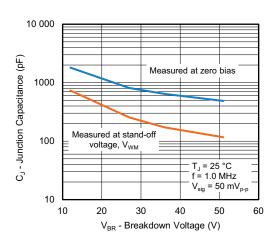


Fig. 4 - Typical Junction Capacitance

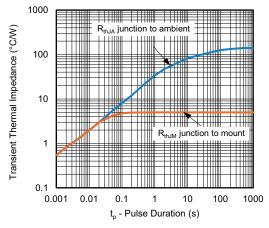


Fig. 5 - Typical Transient Thermal Impedance

### Note

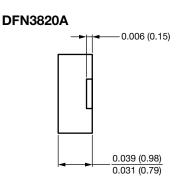
· Fig. 1, power calculations is based on I<sub>PPM</sub> times defined maximum clamping voltage by pulse width

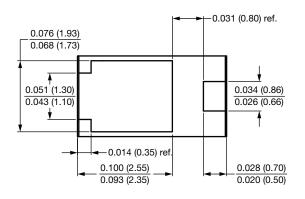


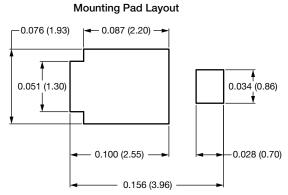
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## **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)

# 0.086 (2.18) 0.078 (1.98) 0.156 (3.95) 0.148 (3.75)









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