Ultra Low V<sub>F</sub> = 0.37 V at I<sub>F</sub> = 5.0 A

# FEATURES

Dual High-Voltage TMBS<sup>®</sup> (Trench MOS Barrier Schottky) Rectifier

- Trench MOS Schottky technology
- Very low profile typical height of 1.7 mm
- · Ideal for automated placement
- Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL J-STD-020. level 1, per LF maximum peak of 260 °C
- AEC-Q101 qualified available: Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### **TYPICAL APPLICATIONS**

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection in commercial, industrial, and automotive application.

### **MECHANICAL DATA**

Case: SMPD (TO-263AC)

Molding compound meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, RoHS-compliant

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 gualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meet JESD 201 class 2 whisker test Polarity: as marked

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	V60D103C	UNIT	
Device marking code			V60D103C		
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	100	V	
Maximum average forward rectified current (fig. 1)	per device	I <sub>F(AV)</sub> <sup>(1)</sup>	60	A	
	per diode		30		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I <sub>FSM</sub>	390	А	
Operating junction temperature range		T <sub>J</sub> <sup>(2)</sup>	-40 to +150	°C	
Storage temperature range		T <sub>STG</sub>	-55 to +150	U	

Notes

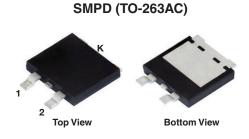
<sup>(1)</sup> Mounted on infinite heatsink

 $^{(2)}$  The heat generated must be less than the thermal conductivity from junction-to-ambient: dP<sub>D</sub>/dT<sub>J</sub> < 1/R<sub>b,IA</sub>

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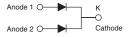
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eSMP<sup>®</sup> Series

www.vishay.com



# LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	2 x 30 A			
V <sub>RRM</sub>	100 V			
I <sub>FSM</sub>	390 A			
V <sub>F</sub> at I <sub>F</sub> = 30 A (T <sub>J</sub> = 125 °C)	0.61 V			
T <sub>J</sub> max.	150 °C			
Package	SMPD (TO-263AC)			
Circuit configuration	Common cathode			

# V60D103C

RoHS

COMPLIANT

HALOGEN FREE

Vishay General Semiconductor





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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_J = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I <sub>F</sub> = 5 A	T <sub>J</sub> = 25 °C	V <sub>F</sub> (1)	0.47	-	V	
	I <sub>F</sub> = 15 A			0.57	-		
	I <sub>F</sub> = 30 A			0.69	0.75		
	$I_F = 5 A$	T <sub>J</sub> = 125 °C		0.37	-		
	I <sub>F</sub> = 15 A			0.50	-		
	I <sub>F</sub> = 30 A			0.61	0.66		
Reverse current per diode	V <sub>R</sub> = 70 V	T <sub>J</sub> = 25 °C	I <sub>R</sub> (2)	0.006	-	mA	
		T <sub>J</sub> = 125 °C		4.5	-		
	V <sub>R</sub> = 100 V	T <sub>J</sub> = 25 °C		-	0.95		
		T <sub>J</sub> = 125 °C		10	37		
Typical junction capacitance	4.0 V, 1 MHz		CJ	3550	-	pF	

Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  5 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER	SYMBOL	V60D103C	UNIT		
Typical thermal resistance per device	R <sub>0JC</sub> <sup>(1)</sup>	0.95	°C/W		
	R <sub>0JA</sub> (2)(3)	50	0/10		

#### Notes

<sup>(1)</sup> Mounted on infinite heatsink

 $^{(2)}$  The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ 

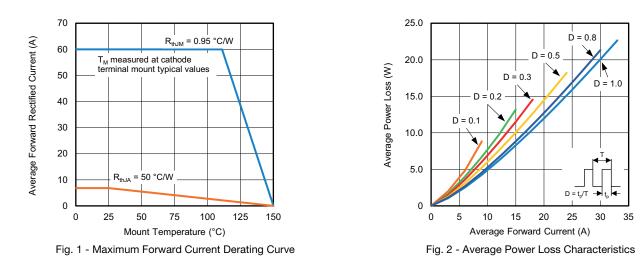
<sup>(3)</sup> Free air, without heatsink

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
V60D103C-M3/I	0.55	I	2000/reel	13" diameter plastic tape and reel		
V60D103CHM3/I (1)	0.55		2000/reel	13" diameter plastic tape and reel		

Note

(1) AEC-Q101 qualified

# RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)

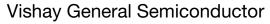


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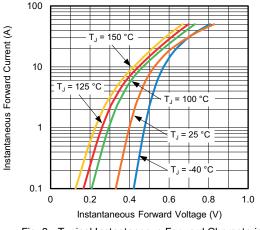


Fig. 3 - Typical Instantaneous Forward Characteristics

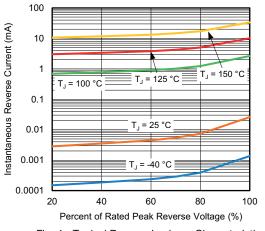


Fig. 4 - Typical Reverse Leakage Characteristics

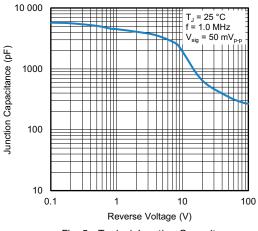


Fig. 5 - Typical Junction Capacitance

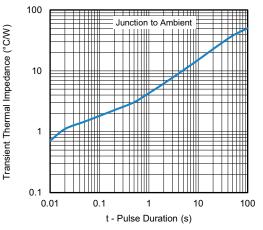
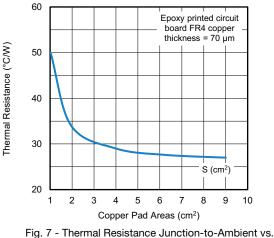


Fig. 6 - Typical Transient Thermal Impedance



-ig. 7 - Thermal Resistance Junction-to-Amblent vs Copper Pad Areas

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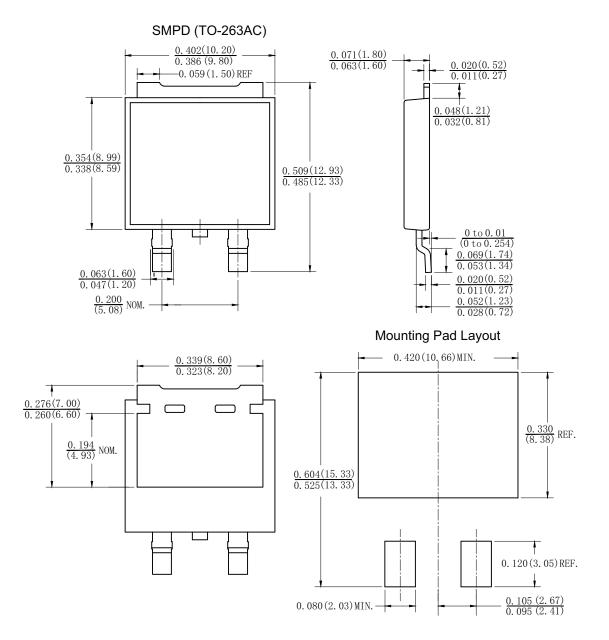
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## **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)





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