COMPLIANT

HALOGEN

FREE



Vishay General Semiconductor

Dual High Voltage TMBS® (Trench MOS Barrier Schottky) Rectifier

Ultra Low $V_F = 0.57 \text{ V}$ at $I_F = 30 \text{ A}$



PRIMARY CHARACTERISTICS					
$I_{F(AV)}$	2 x 60 A				
V_{RRM}	100 V				
I _{FSM}	550 A				
V _F at I _F = 60 A (T _J = 125 °C)	0.7 V				
T _J max.	175 °C				
Package	TO-247AD 3L				
Circuit configuration	Common cathode				

FEATURES

- Trench MOS Schottky technology
- Very low forward voltage drop by TMBS Gen 3 technology
- High efficiency operation
- Solder bath temperature 275 °C maximum, 10 s per JESD 22-B106
- AEC-Q101 qualified available:
 - Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection in commercial, industrial, and automotive application.

MECHANICAL DATA

Case: TO-247AD 3L

Molding compound meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, RoHS-compliant

Base P/NHM3 - halogen-free, RoHS-compliant, and

AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test, HM3 suffix

meets JESD 201 class 2 whisker test

Polarity: as marked

Mounting torque: 10 in-lbs maximum

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)						
PARAMETER		SYMBOL	VX120H103PW	UNIT		
Device marking code			VX120H103PW			
Maximum repetitive peak reverse voltage		V _{RRM}	100	V		
Maximum average forward rectified current (fig. 1)	per device		120	^		
	per diode	I _{F(AV)}	60	A		
Peak forward surge current 8.3 ms single half superimposed on rated load per device	sine-wave	I _{FSM}	550	А		
Operating junction temperature range Storage temperature range		T _J ⁽¹⁾	-40 to +175	°C		
		T _{STG}	-55 to +175			

Note

 $^{(1)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$



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ELECTRICAL CHARACTERISTICS (T _J = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage per diode	I _F = 30 A	T _{.1} = 25 °C	V _F ⁽¹⁾	0.65	-	V
	I _F = 60 A	1j = 25 C		0.78	0.85	
	I _F = 30 A	T _J = 125 °C		0.57	-	
	$I_F = 60 A$			0.7	0.76	
Reverse current at rated V _R per diode	V _R = 70 V	T _J = 25 °C	I _R ⁽²⁾	0.001	-	mA
		T _J = 125 °C		1.6	-	
	V _R = 100 V	T _J = 25 °C		0.007	0.35	
	v _R = 100 v	T _J = 125 °C		4.2	25	

Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

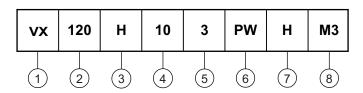
(2) Pulse test: Pulse width ≤ 5 ms

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)						
PARAMETER	METER SYMBOL VX120H103PW UNIT					
Typical thermal resistance per device	R ₀ JC (1)	0.8	°C/W			

Note

ORDERING INFORMATION TABLE

Device code



- 1 Vishay TMBS product
- 2 Current rating (120 = 120 A)
- Process type option (H = low I_R)
- 4 Voltage rating (10 = 100 V)
- 5 TMBS generation option (3 = Gen 3)
- 6 Package type (PW = TO-247AD 3L)
- Quality grade (H = AEC-Q101 qualified, = industry grade)
- Material / Environment category (M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
VX120H103PW-M3/P	5.64	Р	25/tube	Tube	
VX120H103PWHM3/P (1)	5.64	Р	25/tube	Tube	

Note

(1) AEC-Q101 qualified

⁽¹⁾ Thermal resistance junction-to-case to follow JEDEC® 51-14 transient dual interface test method (TDIM)



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RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C, unless otherwise noted)

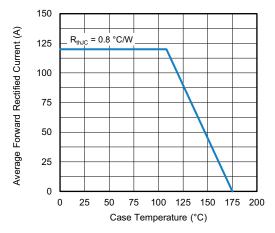


Fig. 1 - Maximum Forward Current Derating Curve Per Device

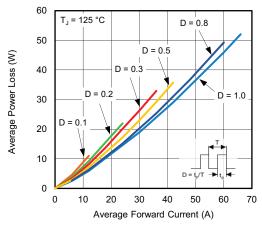


Fig. 2 - Average Power Loss Characteristics Per Diode

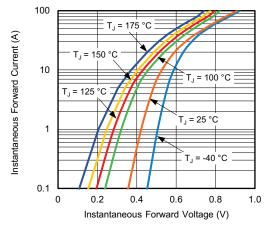


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

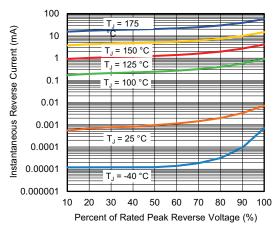


Fig. 4 - Typical Reverse Leakage Characteristics Per Diode

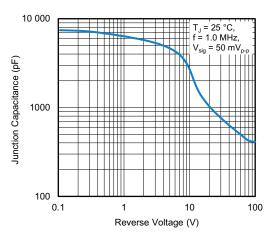


Fig. 5 - Typical Junction Capacitance Per Diode

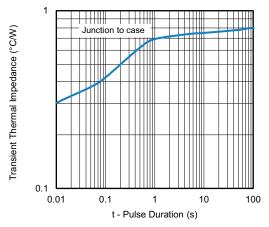
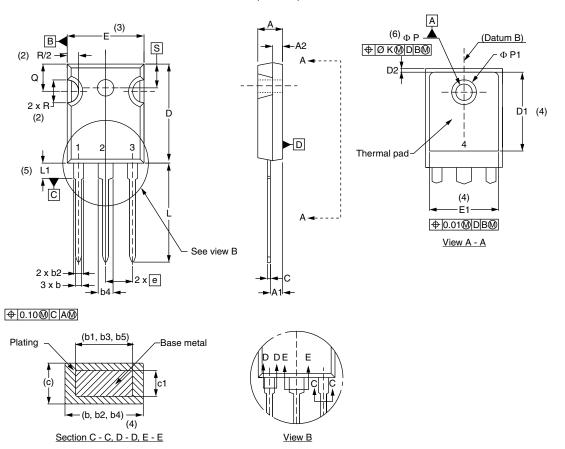


Fig. 6 - Typical Transient Thermal Impedance Per Device



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PACKAGE OUTLINE DIMENSIONS in millimeters (inches) TO-247AD 3L



SYMBOL	MILLIMETERS		INC	NOTES	
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.34	0.065	0.092	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
С	0.38	0.89	0.015	0.035	
c1	0.38	0.84	0.015	0.033	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

SYMBOL	MILLIMETERS		INC	NOTES	
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.30	0.020	0.051	
E	15.29	15.87	0.602	0.625	3
E1	13.46	-	0.53	-	
е	5.46	BSC	0.215	BSC	
ØК	0.2	0.254)10	
L	19.81	20.32	0.780	0.800	
L1	3.71	4.29	0.146	0.169	
ØΡ	3.56	3.66	0.14	0.144	
Ø P1	-	6.98	-	0.275	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	0.178	0.216	
S	5.51 BSC		0.217	BSC	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension A min., D, E min., Q min., S, and note 4



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