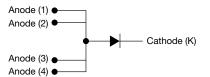
Vishay General Semiconductor

Surface-Mount Standard Rectifier

DFN33A

www.vishay.com



LINKS TO ADDITIONAL RESOURCES



SPICE

Models

PRIMARY CHARACTERISTICS						
I _{F(AV)} 6 A						
V _{RRM}	200 V, 400 V, 600 V					
I _{FSM}	80 A					
V_F at I_F = 6 A (T_J = 125 °C)	0.88 V					
T _J max.	175 °C					
Package	DFN33A					
Circuit configuration	Single					

FEATURES

- Low-profile package
 typical height of 0.88 mm
- Leadless DFN package with side-wettable flanks suitable for customer AOI (Automatic Optical Inspection)
- Ideal for automated replacement
- Oxide planar chip junction
- Low forward voltage drop
- Typical IR less than 0.1 μA
- ESD capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available
 Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

General purpose, power line polarity protection and rail-to-rail protection in consumer, industrial, and automotive applications.

MECHANICAL DATA

Case: DFN33A

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meet JESD 201 class 2 whisker test

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	SYMBOL	SE60N3D	SE60N3G	SE60N3J	UNIT		
Device marking code		6D	6G	6J			
Maximum repetitive peak reverse voltage	V _{RRM}	200	400	600	V		
Maximum avarage featured restified surrent (fig 1)	I _{F(AV)} ⁽¹⁾		А				
Maximum average forward rectified current (fig.1)	I _{F(AV)} ⁽²⁾						
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I _{FSM}		А				
Operating junction temperature range	T _J ⁽³⁾		°C				
Storage temperature range	T _{STG}						

Notes

⁽¹⁾ With infinite heatsink

⁽²⁾ Free air, mounted on recommended copper pad area

 $^{(3)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient: dP_D/dT_J < 1/R_{0JA}

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RoHS

COMPLIANT

HALOGEN

FREE



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ELECTRICAL CHARACTERISTICS ($T_J = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage	I _F = 3 A	T.I = 25 °C		0.91	-		
	$I_F = 6 A$	V _F ⁽¹⁾	0.98	1.05	V		
	I _F = 3 A	- T _J = 125 °C	VF	0.80	-	v	
	I _F = 6 A			0.88	0.98		
Reverse current	DetedV	T _J = 25 °C	1 (2)	-	10		
	Rated V_R $T_J = 125 °C$		I _R ⁽²⁾	18	100	μΑ	
Typical junction capacitance	4.0 V, 1 MHz	-	CJ	40	-	pF	

Notes

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: pulse width \leq 5 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise specified)							
PARAMETER	SYMBOL	TYP.	MAX.	UNIT			
Thermal resistance	R _{0JA} (1)(2)	122	153	°C/W			
memaresistance	R _{θJM} ⁽³⁾	2.9	3.6	C/W			

Notes

⁽¹⁾ The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$

(2) Thermal resistance junction-to-ambient to follow JEDEC[®] 51-2A, device mounted on FR4 PCB, 2 oz., standard footprint
 (3) Thermal resistance junction-to-mount to follow JEDEC 51-14 transient dual interface test method (TDIM)

IMMUNITY TO ELECTRICAL STATIC DISCHARGE TO THE FOLLOWING STANDARDS

$(I_A = 25 {}^{\circ}\text{C} \text{ unless otherwise noted})$									
STANDARD	TEST TYPE	TEST CONDITIONS	SYMBOL	CLASS	VALUE				
AEC-Q101-001	Human body model (contact mode)	C = 100 pF, R = 1.5 k Ω		H3B	> 8 kV				
AEC-Q101-005	Charge device mode	Refer to AEC-Q101-005		C3	> 1000 V				
JESD22-A114	Human body model (contact mode)	C = 100 pF, R = 1.5 kΩ	V _C	3B	> 8 kV				
IEC 61000-4-2 ⁽²⁾	Human body model (contact mode)	C = 150 pF, R = 330 Ω		4	> 8 kV				
1EC 01000-4-2	Human body model (air-discharge mode) ⁽¹⁾	C = 150 pF, R = 330 Ω		4	> 15 kV				

Notes

 $^{(1)}$ Immunity to IEC 61000-4-2 air discharge mode has a typical performance > 30 kV

(2) System ESD standard

ORDERING INFORMATION TABLE

		-	-				
Device code	S	Е	60	N3	J	н	М3
	1	2	3	4	5	6	7
	1	- Vis	hay star	ndard re	covery p	product	
	2	- Oxi	de plana	ar chip t	echnolog	gу	
	3	- Cur	rent rati	ng (60 =	= 6 A)		
	4	- Pac	kage ty	pe (N3 =	= DFN33	3A pack	age)
	5	- Vol	tage rat	ing (D =	200 V,	G = 400	V, J = 6
	6	- Qua	ality gra	de (H =	AEC-Q1	101 qual	ified, ot
	7				ental ca nd termi		

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ORDERING INFORMATION (Example)								
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE				
SE60N3J-M3/I	0.031	I	6000	13" diameter plastic tape and reel				
SE60N3JHM3/I ⁽¹⁾	0.031	I	6000	13" diameter plastic tape and reel				

Note

⁽¹⁾ AEC-Q101 qualified

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

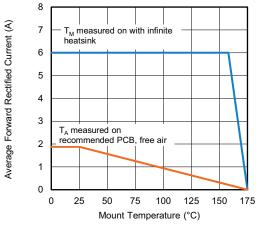


Fig. 1 - Maximum Forward Current Derating Curve

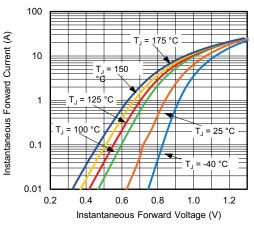


Fig. 3 - Typical Instantaneous Forward Characteristics

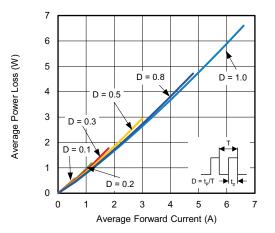


Fig. 2 - Forward Power Loss Characteristics

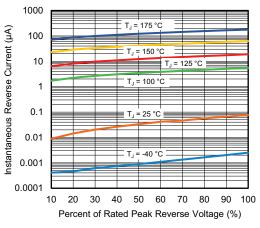


Fig. 4 - Typical Reverse Leakage Characteristics



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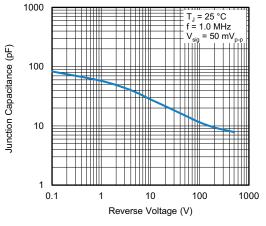


Fig. 5 - Typical Junction Capacitance

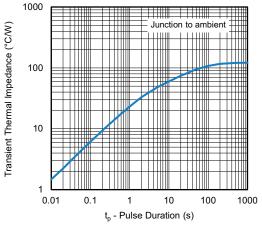
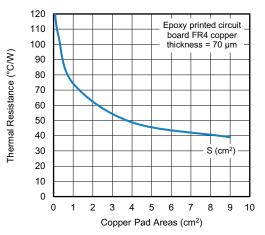
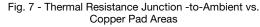


Fig. 6 - Typical Transient Thermal Impedance

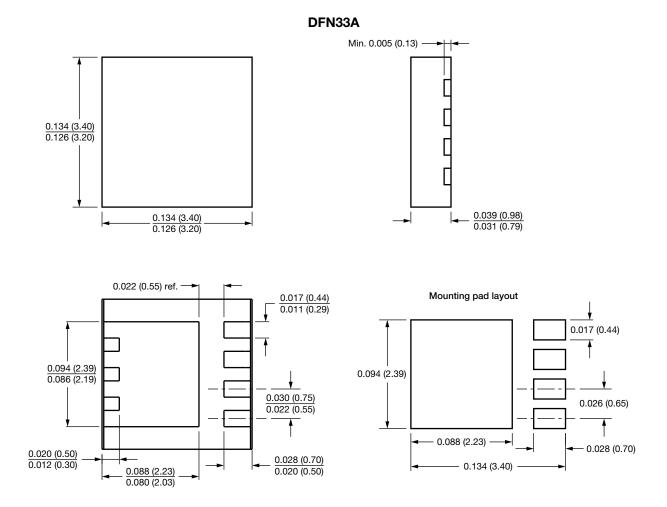






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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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