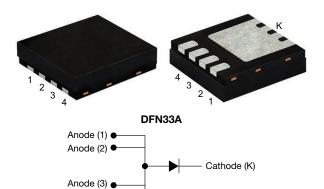


Vishay General Semiconductor

# Surface-Mount TMBS<sup>®</sup> (Trench MOS Barrier Schottky) Rectifier



# LINKS TO ADDITIONAL RESOURCES

Anode (4)



PRIMARY CHARACTERISTICS							
I <sub>F(AV)</sub>	7 A						
V <sub>RRM</sub>	150 V						
I <sub>FSM</sub>	120 A						
$V_F$ at $I_F$ = 3.5 A ( $T_J$ = 125 °C)	0.56 V						
T <sub>J</sub> max.	175 °C						
Package	DFN33A						
Circuit configuration	Single						

## FEATURES

- Low profile package typical height of 0.88 mm
  Available
- Leadless DFN package with side-wettable flanks suitable for customer AOI (Automatic Optical Inspection)



RoHS

COMPLIANT HALOGEN

FREE

- Very low voltage drop by TMBS Gen3 technology
- · Low power losses, high efficiency
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available
  Automotive ordering code; base P/NHM3
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

## **TYPICAL APPLICATIONS**

For use in low voltage, high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

## MECHANICAL DATA

Case: DFN33A

Molding compound meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meet JESD 201 class 2 whisker test

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)								
PARAMETER	SYMBOL	V7N3M153	UNIT					
Device marking code		7M153						
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	150	V					
Maximum average forward rectified current (fig. 1)	I <sub>F(AV)</sub> <sup>(1)</sup>	7	А					
Maximum average forward rectilied current (lig. 1)	I <sub>F(AV)</sub> <sup>(2)</sup>	2.2	А					
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	120	А					
Operating junction temperature range	T <sub>J</sub> <sup>(3)</sup>	-40 to +175	°C					
Storage temperature range	T <sub>STG</sub>	-55 to +175	°C					

Notes

(1) With infinite heatsink

<sup>(2)</sup> Free air, mounted on FR4 PCB, 2 oz., standard footprint

 $^{(3)}$  The heat generated must be less than the thermal conductivity from junction-to-ambient: dP<sub>D</sub>/dT<sub>J</sub> < 1/R<sub> $\theta$ JA</sub>

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# V7N3M153



www.vishay.com

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ELECTRICAL CHARACTERISTICS (T <sub>J</sub> = 25 °C unless otherwise noted)								
PARAMETER	TEST CO	ONDITIONS	SYMBOL	TYP.	MAX.	UNIT		
	I <sub>F</sub> = 3.5 A	T <sub>J</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.71	-			
Instantaneous forward voltage	$I_F = 7 A$	1j=25 C		0.9	0.98	v		
Instantaneous forward voltage	I <sub>F</sub> = 3.5 A	T <sub>J</sub> = 125 °C		0.56	-	v		
	I <sub>F</sub> = 7 A			0.64	0.69			
	V - 100 V	T <sub>J</sub> = 25 °C T <sub>J</sub> = 125 °C	I <sub>R</sub> <sup>(2)</sup>	0.0012	-	mA		
Reverse current	v <sub>R</sub> = 100 v	T <sub>J</sub> = 125 °C		1.0	-	IIIA		
	V - 150 V	T <sub>J</sub> = 25 °C T <sub>J</sub> = 125 °C	I <sub>R</sub> <sup>(2)</sup>	-	0.07	mA		
	v <sub>R</sub> = 150 v	T <sub>J</sub> = 125 °C		2.5	7			
Typical junction capacitance	4.0 V, 1 MH	4.0 V, 1 MHz		390	-	pF		

Notes

<sup>(1)</sup> Pulse test: 300 µs pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: pulse width  $\leq$  5 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise specified)							
PARAMETER	SYMBOL	TYP.	MAX.	UNIT			
	R <sub>0JA</sub> (1)(2)	118	148				
Thermal resistance	R <sub>0JA</sub> <sup>(3)</sup>	-	65	°C/W			
	R <sub>0JM</sub> <sup>(4)</sup>	2.9	3.63				

#### Notes

<sup>(1)</sup> The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ 

(2) Thermal resistance junction-to-ambient to follow JEDEC® 51-2A, device mounted on FR4 PCB, 2 oz., standard footprint

<sup>(3)</sup> Thermal resistance junction-to-ambient, free air with device mounted on FR4 PCB, 2 oz., 20 mm x 20 mm pad area

<sup>(4)</sup> Thermal resistance junction-to-mount to follow JEDEC 51-14 transient dual interface test method (TDIM)

### **ORDERING INFORMATION TABLE**

Device code	v	7	N3	м	15	3	н	М3	
	1	2	3	4	5	6	7	8	
	1	- Vis	hay TMI	BS prod	uct				
	2	- Current rating (7 = 7 A)							
	3	- Pao	Package type (N3 = DFN33A)						
	4	- Process type option (M = low I <sub>R</sub> )							
	5	- Vol	tage rati	ing (15 =	= 150 V)				
	6	- TM	TMBS generation option (3 = Gen3)						
	7	- Qu	Quality grade (H = AEC-Q101 qualified, otherwise = industry grade Material / environmental category (M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free)						
	8								

ORDERING INFORMATION (Example)									
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE					
V7N3M153-M3/I	0.031	I	6000	13" diameter plastic tape and reel					
V7N3M153HM3/I <sup>(1)</sup>	0.031	I	6000	13" diameter plastic tape and reel					

Note

(1) AEC-Q101 qualified

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# **RATINGS AND CHARACTERISTICS CURVES** (T<sub>A</sub> = 25 °C unless otherwise noted)

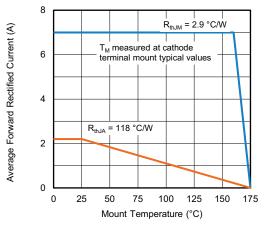


Fig. 1 - Maximum Forward Current Derating Curve

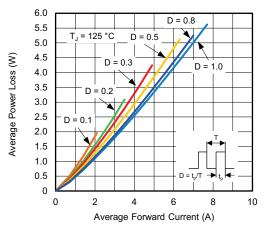


Fig. 2 - Forward Power Loss Characteristics

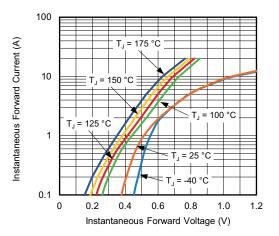


Fig. 3 - Typical Instantaneous Forward Characteristics

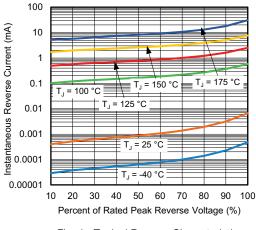


Fig. 4 - Typical Reverse Characteristics

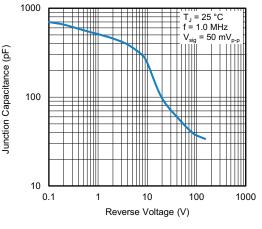


Fig. 5 - Typical Junction Capacitance

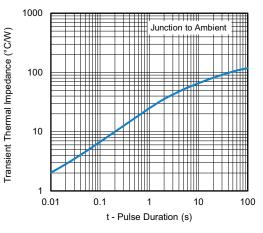


Fig. 6 - Typical Transient Thermal Impedance

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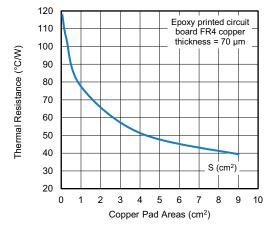


Fig. 7 - Thermal Resistance Junction-to-Ambient vs. Copper Pad Areas

# **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)

Min. 0.005 (0.13) 0.134 (3.40) 0.126 (3.20) 0.039 (0.98) 0.134 (3.40) 0.031 (0.79) 0.126 (3.20) 0.022 (0.55) ref. -Mounting pad layout 0.017 (0.44) 0.011 (0.29) ¥ 0.017 (0.44) 0.094 (2.39) 0.086 (2.19) 0.094 (2.39) 0.030 (0.75) 0.026 (0.65) 0.022 (0.55) 4 0.088 (2.23) - 0.028 (0.70) 0.020 (0.50) 0.028 (0.70) 0.012 (0.30) 0.134 (3.40) 0.088 (2.23) 0.020 (0.50) 0.080 (2.03)

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