Surface-Mount Schottky Barrier Rectifier

SMA-1 (DO-214AC)

Cathode ○—|◀|—○ Anode

LINKS TO ADDITIONAL RESOURCES

Related Documents

PRIMARY CHARACTERISTICS	
$I_{F(AV)}$	1.0 A
V_{RRM}	100 V
I_{FSM}	50 A
V_F at I_F = 1.0 A (T_J = 125 °C)	0.55 V
T_J max.	175 °C
Package	SMA-1 (DO-214AC)
Circuit configuration	Single

FEATURES

- Guardring for overvoltage protection
- Low power losses, high efficiency
- Low forward voltage drop
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available
  - Automotive ordering code; base P/NHM3
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

RoHS COMPLIANT

HALOGEN FREE

TYPICAL APPLICATIONS

For use in low voltage, high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

MECHANICAL DATA

Case: SMA-1 (DO-214AC)
Molding compound meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

Polarity: color band denotes the cathode end

M3 and HM3 suffix meet JESD 201 class 2 whisker test

MAXIMUM RATINGS (T_A = 25 °C unless otherwise noted)			
PARAMETER	**SYMBOL**	**SS1AH10**	**UNIT**
Marking device code (SMA-1)		1HA	
Maximum repetitive peak reverse voltage	V_{RRM}	100	V
Maximum average forward rectified current (fig. 1)	$I_{F(AV)}$ (1)	1	A
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I_{FSM}	50	A
Operating junction temperature range	T_J (2)	-55 to +175	°C
Storage temperature range	T_{STG}	-55 to +175	°C

Notes

(1) Free air, mounted on FR4 PCB, 2 oz., standard footprint

(2) The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$

ELECTRICAL CHARACTERISTICS (T_J = 25 °C unless otherwise noted)						
PARAMETER	**TEST CONDITIONS**		**SYMBOL**	**TYP.**	**MAX.**	**UNIT**
Instantaneous forward voltage	I_F = 1.0 A	T_J = 25 °C	V_F (1)	0.68	0.75	V
	I_F = 2.0 A			0.79	0.86	
	I_F = 1.0 A	T_J = 125 °C		0.55	0.59	
	I_F = 2.0 A			0.62	0.70	
Reverse current	V_R = 100 V	T_J = 25 °C	I_R (2)	-	0.05	mA
		T_J = 125 °C		0.6	1.0	
Typical junction capacitance	4.0 V, 1 MHz		C_J	58	-	pF

Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: pulse width ≤ 5 ms

THERMAL - MECHANICAL SPECIFICATIONS (T_A = 25 °C unless otherwise specified)			
PARAMETER	**SYMBOL**	**TYP.**	**UNIT**
Thermal resistance	$R_{\theta JA}$ (1)(2)	105	°C/W
	$R_{\theta JM}$ (3)	10	

Notes
(1) The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$
(2) Thermal resistance junction-to-ambient to follow JEDEC® 51-2A, device mounted on FR4 PCB, 2 oz., standard footprint
(3) Thermal resistance junction-to-mount to follow JEDEC® 51-14 transient dual interface test method (TDIM)

ORDERING INFORMATION TABLE

Device code

SS	1	A	H	10	H	M3
1	2	3	4	5	6	7

1 - Vishay Semiconductors Planar Schottky surface mount product
2 - Current rating (1 = 1 A)
3 - Package type (A = SMA-1)
4 - Process type option (H = low I_R)
5 - Voltage rating (10 = 100 V)
6 - Quality grade (H = AEC-Q101 qualified, otherwise = industry grade)
7 - Material / Environmental category (M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION (Example)				
PREFERRED P/N	**UNIT WEIGHT (g)**	**PREFERRED PACKAGE CODE**	**BASE QUANTITY**	**DELIVERY MODE**
SS1AH10-M3/IA	0.064	IA	7500	13" diameter plastic tape and reel
SS1AH10HM3/IA (1)	0.064	IA	7500	13" diameter plastic tape and reel

Note
(1) AEC-Q101 qualified

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

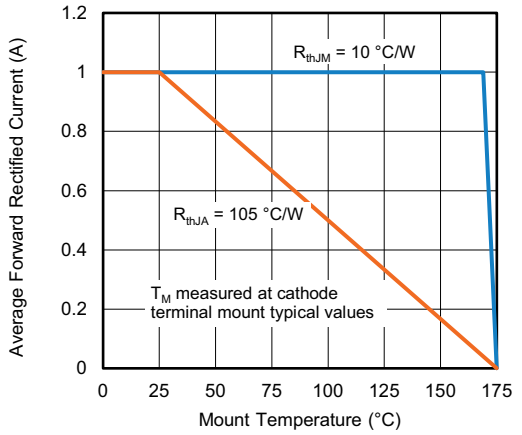


Fig. 1 - Maximum Forward Current Derating Curve

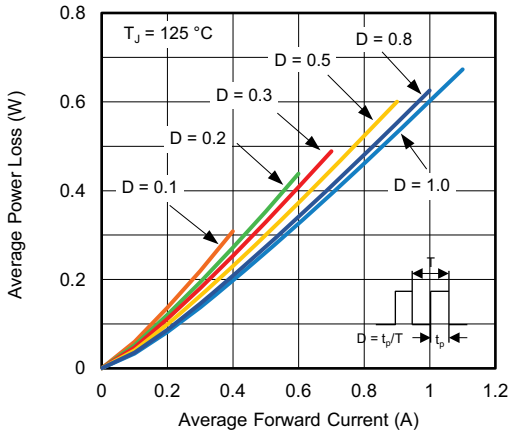


Fig. 2 - Forward Power Loss Characteristics

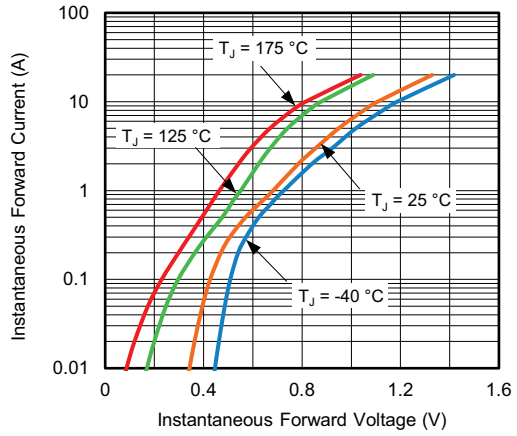


Fig. 3 - Typical Instantaneous Forward Characteristics

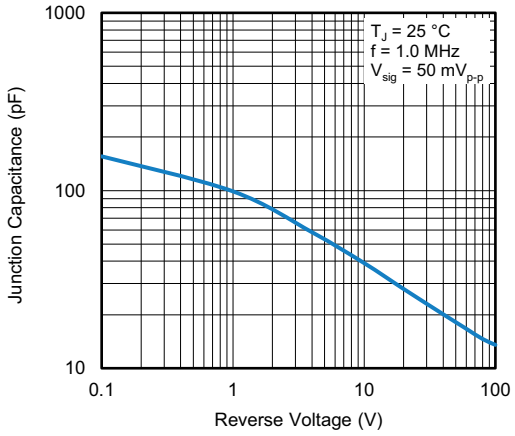


Fig. 5 - Typical Junction Capacitance

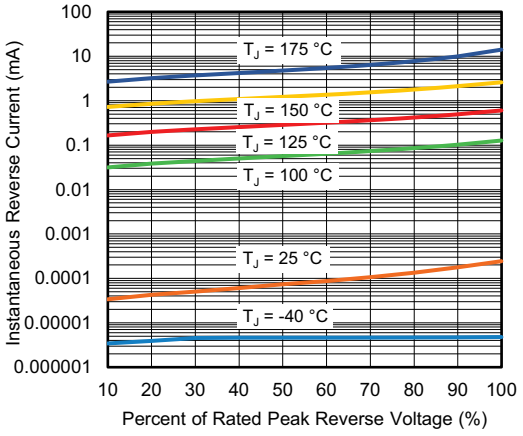


Fig. 4 - Typical Reverse Characteristics

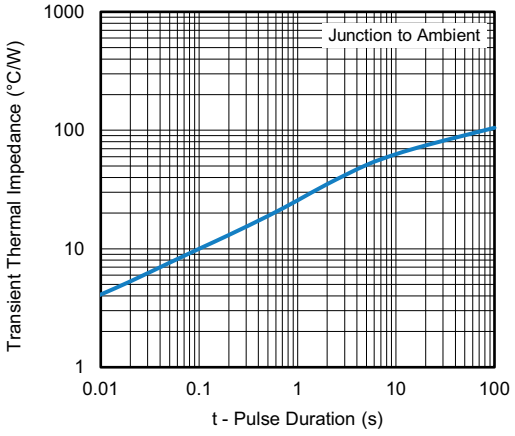


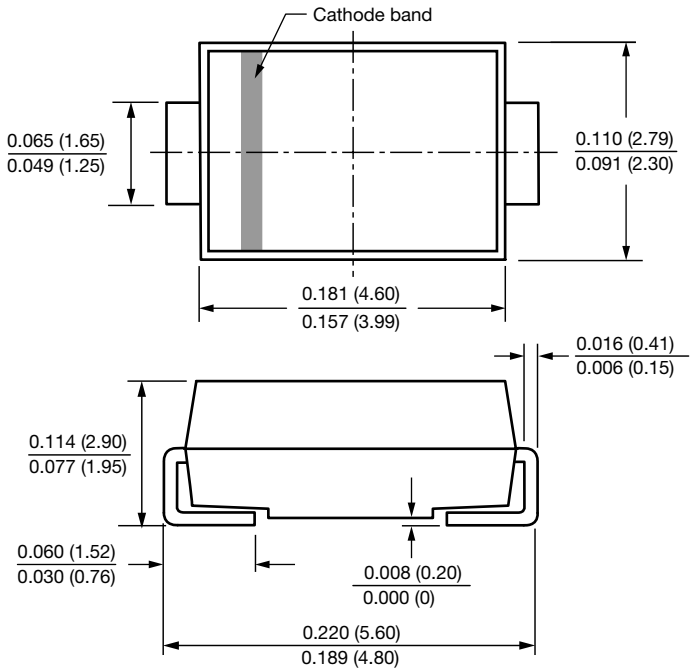
Fig. 6 - Typical Transient Thermal Impedance

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?97292
Part marking information	www.vishay.com/doc?98657
Packaging information	www.vishay.com/doc?98659

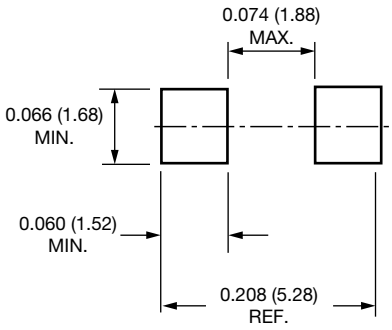
SMA-1 (DO-214AC)

DIMENSIONS in inches (millimeters)

SMA-1 (DO-214AC)



Mounting Pad Layout



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