Vishay General Semiconductor

# **Surface-Mount Glass Passivated Rectifier**



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SMB (DO-214AA)

Cathode O Anode

### LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	2.0 A				
V <sub>RRM</sub>	400 V, 600 V, 800 V, 1000 V				
I <sub>FSM</sub>	70 A				
I <sub>R</sub>	1.0 µA				
$V_F$ at $I_F$ = 2.0 A	0.86 V				
T <sub>J</sub> max.	150 °C				
Package	SMB (DO-214AA)				
Circuit configuration	Single				

#### **FEATURES**

- Low profile package
- Ideal for automated placement
- Glass passivated chip junction
- Low forward voltage drop
- Low leakage current
- · High forward surge capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### **TYPICAL APPLICATIONS**

For use in general purpose rectification of power supplies, inverters, converters and freewheeling diodes for consumer, medical and telecommunication.

### **MECHANICAL DATA**

Case: SMB (DO-214AA)

Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS compliant, commercial grade Base P/N-M3 - halogen-free, RoHS compliant, and commercial grade

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 and M3 suffix meets JESD 201 class 1A whisker test

Polarity: color band denotes the cathode end

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	SYMBOL	S2BG	S2BJ	S2BK	S2BM	UNIT	
Device marking code		2G	2J	2K	2M		
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	400	600	800	1000	V	
Maximum RMS voltage	V <sub>RMS</sub>	280	420	560	700	V	
Maximum DC forward current (fig. 1)		2.0				А	
		1.3				А	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	70				А	
Operating and storage temperature range	$T_J$ <sup>(3)</sup> , $T_{STG}$		-55 to	+150		°C	

Notes

(1) Mounted on 8 mm x 8 mm pad areas, 1 oz. FR4 PCB

<sup>(2)</sup> Free air mounted on recommended copper pad area

 $^{(3)}$  The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ 





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ELECTRICAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage	I <sub>F</sub> = 1.0 A	T - 25 °C	- V <sub>F</sub> <sup>(1)</sup>	0.90	-	v	
	I <sub>F</sub> = 2.0 A	T <sub>J</sub> = 25 °C		0.96	1.1		
	I <sub>F</sub> = 1.0 A	T <sub>.1</sub> = 125 °C		0.78	-		
	I <sub>F</sub> = 2.0 A	13 = 125 C		0.86	1.05		
Reverse current	Rated V <sub>B</sub>	T <sub>J</sub> = 25 °C	- I <sub>R</sub> <sup>(2)</sup>	0.15	1.0	μA	
	naleu v <sub>R</sub>	T <sub>J</sub> = 125 °C		36	125		
Typical reverse recovery time	I <sub>F</sub> = 0.5 A, I <sub>R</sub> = I <sub>rr</sub> = 0.25 A	$I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A}, I_{rr} = 0.25 \text{ A}$		2.0		μs	
Typical junction capacitance	Rated V <sub>B</sub> = 4.	Rated $V_{B} = 4.0 V, 1 MHz$		16		pF	

Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width,  $\leq$  40 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	SYMBOL	S2BG	S2BJ	S2BK	S2BM	UNIT	
Typical thermal resistance	R <sub>0JA</sub> <sup>(1)(2)</sup>	107				°C/W	
Typical thermal resistance	R <sub>0JM</sub> <sup>(3)</sup>	7.2				0/10	

#### Notes

<sup>(1)</sup> The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta,JA}$ 

<sup>(2)</sup> Thermal resistance junction-to-ambient to follow JEDEC<sup>®</sup> 51-2A, device mounted on FR4 PCB, 2 oz. standard footprint

(3) Thermal resistance junction-to-mount to follow JEDEC® 51-14, transient dual interface test method (TDIM)

ORDERING INFORMATION (Example)							
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE			
S2BJ-M3/I	0.096	I	3200	13" diameter plastic tape and reel			
S2BJ-E3/I	0.096	I	3200	13" diameter plastic tape and reel			

### RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)

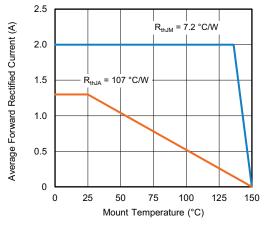


Fig. 1 - Maximum Forward Current Derating Curve

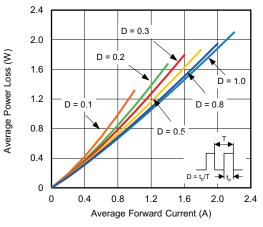


Fig. 2 - Forward Power Loss Characteristics

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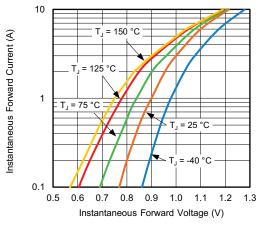


Fig. 3 - Typical Instantaneous Forward Characteristics

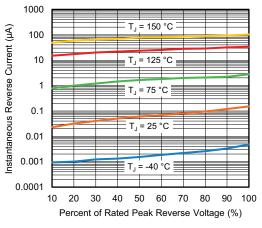
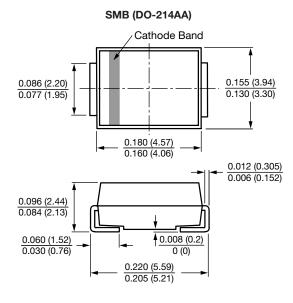


Fig. 4 - Typical Reverse Characteristics

#### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)



## S2BG, S2BJ, S2BK, S2BM

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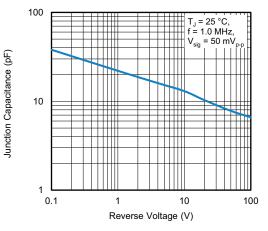


Fig. 5 - Typical Junction Capacitance

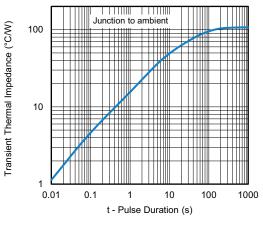
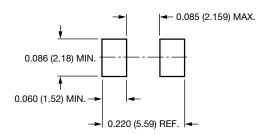


Fig. 6 - Typical Transient Thermal Impedance

#### Mounting Pad Layout



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