SE100PWTLK

Vishay General Semiconductor

Surface-Mount Low V_F Standard Rectifier



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LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I _{F(AV)}	10 A			
V _{RRM}	800 V			
I _{FSM}	240 A			
V_F at I_F = 10 A (T_J = 125 °C)	0.81 V			
T _J max.	175 °C			
Package	SlimDPAK 2L			
Circuit configurations	Single			

FEATURES

- Creepage and clearance distance 2.8 mm minimum
- Very low profile typical height of 1.3 mm
- Ideal for automated placement
- Oxide planar chip junction
- Low forward voltage drop
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

General purpose, power line polarity protection, in both industry and automotive on board charger applications.

MECHANICAL DATA

Case: SlimDPAK 2L

Molding compound meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, RoHS-compliant

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test

Polarity: as marked

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	SE100PWTLK	UNIT	
Device marking code		SE100PWTLK		
Maximum repetitive peak reverse voltage	V _{RRM}	800	V	
Maximum average forward rectified current (Fig. 1)	I _{F(AV)} ⁽¹⁾	10	A	
	I _{F(AV)} ⁽²⁾	2.6		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	240	А	
Operating junction and storage temperature range	T _J , T _{STG}	-55 to +175	°C	

Notes

⁽¹⁾ With infinite heatsink

⁽²⁾ Free air, mounted on recommended copper pad area

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HALOGEN

FREE





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ELECTRICAL CHARACTERISTICS (T_J = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Maximum Instantaneous forward voltage	$I_F = 5.0 \text{ A}$	T _J = 25 °C	V _F (1)	0.85	-	V
	I _F = 10.0 A			0.93	1.0	
	I _F = 5.0 A	T _J = 125 °C		0.72	-	
	I _F = 10.0 A			0.81	0.88	
Reverse current	Rated V _B	T _J = 25 °C	I _R ⁽²⁾	-	5	μA
	hated v _R	T _J = 125 °C		14	50	
Typical reverse recovery time	$I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A}, I_{rr} = 0.25 \text{ A}$		t _{rr}	380	-	ns
Typical junction capacitance	4.0 V, 1 MHz		CJ	80	-	pF

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: pulse width \leq 40 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER	SYMBOL	TYP.	MAX.	UNIT	
Typical thermal resistance	R _{0JA} (1)(2)	81	94	°C/W	
	R _{0JM} ⁽³⁾	1.1	2	0/10	

Notes

⁽¹⁾ The heat generated must be less than thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$

(2) Thermal resistance junction to ambient to follow JEDEC® 51-2A, device mounted on FR4 PCB, 2 oz., standard footprint

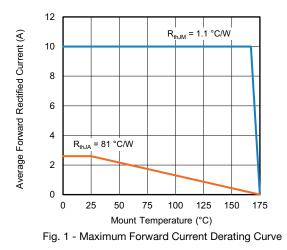
(3) Thermal resistance junction-to-mount to follow JEDEC® 51-14 transient dual interface test method (TDIM)

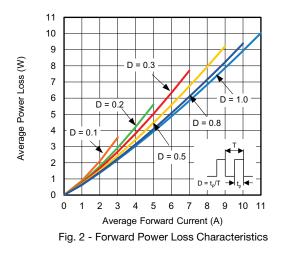
ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g) PREFERRED PACKAGE CODE		BASE QUANTITY	DELIVERY MODE	
SE100PWTLK-M3/I	0.185	l	4500	13" diameter plastic tape and reel	

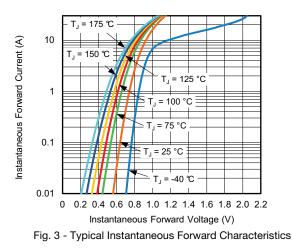


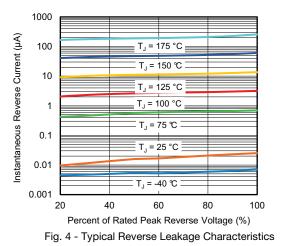
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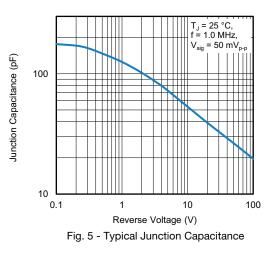
RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

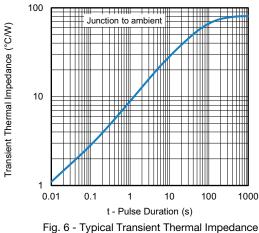












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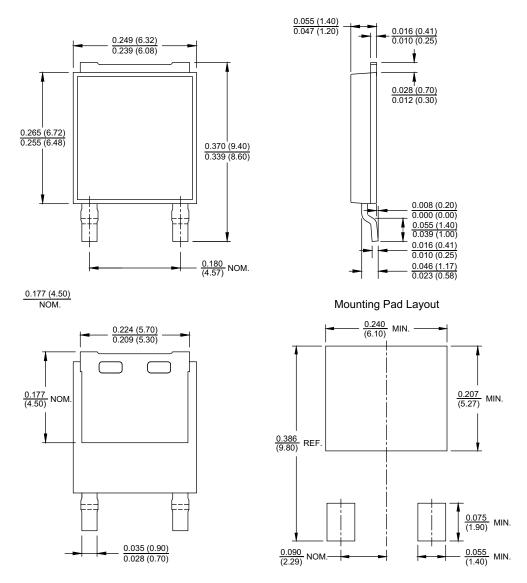
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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

SlimDPAK 2L



Note

• The suggested mounting pad layout is provided for reference only, as actual pad layouts may vary depending on application



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