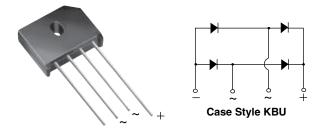
KBU8A, KBU8B, KBU8D, KBU8G, KBU8J, KBU8K, KBU8M



Vishay General Semiconductor

# **Glass Passivated Single-Phase Bridge Rectifier**



## LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS							
Package	KBU						
I <sub>F(AV)</sub>	8 A						
V <sub>RRM</sub>	50 V, 100 V, 200 V, 400 V, 600 V, 800 V, 1000 V						
I <sub>FSM</sub>	300 A						
$V_F$ at $I_F = 8 A$	0.91 V						
T <sub>J</sub> max.	150 °C						
Circuit configuration	In-line						

## **FEATURES**

- UL recognition, file number E54214
- Ideal for printed circuit boards
- High surge current capability
- Glass passivated pellet chip junction
- High case dielectric strength of 1500  $V_{\text{RMS}}$
- Solder dip 275 °C max. 10 s, per JESD 22-B106
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

## **TYPICAL APPLICATIONS**

General purpose use in AC/DC bridge full wave rectification for monitor, TV, printer, SMPS, adapter, audio equipment, and home appliances applications.

### **MECHANICAL DATA**

#### Case: KBU

Molding compound meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, RoHS-compliant, commercial grade

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked on body

Mounting Torque: 10 cm-kg (8.8 inches-lbs) max.

Recommended Torque: 5.7 cm-kg (5 inches-lbs)

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)										
PARAMETER		SYMBOL	KBU8A	KBU8B	KBU8D	KBU8G	KBU8J	KBU8K	KBU8M	UNIT
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	50	100	200	400	600	800	1000	V
Maximum RMS voltage		V <sub>RMS</sub>	35	70	140	280	420	560	700	V
Maximum DC blocking voltage		V <sub>DC</sub>	50	100	200	400	600	800	1000	V
Maximum average forward	$T_{C} = 132 \ ^{\circ}C \ ^{(1)}$		8.0							А
rectified output current at $T_A = 31 \text{ °C}^{(2)}$		I <sub>F(AV)</sub>	8.0							
Peak forward surge current single sine-wave superimposed on rated load		I <sub>FSM</sub>	300							А
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	-50 to +150							°C

#### Notes

<sup>(1)</sup> Units mounted on infinite heatsink

(2) Units mounted in free air, no heatsink, PCB at 0.375" (9.5 mm) lead length with 0.5" x 0.5" (12 mm x 12 mm) copper pads

<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25$ °C unless otherwise noted)										
PARAMETER	TEST CONDITIONS	SYMBOL	KBU8A	KBU8B	KBU8D	KBU8G	KBU8J	KBU8K	KBU8M	UNIT
Maximum instantaneous	I <sub>F</sub> = 4.0 A	VF	0.87 (typ.)							v
forward drop per diode	I <sub>F</sub> = 8.0 A	۷F	0.98							v
Maximum DC reverse current at rated DC blocking	T <sub>J</sub> = 25 °C	1_	10						μA	
voltage per diode	T <sub>J</sub> = 125 °C	IR	0.5						mA	

Revision: 24-Jan-2025

Document Number: 98733

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COMPLIANT

HALOGEN

FREE

KBU8A, KBU8B, KBU8D, KBU8G, KBU8J, KBU8K, KBU8M

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<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)									
PARAMETER	SYMBOL	KBU8A KBU8B KBU8D KBU8G KBU8J KBU8K KBU8M UNIT							
Typical thermal resistance	R <sub>0JA</sub> <sup>(1)</sup>	17							°C/W
Typical thermal resistance	R <sub>0JC</sub> <sup>(2)</sup>	2.5							0/00

#### Notes

(1) Units mounted in free air, no heatsink, PCB at 0.375" (9.5 mm) lead length with 0.5" x 0.5" (12 mm x 12 mm) copper pads

<sup>(2)</sup> Thermal resistance junction-to-case to follow JEDEC<sup>®</sup> 51-14 transient dual interface test method (TDIM)

ORDERING INFORMATION (Example)								
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE				
KBU8J-M3/P	6.9	Р	20	Tube				
KBU8J-M3/A	6.9	А	250	Anti-static PVC tray				

## RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)

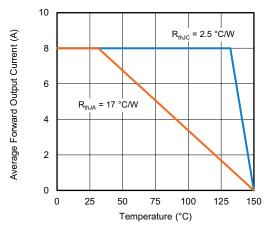


Fig. 1 - Derating Curve Output Rectified Current

T<sub>J</sub> = 150 °C

8.3 ms single half sine-wave

100

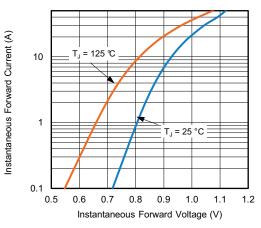


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

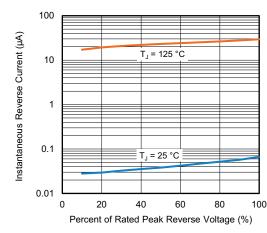


Fig. 4 - Typical Reverse Leakage Characteristics Per Diode

Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current

10

Number of Cycles at 60 Hz

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300

250

200

150

100

50

0

Peak Forward Surge Current (A)

2

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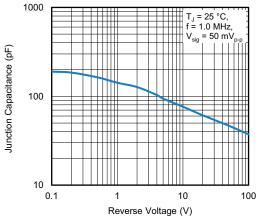
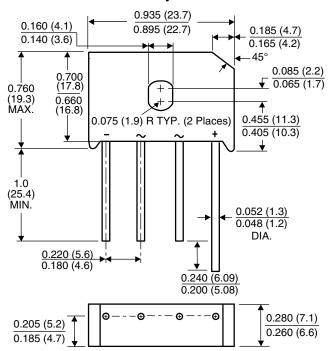


Fig. 5 - Typical Junction Capacitance Per Diode

### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)



**Case Style KBU** 



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Revision: 01-Jan-2025

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