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Vishay Semiconductors

Surface-Mount Schottky Barrier Rectifier



SMB-1 (DO-214AA)

Cathode O Anode

LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS			
I _{F(AV)}	2.0 A		
V _{RRM}	40 V		
I _{FSM}	75 A		
V _F	0.5 V		
T _J max.	150 °C		
Package	SMB-1 (DO-214AA)		
Circuit configuration	Single		

FEATURES

- · Guardring for overvoltage protection
- · Low power losses, high efficiency
- · Low forward voltage drop
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- n HALOGEN

COMPLIANT

- AEC-Q101 qualified available
 - Automotive ordering code; base P/NHM3
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in low voltage, high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

MECHANICAL DATA

Case: SMB-1 (DO-214AA)

Molding compound meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, RoHS-compliant, and

commercial grade

Base P/NHM3_X - halogen-free, RoHS-compliant, and

AEC-Q101 qualified

("X" denotes revision code e,g, A, B,...)

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

Polarity: color band denotes the cathode end

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	SS24	UNIT	
Device marking code		S4		
Maximum repetitive peak reverse voltage	V _{RRM}	40	V	
Maximum average forward rectified current (fig. 1)	I _{F(AV)} (1)	2.0	A	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	75	А	
Operating junction temperature range	T _J ⁽²⁾	-55 to +150	°C	
Storage temperature range	T _{STG}	-55 to +150	°C	

Notes

- (1) Free air, mounted on FR4 PCB, 2 oz., standard footprint
- $^{(2)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$

ELECTRICAL CHARACTERISTICS (T _J = 25 °C unless otherwise noted)					
PARAMETER	TEST CONDITIONS		SYMBOL	MAX.	UNIT
Instantaneous forward voltage	I _F = 2.0 A	T _J = 25 °C	V _F ⁽¹⁾	0.5	V
Reverse current	V = 40 V	V _B = 40 V	I _R ⁽²⁾	0.4	- mA
Reverse current	v _R = 40 v	T _J = 100 °C		10	

Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: pulse width ≤ 5 ms



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THERMAL - MECHANICAL SPECIFICATIONS (T _A = 25 °C unless otherwise specified)			
PARAMETER	SYMBOL	TYP.	UNIT
Thermal resistance	R ₀ JA (1)(2)	90	°C/W
	R _{eJM} (3)	9	C/VV

Notes

- (1) The heat generated must be less than the thermal conductivity from junction-to-ambient: dPp/dTJ < 1/R_{6JA}
- (2) Thermal resistance junction-to-ambient to follow JEDEC® 51-2A, device mounted on FR4 PCB, 2 oz., standard footprint
- (3) Thermal resistance junction-to-mount to follow JEDEC® 51-14 transient dual interface test method (TDIM)

ORDERING INFORMATION (Example)				
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE
SS24-M3/IA	0.096	IA	3200	13" diameter plastic tape and reel
SS24HM3_B/IA (1)	0.096	IA	3200	13" diameter plastic tape and reel

Note

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

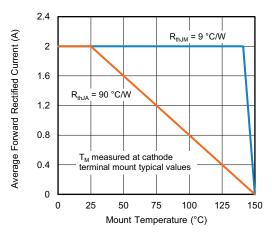


Fig. 1 - Maximum Forward Current Derating Curve

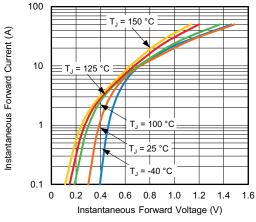


Fig. 3 - Typical Instantaneous Forward Characteristics

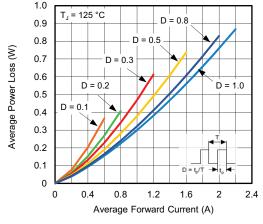


Fig. 2 - Forward Power Loss Characteristics

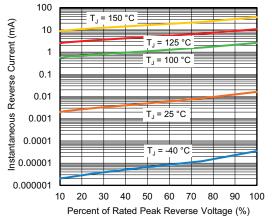


Fig. 4 - Typical Reverse Characteristics

⁽¹⁾ AEC-Q101 qualified



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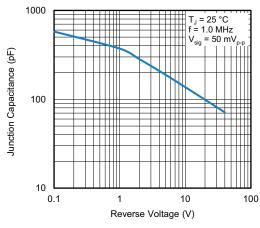


Fig. 5 - Typical Junction Capacitance

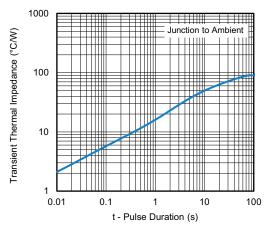
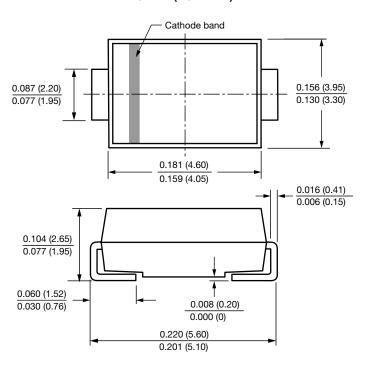
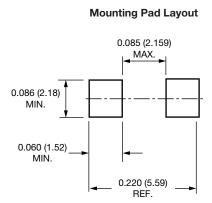


Fig. 6 - Typical Transient Thermal Impedance

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

SMB-1 (DO-214AA)





LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?97362
Part marking information	www.vishay.com/doc?98657
Packaging information	www.vishay.com/doc?98659

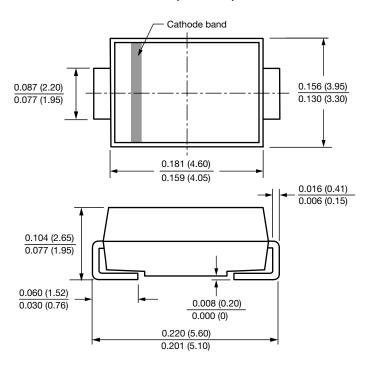


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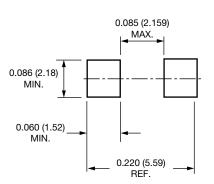
SMB-1 (DO-214AA)

DIMENSIONS in inches (millimeters)

SMB-1 (DO-214AA)



Mounting Pad Layout





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