AUTOMOTIVE GRADE

Available

RoHS

COMPLIANT

HALOGEN FREE



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Vishay General Semiconductor

Low Noise High Current Density Surface-Mount TMBS® (Trench MOS Barrier Schottky) Rectifier

Ultra Low $V_F = 0.35 \text{ V}$ at $I_F = 2.5 \text{ A}$



LINKS TO ADDITIONAL RESOURCES







PRIMARY CHARACTERISTICS				
I _{F(AV)}	5 A			
V_{RRM}	60 V			
I _{FSM}	160 A			
V_F at $I_F = 5$ A $(T_J = 125 °C)$	0.44 V			
T _J max.	150 °C			
Package	SMPC (TO-277A)			
Circuit configuration	Single			

FEATURES

- Very low profile typical height of 1.1 mm
- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- High efficiency operation
- · Low reverse spike voltage
- Very low junction capacitance
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available
 - Automotive ordering code; base P/NHM3
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in low voltage high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

MECHANICAL DATA

Case: SMPC (TO-277A)

Molding compound meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified **Terminals:** matte tin plated leads, solderable per

Terminals: matte tin plated J-STD-002 and JESD 22-B102

M3 and HM3 suffix meets JESD 201 class 2 whisker test

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)			
PARAMETER	SYMBOL	V5PL64	UNIT
Device marking code		5L64	
Maximum repetitive peak reverse voltage	V _{RRM}	60	V
Maximum average forward rectified current (fig. 1)	I _{F(AV)} (1)	5 3.6	
	I _{F(AV)} (2)		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	160	А
Operating junction temperature range	T _J ⁽³⁾	-40 to +150	°C
Storage temperature range	T _{STG}	-55 to +150	°C

Notes

- (1) Mounted on 30 mm x 30 mm pad areas aluminum PCB
- (2) Free air, mounted on recommended copper pad area
- $^{(3)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$



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ELECTRICAL CHARACTERISTICS (T _J = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I _F = 2.5 A	T _J = 25 °C	V _F ⁽¹⁾	0.44	-	V
	I _F = 5 A			0.50	0.56	
	I _F = 2.5 A	T _J = 125 °C		0.35	-	
	I _F = 5 A			0.44	0.50	
Reverse current	V _R = 60 V	T _J = 25 °C T _J = 125 °C	1 (2)	0.07	mA	
	v _R = 00 v		IR (=)	4.5	10	l IIIA
Typical junction capacitance	4.0 V, 1 MHz		CJ	400	-	pF

Notes

(4) Pulse test: 300 µs pulse width, 1 % duty cycle

(5) Pulse test: pulse width ≤ 5 ms

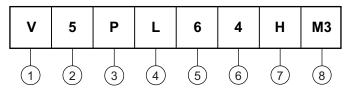
THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)				
PARAMETER SYMBOL V5PL64				
Tuning the word registered	R ₀ JA (1)(2)	80	°C/W	
Typical thermal resistance	R _{0JM} (3)	4	C/VV	

Notes

- $^{(6)}$ The heat generated must be less than the thermal conductivity from junction to ambient: $dP_D/dT_J < 1/R_{\theta JA}$
- (7) Free air mounted on recommended copper pad area; thermal resistance R_{0JA} junction to ambient
- $^{(8)}$ Mounted on 30 mm x 30 mm aluminum PCB; thermal resistance $R_{\theta JM}$ junction to mount

ORDERING INFORMATION TABLE





- 1 Vishay TMBS product
- 2 Current rating (5 = 5 A)
- 3 Package type (P = SMPC (TO-277A))
- Process type option (L = low V_F)
- 5 Voltage rating (6 = 60 V)
- 6 TMBS generation option (4 = gen 4)
- 7 Quality grade (H = AEC-Q101 qualified, = industry grade)
- Material / Environment category
 (M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free)

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V5PL64-M3/H	0.10	Н	1500	7" diameter plastic tape and reel	
V5PL64-M3/I	0.10	I	6500	13" diameter plastic tape and reel	
V5PL64HM3/H (1)	0.10	Н	1500	7" diameter plastic tape and reel	
V5PL64HM3/I (1)	0.10	ı	6500	13" diameter plastic tape and reel	

Note

(1) AEC-Q101 qualified

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RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

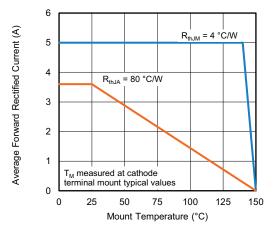


Fig. 1 - Forward Current Derating Curve

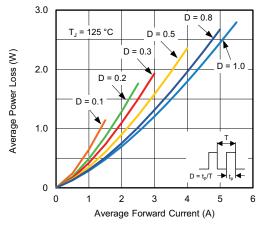


Fig. 2 - Forward Power Loss Characteristics

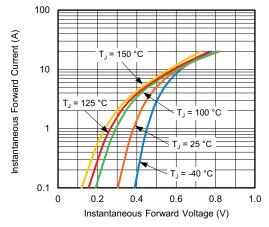


Fig. 3 - Typical Instantaneous Forward Characteristics

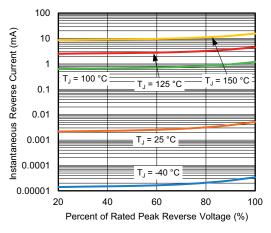


Fig. 4 - Typical Reverse Leakage Characteristics Per Diode

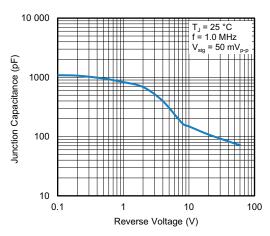


Fig. 5 - Typical Junction Capacitance

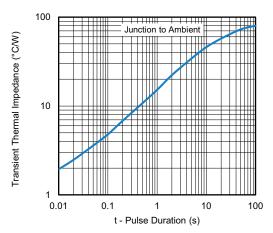
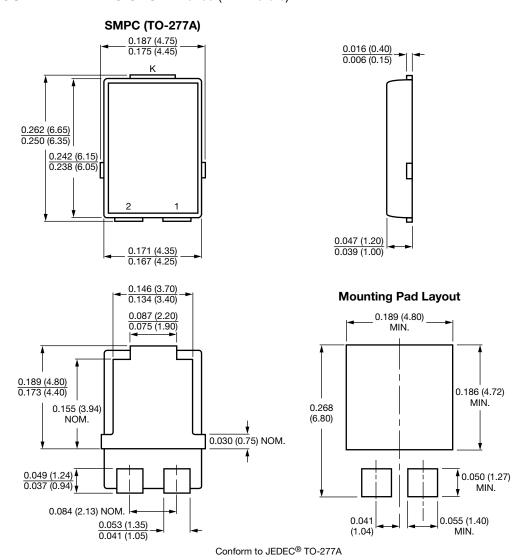


Fig. 6 - Typical Transient Thermal Impedance



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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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