COMPLIANT

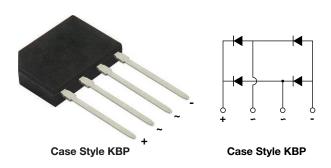
HALOGEN

FREE



## Vishay General Semiconductor

# Low V<sub>F</sub> Single-Phase Single In-Line Bridge Rectifier



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	4 A				
$V_{RRM}$	800 V				
I <sub>FSM</sub>	130 A				
$V_F$ at $I_F = 2 \text{ A (T}_J = 125 °\text{C)}$	0.68 V				
T <sub>J</sub> max.	175 °C				
Package	KBP				
Circuit configuration	In-line				

### **FEATURES**

- UL recognition file number E54214
- Oxide planar chip junction
- Low forward voltage drop
- Solder dip 275 °C max. 10 s, per JESD 22-B106
- Ideal for printed circuit boards
- · High surge current capability
- High case dielectric strength of 1500 V<sub>RMS</sub>
- Material categorization: for definitions of compliance please see <a href="https://www.vishav.com/doc?99912"><u>www.vishav.com/doc?99912</u></a>

### TYPICAL APPLICATIONS

General purpose use in AC/DC bridge full wave rectification for electronic devices, fast charger, adapter, and home appliances applications.

### **MECHANICAL DATA**

Case: KBP

Molding compound meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, RoHS-compliant, and industrial grade

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD22-B102

M3 suffix meet JESD 201 class 1A whisker test

Polarity: as marked on body

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER		SYMBOL	KBPE0480	UNIT		
Device marking code			KBPE0480			
Maximum repetitive peak reverse voltage		$V_{RRM}$	800	V		
Maximum RMS voltage		$V_{RMS}$	560	V		
Maximum DC blocking voltage		$V_{DC}$	800	V		
Maximum average forward rectified output current at	T <sub>C</sub> = 150 °C	I <sub>O</sub> <sup>(1)</sup>	4	A		
	T <sub>A</sub> = 25 °C	I <sub>O</sub> <sup>(2)</sup>	2.1			
Non-repetitive peak forward surge current 8.3 ms single sine-wave, T <sub>J</sub> = 25 °C		I <sub>FSM</sub>	130	Α		
Non-repetitive peak forward surge current 1.0 ms single sine-wave, $T_J = 25$ $^{\circ}$ C		I <sub>FSM</sub>	260	Α		
Rating for fusing (t < 8.3 ms)		l <sup>2</sup> t	70	A <sup>2</sup> s		
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +175	°C		

### Notes

- (1) Unit case mounted on 15 x 5.7 x 5.5 cm heatsink
- (2) Units mounted on PCB without heatsink



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Maximum instantaneous forward voltage drop	I <sub>F</sub> = 2 A	T <sub>J</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.82	0.9	V
per diode	IF = Z A	T <sub>J</sub> = 125 °C	<b>v</b> F \.,	0.68	-	V
Maximum DC reverse current at rated DC		T <sub>J</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	0.03	5	
blocking voltage per diode	$V_{R} = 800 \text{ V}$	T <sub>J</sub> = 125 °C	IR (−/	17.5	-	μΑ
Typical reverse recovery time	$I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A}, I_{rr} = 0.25 \text{ A}$		t <sub>rr</sub>	250	-	ns
Typical junction capacitance per diode	4.0 V, 1 MHz		CJ	70	-	pF

### **Notes**

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL KBPE0480		UNIT	
Typical thermal registance	$R_{\theta JA}^{(1)(2)}$	50	°C/W	
Typical thermal resistance	R <sub>0</sub> JC (3)	3.7	C/VV	

#### **Notes**

- (1) The heat generated must be less than the thermal conductivity from junction-to-ambient: dPp/dTJ < 1/ReJA
- (2) Thermal resistance junction-to-ambient to follow JEDEC® 51-2A, device mounted on FR4 PCB, 2 oz.
- (3) Thermal resistance junction-to-mount to follow JEDEC® 51-14 transient dual interface test method (TDIM)

ORDERING INFORMATION					
PREFERRED P/N	UNIT WEIGHT (g) PREFERRED PACKAGE CODE		BASE QUANTITY	DELIVERY MODE	
KBPE0480-M3/P	1.42	Р	25	Tube	

## RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)

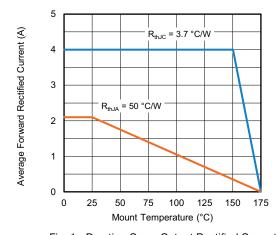


Fig. 1 - Derating Curve Output Rectified Current

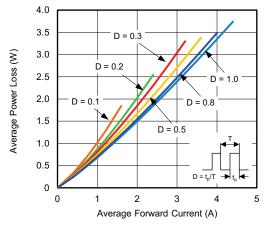


Fig. 2 - Forward Power Loss Characteristics Per Diode



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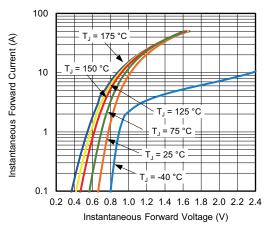


Fig. 3 - Typical Forward Characteristics Per Diode

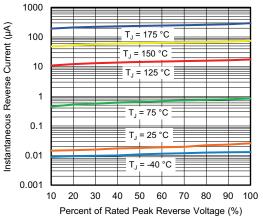


Fig. 4 - Typical Reverse Leakage Characteristics Per Diode

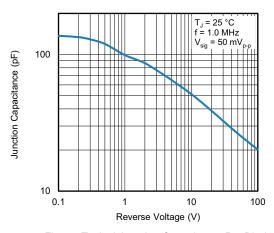


Fig. 5 - Typical Junction Capacitance Per Diode

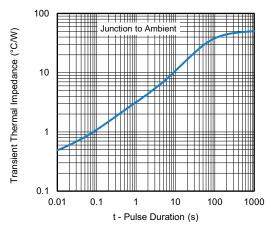
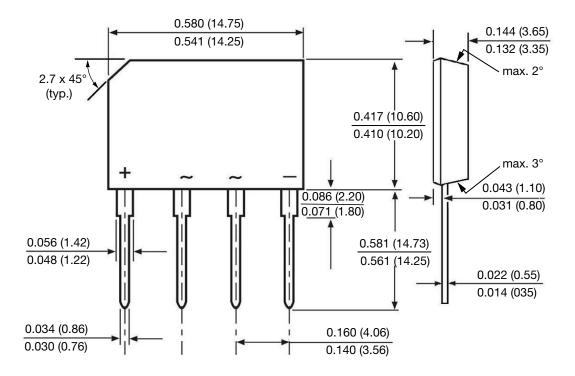


Fig. 6 - Typical Transient Thermal Impedance Per Diode

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## **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)

## **KBP**





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