ROHS

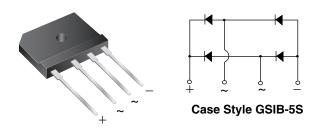
HALOGEN

FREE



Vishay General Semiconductor

Low V_F Single-Phase Single In-Line Bridge Rectifiers



LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS			
I _{F(AV)} ⁽¹⁾	35 A		
V_{RRM}	600 V		
I _{FSM}	400 A		
I _R	10 μA		
V _F at I _F = 17.5 A, T _J = 125 °C	0.75 V		
T _J max.	175 °C		
Package	GSIB-5S		
Circuit configuration	In-line		

FEATURES

- UL recognition file number E312394
- Thin single in-line package
- Oxide planar chip junction
- Low forward voltage drop
- · High surge current capability
- · Low noise
- High case dielectric strength of 2500 V_{RMS}, 1 minute
- Solder dip 275 °C max. 10 s, per JESD 22-B106
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

General purpose use in AC/DC bridge full wave rectification for switching power supply, home appliances, and white-goods applications specially for telecom power supply, high efficiency desktop PC, and server SMPS.

MECHANICAL DATA

Case: GSIB-5S

Epoxy meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, RoHS-compliant, and

commercial grade

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked on body

Mounting Torque: 10 cm-kg (8.8 in-lbs) maximum **Recommended Torque:** 5.7 cm-kg (5 in-lbs)

PARAMETER		SYMBOL	LVE3560E	UNIT
Marking code			LVE3560E	
Maximum repetitive peak reverse voltage		V _{RRM}	600	V
Maximum RMS voltage		V _{RMS}	420	V
Maximum DC blocking voltage		V _{DC}	600	V
Maximum average forward rectified output current at	T _C = 87.7 °C	I _O ⁽¹⁾	35	Α
	T _A = 25 °C	I _O ⁽²⁾	5.0	A
Non-repetitive peak forward surge current 8.3 ms single sine-wave, $T_J = 25$ °C		I _{FSM}	400	А
Rating for fusing (t < 8.3 ms), $T_J = 25$ °C		l ² t	660	A ² s
Operating junction and storage temperature range		T _J , T _{STG}	-55 to +175	°C

Notes

- (1) With heatsink, 50 Hz sine wave, resistance load
- (2) Units mounted on PCB without heatsink



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ELECTRICAL CHARACTERISTICS (T _J = 25 °C unless otherwise noted)						
PARAMETER	TEST CO	TEST CONDITIONS		TYP.	MAX.	UNIT
Instantaneous forward voltage	I _E = 17.5 A	T _J = 25 °C	V _F ⁽¹⁾	0.87	0.90	V
	I _F = 17.5 A	T _J = 125 °C		0.75	-	
Reverse current per diode	V 600 V	T _J = 25 °C	I _R ⁽²⁾	0.03	10	μΑ
	V _R = 600 V	T _J = 125 °C		23	-	
Typical reverse recovery time	I _F = 0.5 A, I _R =	I _F = 0.5 A, I _R = 1.0 A, I _{rr} = 0.25 A		275	-	ns
Typical junction capacitance	4.0 V, 1 MHz		CJ	290	-	pF

Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)			
PARAMETER	SYMBOL	LVE3560E	UNIT
Maximum thermal resistance	$R_{\theta JA}^{(1)(2)}$	22	°C/W
	R ₀ JC (3)	1.3]

Notes

- ⁽¹⁾ The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$
- (2) Thermal resistance junction-to-ambient to follow JEDEC® 51-2A, device mounted on FR4 PCB, 2 oz.
- (3) Thermal resistance junction-to-caseto follow JEDEC® 51-14 transient dual interface test method (TDIM)

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
LVE3560E-M3/P	6.9	Р	20	Tube		

RATINGS AND CHARACTERISTICS CURVES ($T_A = 25$ °C unless otherwise noted)

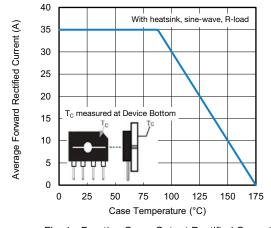


Fig. 1 - Derating Curve Output Rectified Current

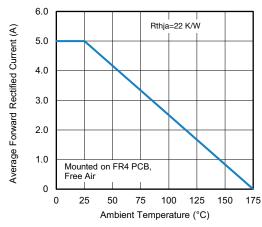


Fig. 2 - Forward Current Derating Curve



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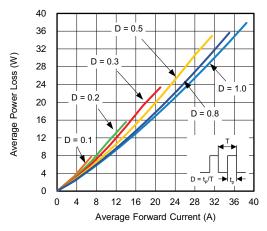


Fig. 3 - Forward Power Dissipation

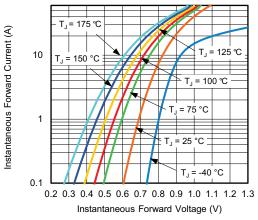


Fig. 4 - Typical Forward Characteristics Per Diode

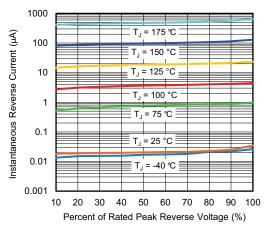


Fig. 5 - Typical Reverse Characteristics Per Diode

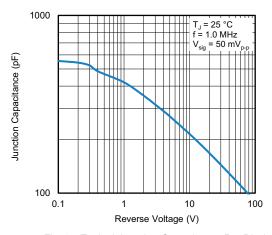


Fig. 6 - Typical Junction Capacitance Per Diode

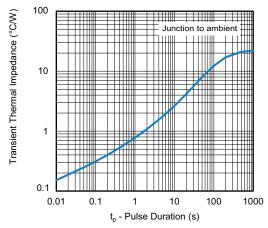
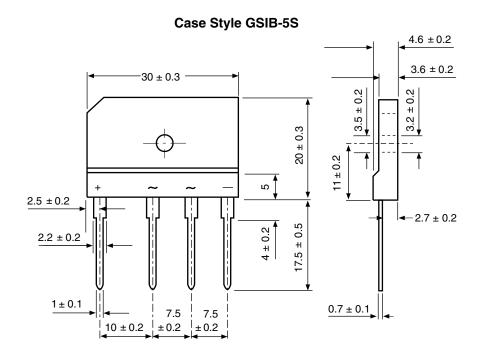


Fig. 7 - Typical Transient Thermal Impedance



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PACKAGE OUTLINE DIMENSIONS in millimeters





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