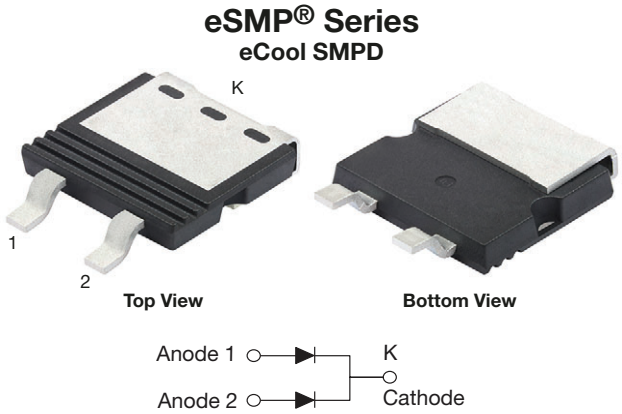
Dual High-Voltage TMBS® (Trench MOS Barrier Schottky) Rectifier

Ultra Low V_F = 0.51 V at I_F = 5.0 A



FEATURES

- Top side cool
- Trench MOS Schottky technology
- Ideal for automated placement
- Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available:
  - Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

AUTOMOTIVE GRADE Available

RoHS COMPLIANT

HALOGEN FREE

LINKS TO ADDITIONAL RESOURCES

3D Models | Marking | Packages

TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection in commercial, industrial, and automotive application.

PRIMARY CHARACTERISTICS	
$I_{F(AV)}$	2 x 30 A
V_{RRM}	200 V
I_{FSM}	430 A
V_F at I_F = 30 A (T_J = 125 °C)	0.70 V
T_J max.	150 °C
Package	eCool SMPD
Circuit configuration	Common cathode

MECHANICAL DATA

Case: eCool SMPD

Molding compound meets UL 94 V-0 flammability rating
Base P/N-M3 - halogen-free, RoHS-compliant
Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meet JESD 201 class 2 whisker test

Polarity: as marked

MAXIMUM RATINGS (T_A = 25 °C unless otherwise noted)

PARAMETER		SYMBOL	V60C202C	UNIT
Device marking code			V60C202C	
Maximum repetitive peak reverse voltage		V_{RRM}	200	V
Maximum average forward rectified current (fig. 1)	per device	$I_{F(AV)}$ (1)	60	A
	per diode	$I_{F(AV)}$ (1)	30	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I_{FSM}	430	A
Operating junction temperature range		T_J (2)	-40 to +150	°C
Storage temperature range		T_{STG}	-55 to +150	

Notes

(1) With infinite heatsink

(2) The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$

ELECTRICAL CHARACTERISTICS (T_J = 25 °C unless otherwise noted)						
PARAMETER	**TEST CONDITIONS**		**SYMBOL**	**TYP.**	**MAX.**	**UNIT**
Instantaneous forward voltage per diode	I_F = 5 A	T_J = 25 °C	V_F (1)	0.67	-	V
	I_F = 15 A			0.77	-	
	I_F = 30 A			0.83	0.88	
	I_F = 5 A	T_J = 125 °C		0.51	-	
	I_F = 15 A			0.62	-	
	I_F = 30 A			0.70	0.75	
Reverse current at rated V_R per diode	V_R = 160 V	T_J = 25 °C	I_R (2)	0.003	-	mA
		T_J = 125 °C		3	-	
	V_R = 200 V	T_J = 25 °C		-	0.15	
		T_J = 125 °C		7	25	
Typical junction capacitance per diode	4.0 V, 1 MHz		C_J	1300	-	pF

Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 5 ms

THERMAL CHARACTERISTICS (T_A = 25 °C unless otherwise noted)				
PARAMETER	**SYMBOL**	**TYP.**	**MAX.**	**UNIT**
Typical thermal resistance	$R_{\theta JA}$ (1)(2)	26.5	34	°C/W
	$R_{\theta JC}$ (3)	0.8	1.0	

Notes

(1) The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$

(2) Thermal resistance junction-to-ambient to follow JEDEC® 51-2A, device mounted on FR4 PCB, 2 oz. standard footprint with top-side aluminum cooling heatsink size L 35 mm x W 20 mm x H 10 mm

(3) Thermal resistance junction-to-top case to follow JEDEC® 51-14 transient dual interface test method (TDIM)

ORDERING INFORMATION TABLE

Device code

V	60	C	20	2	C	H	M3
1	2	3	4	5	6	7	8

1 - Vishay TMBS product

2 - Current rating (60 = 2 x 30 A)

3 - Package type (C = eCool SMPD package)

4 - Voltage rating (20 = 200 V)

5 - TMBS generation option (2 = Gen 2)

6 - Circuit configuration (C = Common cathode)

7 - Quality grade (H = AEC-Q101 qualified, otherwise = industry grade)

8 - Material / environmental category
(M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free)

ORDERING INFORMATION (Example)				
PREFERRED P/N	**UNIT WEIGHT (g)**	**PACKAGE CODE**	**BASE QUANTITY**	**DELIVERY MODE**
V60C202C-M3/I	0.66	I	2000/reel	13" diameter plastic tape and reel
V60C202CHM3/I (1)	0.66	I	2000/reel	13" diameter plastic tape and reel

Note

(1) AEC-Q101 qualified

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

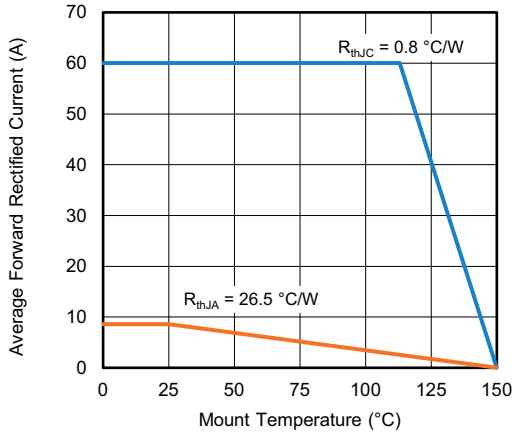


Fig. 1 - Maximum Forward Current Derating Curve

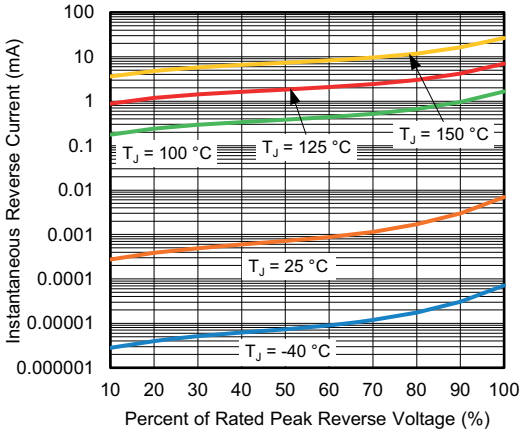


Fig. 4 - Typical Reverse Leakage Characteristics Per Diode

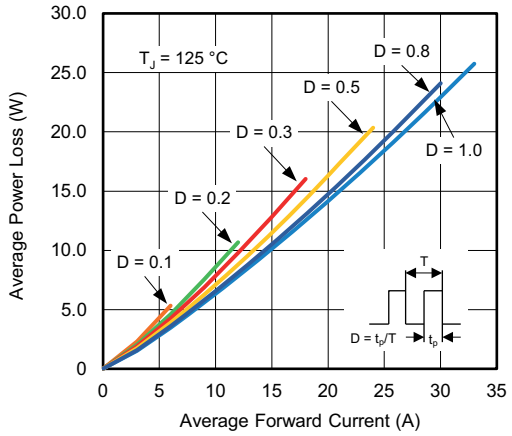


Fig. 2 - Average Power Loss Characteristics Per Diode

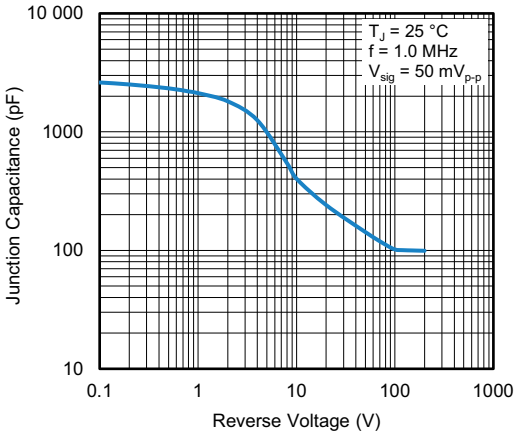


Fig. 5 - Typical Junction Capacitance Per Diode

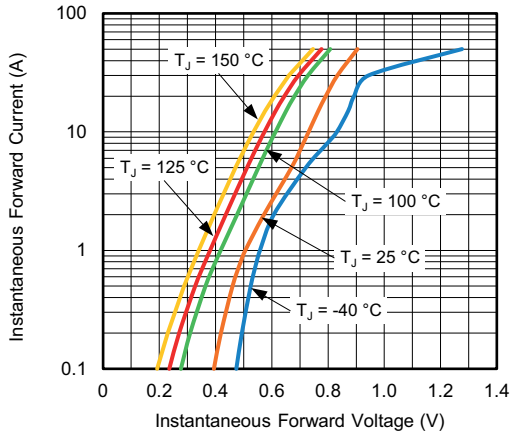


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

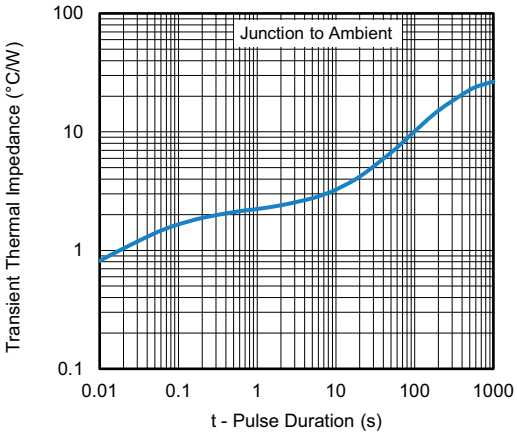
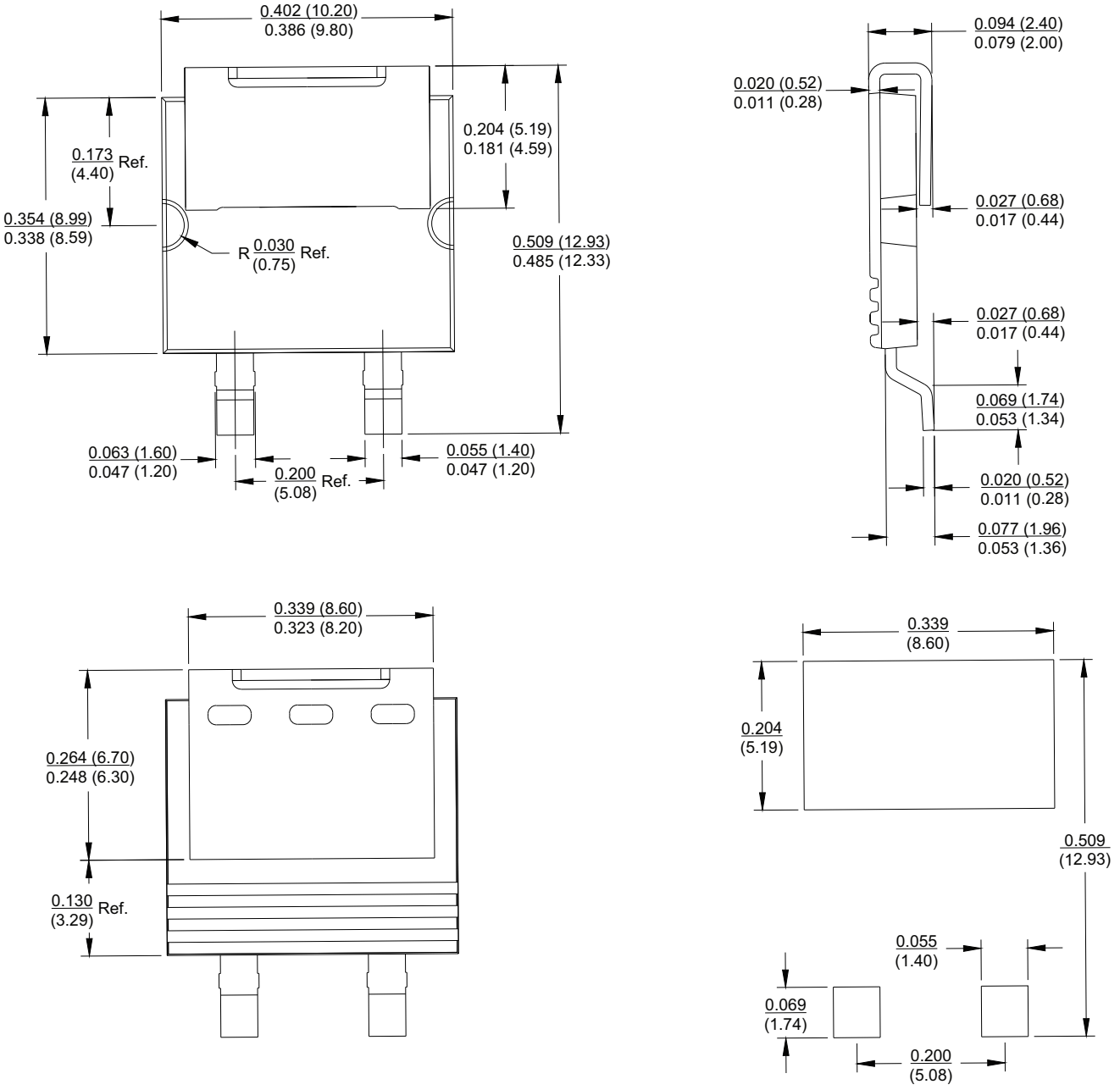


Fig. 6 - Typical Transient Thermal Impedance

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

eCool SMPD



Note

- The suggested mounting pad layout is provided for reference only, as actual pad layouts may vary depending on application

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