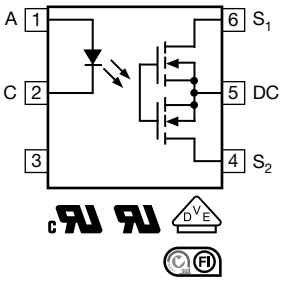
1 Form A Solid-State Relay (Normally Open)



Pb-free

RoHS COMPLIANT

HALOGEN FREE GREEN (5-2008)

FEATURES

- Isolation test voltage 5300 V_{RMS}
- Load voltage 250 V
- Load current 155 mA / 300 mA
- Clean bounce free switching
- Low power consumption
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- General telecom switching
- Instrumentation
- Industrial controls
- Automatic test equipment

LINKS TO ADDITIONAL RESOURCES

Product Page | Design Tools | SPICE Models

AGENCY APPROVALS

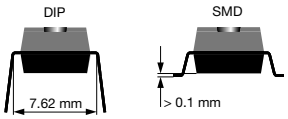
- UL 1577
- cUL
- DIN EN 60747-5-5 (VDE 0884-5)
- FIMKO

DESCRIPTION

The LH1518 is an SPST normally open switch (1 Form A) that can replace electromechanical relays in many applications. The relay is constructed using a GaAlAs LED for actuation control and high reliable MOSFETs for the output switch.

ORDERING INFORMATION

L	H	1	5	1	8	#	#	#	T	R
PART NUMBER						ELECTR. VARIATION	PACKAGE CONFIG.		TAPE AND REEL	



PACKAGE	UL, cUL, FIMKO, VDE
SMD-6, tube	LH1518AAB
SMD-6, tape and reel	LH1518AABTR
DIP-6, tube	LH1518AT

ABSOLUTE MAXIMUM RATINGS (T_{amb} = 25 °C, unless otherwise specified)				
PARAMETER	**CONDITION**	**SYMBOL**	**VALUE**	**UNIT**
INPUT				
IRED continuous forward current		I_F	50	mA
IRED reverse voltage		V_R	5	V
Input power dissipation		P_{diss}	80	mW
OUTPUT				
DC or peak AC load voltage		V_L	250	V
Continuous load current (AC/DC configuration)		I_L	155	mA
Continuous load current (DC only configuration)		I_L	300	mA
SSR output power dissipation (continuous)		P_{diss}	550	mW
SSR				
Ambient temperature range		T_{amb}	-40 to +85	°C
Storage temperature range		T_{stg}	-40 to +150	°C
Soldering temperature	t = 10 s max.	T_{sld}	260	°C

Note

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

ELECTRICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)						
PARAMETER	**TEST CONDITION**	**SYMBOL**	**MIN.**	**TYP.**	**MAX.**	**UNIT**
INPUT						
IRED forward current, switch turn-on	I_L = 100 mA, t = 10 ms	I_{Fon}	-	0.4	2	mA
IRED forward current, switch turn-off	V_L = ± 200 V	I_{Foff}	0.05	0.35	-	mA
IRED forward voltage	I_F = 10 mA	V_F	1.15	1.4	1.6	V
OUTPUT						
On-resistance (AC/DC configuration)	I_F = 5 mA, I_L = 50 mA	R_{ON}	6	12	20	Ω
On-resistance (DC only configuration)	I_F = 5 mA, I_L = 100 mA	R_{ON}	1.5	3.2	5	Ω
Off-resistance	I_F = 0 mA, V_L = ± 100 V	R_{OFF}	0.5	5000	-	GΩ
Off-state leakage current	I_F = 0 mA, V_L = ± 100 V	I_O	-	< 1	200	nA
	I_F = 0 mA, V_L = ± 250 V	I_O	-	< 1	1000	nA
Output capacitance (AC/DC configuration)	I_F = 0 mA, V_L = 1 V, 1 MHz	C_O	-	39	-	pF
	I_F = 0 mA, V_L = 50 V, 1 MHz	C_O	-	6	-	pF
TRANSFER						
Capacitance (input to output)	V_{IO} = 1 V	C_{IO}	-	0.4	-	pF

Note

- Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.

PIN CONFIGURATION

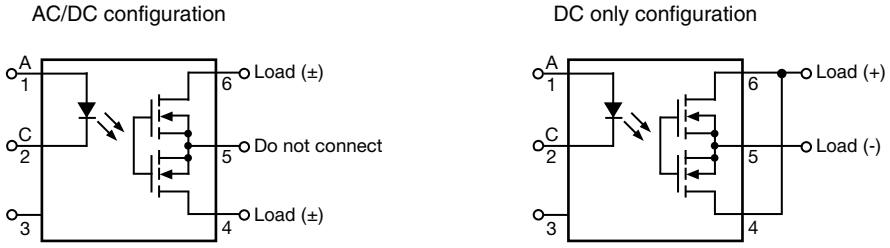


Fig. 1 - Pin Configuration

SWITCHING CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)						
PARAMETER	**TEST CONDITION**	**SYMBOL**	**MIN.**	**TYP.**	**MAX.**	**UNIT**
Turn-on time	I_F = 5 mA, I_L = 50 mA	t_{on}	-	0.20	3	ms
Turn-off time	I_F = 5 mA, I_L = 50 mA	t_{off}	-	0.03	3	ms

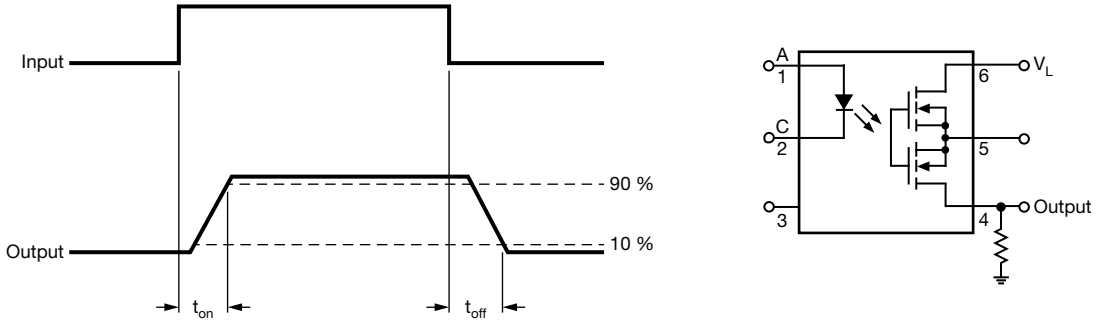


Fig. 2 - Timing Schematic

SAFETY AND INSULATION RATINGS				
PARAMETER	**CONDITION**	**SYMBOL**	**VALUE**	**UNIT**
Climatic classification	According to IEC 68 part 1		40 / 85 / 21	
Pollution degree	According to DIN VDE 0109		2	
Comparative tracking index	Insulation group IIIa	CTI	175	
Maximum rated withstanding isolation voltage	According to UL1577, t = 1 min	V_{ISO}	5300	V_{RMS}
Maximum transient isolation voltage	According to DIN EN 60747-5-5	V_{IOTM}	8000	V_{peak}
Maximum repetitive peak isolation voltage	According to DIN EN 60747-5-5	V_{IORM}	890	V_{peak}
Insulation resistance	V_{IO} = 500 V, T_{amb} = 25 °C	R_{IO}	$\geq 10^{12}$	Ω
	V_{IO} = 500 V, T_{amb} = 100 °C	R_{IO}	$\geq 10^{11}$	Ω
Output safety power		P_{SO}	700	mW
Input safety current		I_{SI}	240	mA
Safety temperature		T_S	175	°C
Creepage distance			≥ 7	mm
Clearance distance			≥ 7	mm
Insulation thickness		DTI	≥ 0.4	mm
Input to output test voltage, method B	V_{IORM} x 1.875 = V_{PR}, 100 % production test with t_M = 1 s, partial discharge < 5 pC	V_{PR}	1669	V_{peak}
Input to output test voltage, method A	V_{IORM} x 1.6 = V_{PR}, 100 % sample test with t_M = 10 s, partial discharge < 5 pC	V_{PR}	1424	V_{peak}

Note

- As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for “safe electrical insulation” only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

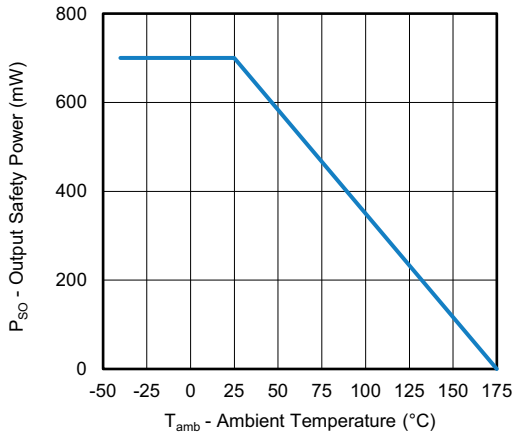


Fig. 3 - Output Safety Power vs. Ambient Temperature

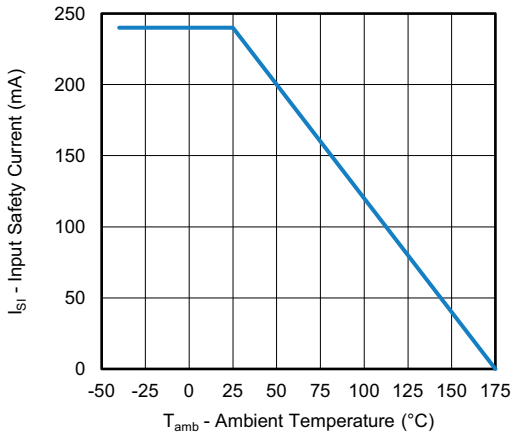


Fig. 4 - Input Safety Current vs. Ambient Temperature

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

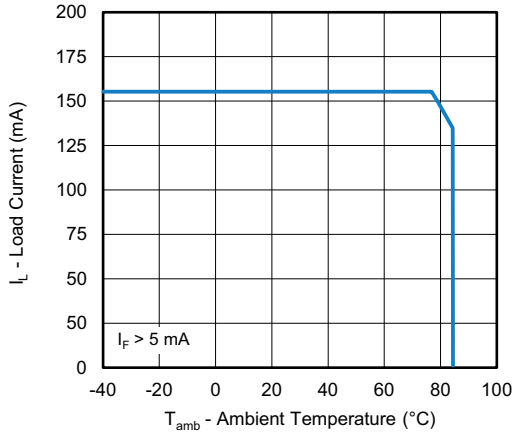


Fig. 5 - Load Current vs. Ambient Temperature

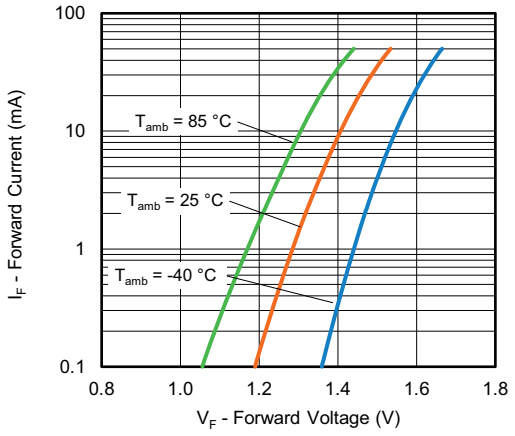


Fig. 7 - Forward Current vs. Forward Voltage

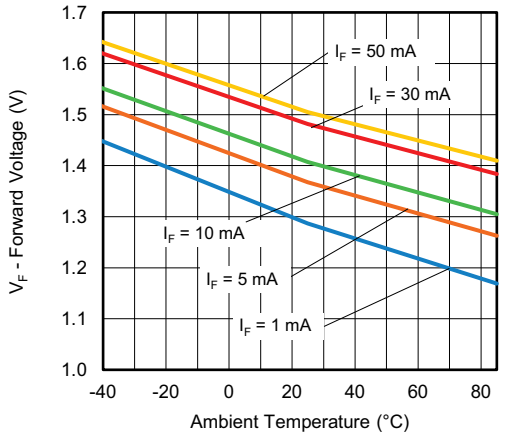


Fig. 6 - Forward Voltage vs. Ambient Temperature

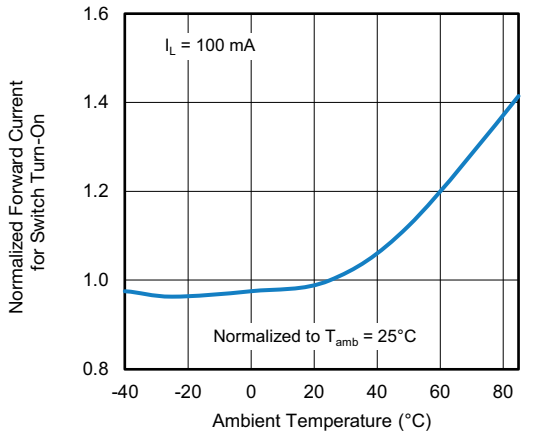


Fig. 8 - Normalized Forward Current for Switch Turn-On vs. Ambient Temperature

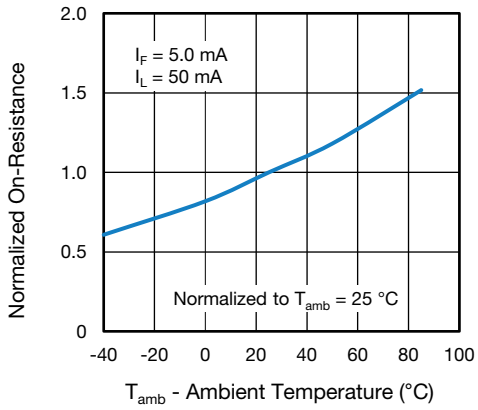


Fig. 9 - Normalized On-Resistance vs. Ambient Temperature

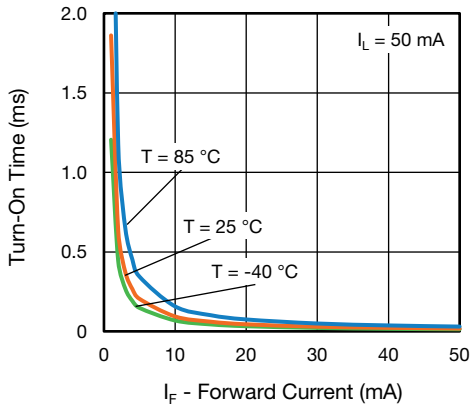


Fig. 12 - Turn-On Time vs. Forward Current

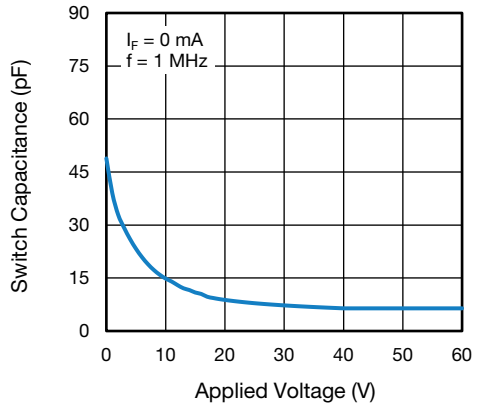


Fig. 10 - Switch Capacitance vs. Applied Voltage

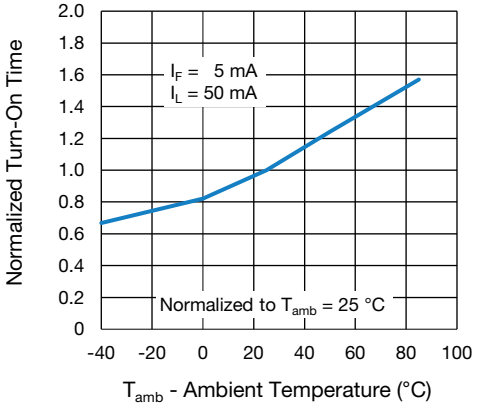


Fig. 13 - Normalized Turn-On Time vs. Ambient Temperature

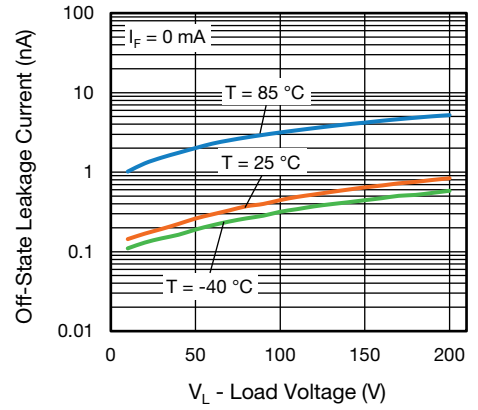


Fig. 11 - Off-State Leakage Current vs. Load Voltage

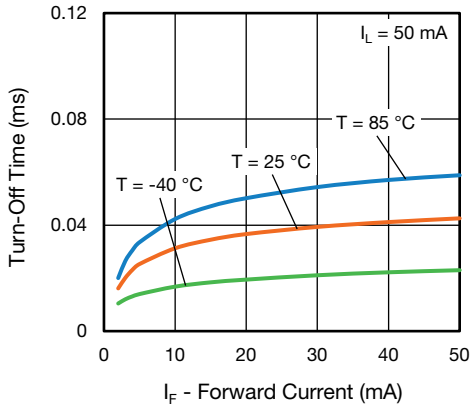


Fig. 14 - Turn-Off Time vs. Forward Current

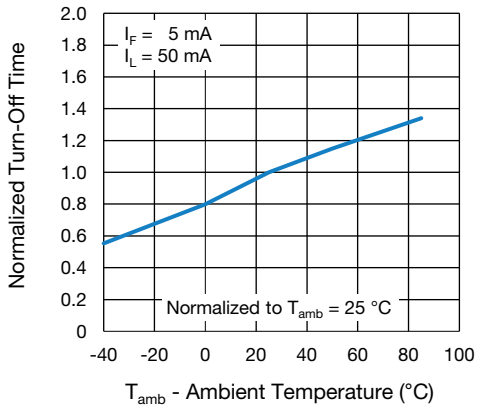


Fig. 15 - Normalized Turn-Off Time vs. Ambient Temperature

PACKAGE DIMENSIONS (in millimeters)

SMD-6

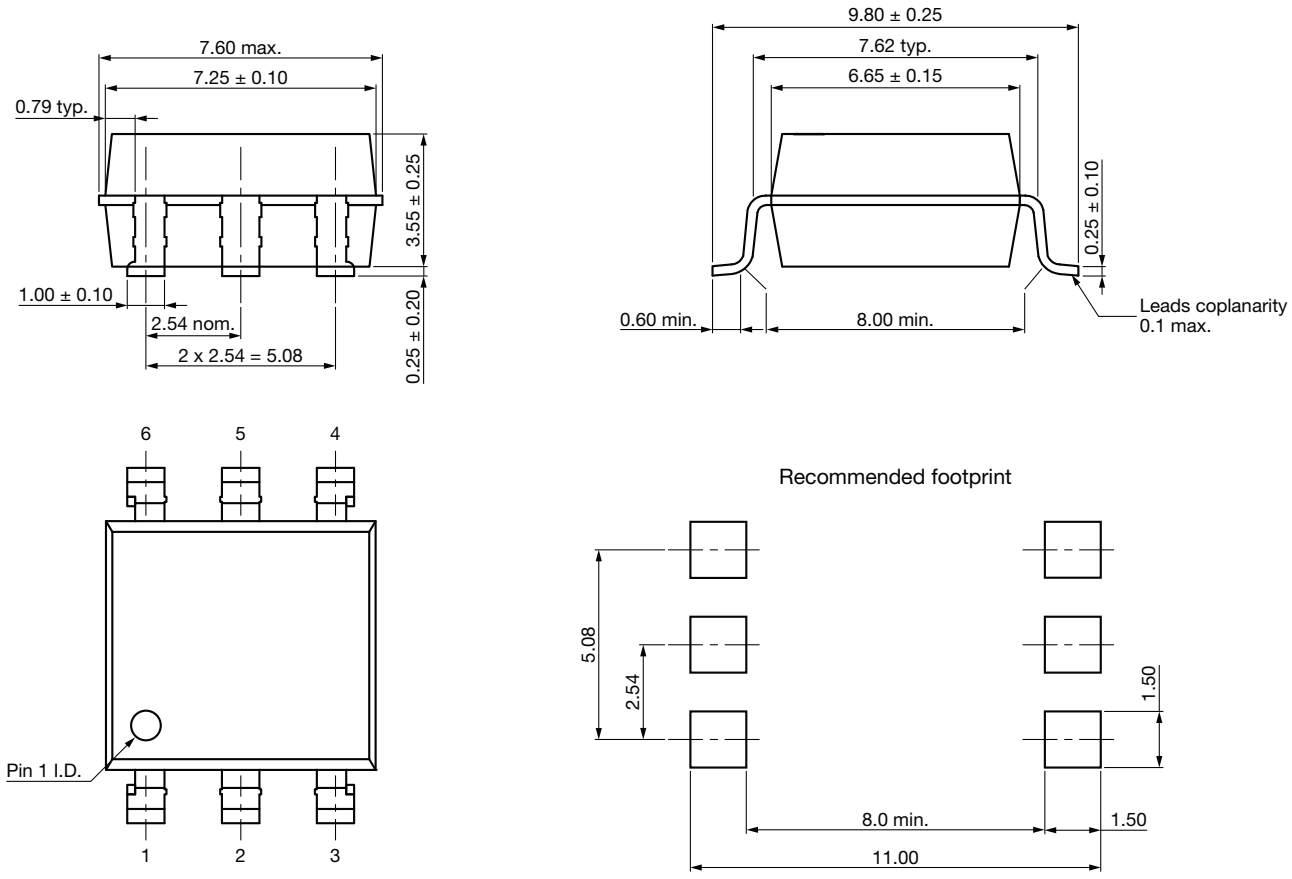


Fig. 16 - Package Drawings

DIP-6

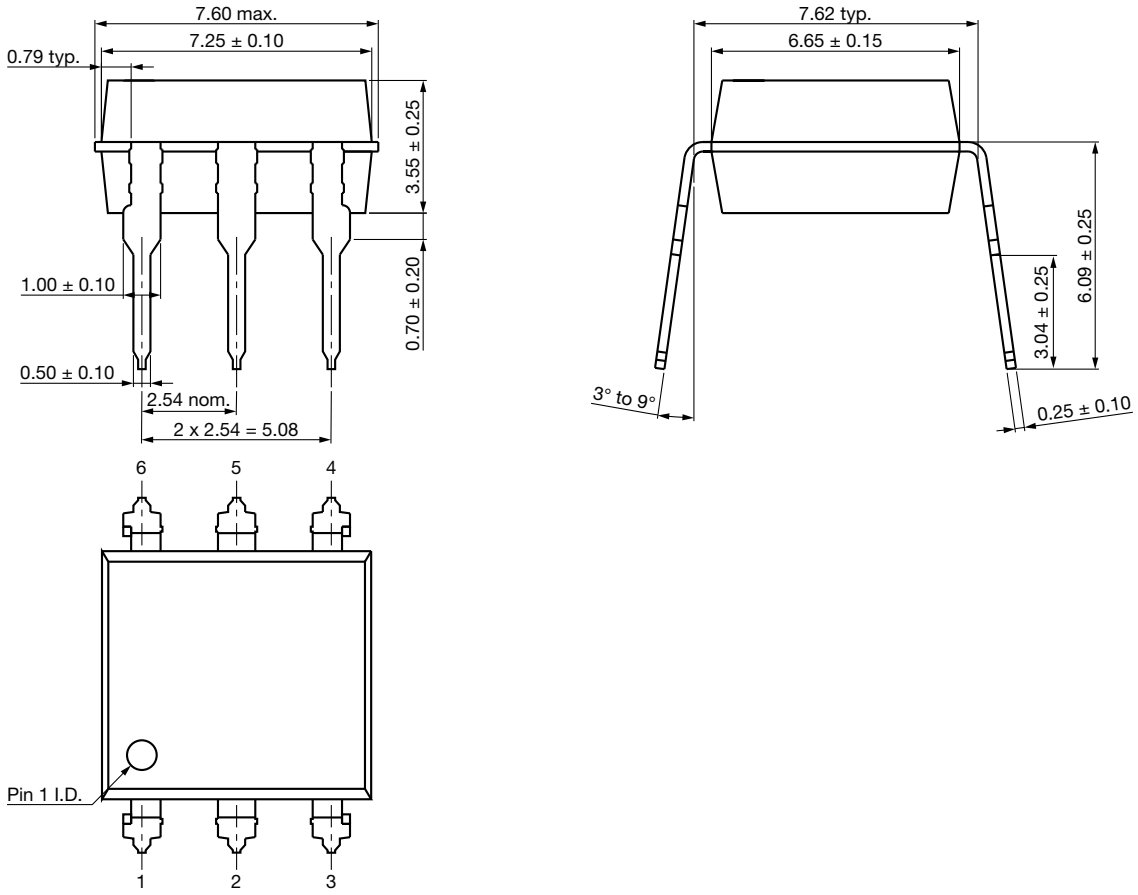


Fig. 17 - Package Drawings

PACKAGE MARKING

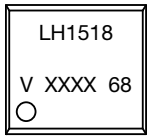


Fig. 18 - LH1518

Notes

- XXXX = LMC (lot marking code)
- Tape and reel suffix (TR) is not part of the package marking

PACKING INFORMATION (in millimeters)

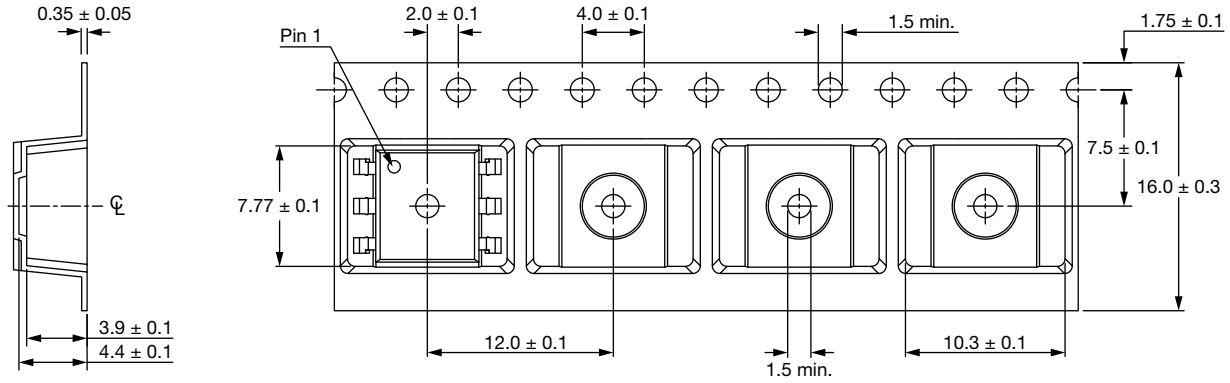


Fig. 19 - Tape and Reel Packing

TAPE AND REEL PACKING	
TYPE	**UNITS/REEL**
SMD-6	1000

TUBE PACKING			
TYPE	**UNITS/TUBE**	**TUBES/BOX**	**UNITS/BOX**
SMD-6	50	40	2000
DIP-6	50	40	2000

SOLDER PROFILES

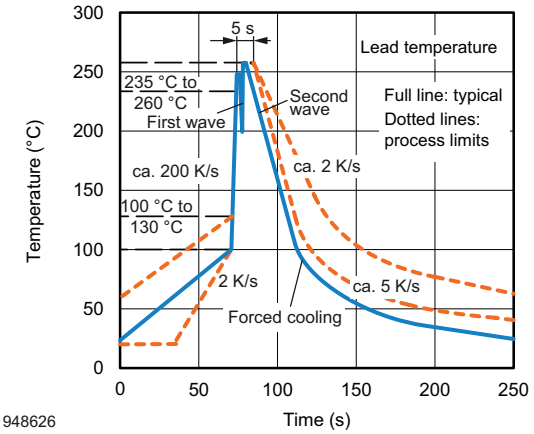


Fig. 20 - Wave Soldering Double Wave Profile
According to J-STD-020 for DIP Devices

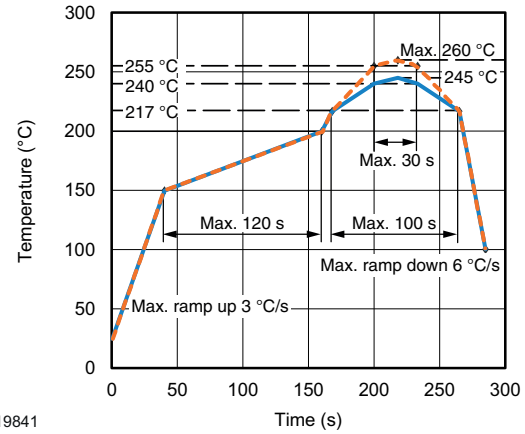


Fig. 21 - Lead (Pb)-free Reflow Solder Profile
According to J-STD-020 for SMD Devices

HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2

Floor life: unlimited

Conditions: T_{amb} < 30 °C, RH < 60 %

Moisture sensitivity level 1, according to J-STD-020

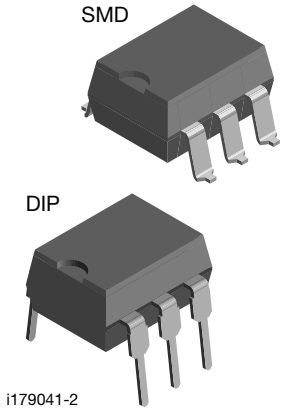
Footprint and Schematic Information for LH1518AAB, LH1518AABTR, LH1518AT

The footprint and schematic symbols for the following parts can be accessed using the associated links. They are available in Eagle, Altium, KiCad, OrCAD / Allegro, Pulsonix, and PADS.

Note that the 3D models for these parts can be found on the Vishay product page.

PART NUMBER	FOOTPRINT / SCHEMATIC
LH1518AAB	www.snapeda.com/parts/LH1518AAB/Vishay/view-part
LH1518AABTR	www.snapeda.com/parts/LH1518AABTR/Vishay/view-part
LH1518AT	www.snapeda.com/parts/LH1518AT/Vishay/view-part

For technical issues and product support, please contact optocoupleranswers@vishay.com.



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