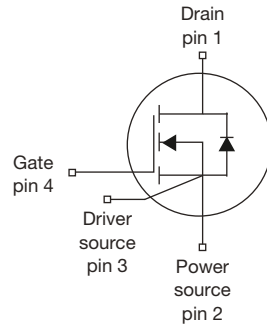
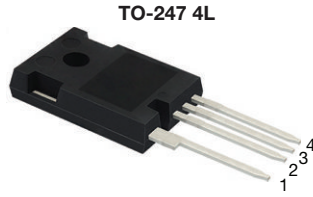


MaxSiC™ 1200 V N-Channel SiC MOSFET


Marking Code: 120A250FL

PRODUCT SUMMARY	
V_{DS} (V) at T_J max.	1200
$R_{DS(on)}$ typ. ($m\Omega$) at 25 °C	$V_{GS} = 20$ V 250
Q_g typ. (nC)	20
I_D (A)	10.5
C_{oss} (pF)	21.2
P_D (W)	56
Configuration	Single

ORDERING INFORMATION	
Package	TO-247 4L
Lead (Pb)-free and halogen-free	MXP120A250FL-Y-GE3

ABSOLUTE MAXIMUM RATINGS ($T_C = 25$ °C, unless otherwise noted)				
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-source voltage ^a		V_{DS}	1200	V
Gate-source voltage		V_{GS}	-10 / +22	
Continuous drain current	$T_C = 25$ °C	I_D	10.5	A
	$T_C = 100$ °C	I_D	6.7	
Pulsed drain current ^b		I_{DM}	21	
Short-circuit withstand time		T_{SC}	3	μ s
Maximum power dissipation	$T_C = 25$ °C	P_D	56	W
	$T_C = 100$ °C	P_D	22	
Operating junction and storage temperature range		T_J, T_{stg}	-55 to +150	°C

Notes

- a. $T_J = 25$ °C to 150 °C
 b. Repetitive rating; pulse width limited by maximum junction temperature

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

FEATURES

- Fast switching speed
- Short circuit withstand time 3 μ s
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912


RoHS
 COMPLIANT
 HALOGEN
FREE
APPLICATIONS

- Charger
- Industrial UPS
- Boost inverter
- DC/DC converter



THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	TYP.	MAX.	UNIT
Maximum junction-to-ambient	R_{thJA}	-	40	°C/W
Maximum junction-to-case (drain)	R_{thJC}	-	2.24	

SPECIFICATIONS ($T_J = 25\text{ °C}$, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-source breakdown voltage	V_{DS}	$V_{GS} = 0\text{ V}, I_D = 1\text{ mA}$	1200	-	-	V
Gate-source threshold voltage (N)	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 10\text{ mA}$	-	3.1	-	V
		$V_{DS} = V_{GS}, I_D = 10\text{ mA}, T_J = 150\text{ °C}$	-	2.3	-	V
Gate-source leakage	I_{GSS}	$V_{GS} = +22\text{ V}, V_{DS} = 0\text{ V}$	-	-	100	nA
		$V_{GS} = -10\text{ V}, V_{DS} = 0\text{ V}$	-	-	-100	
Zero gate voltage drain current	I_{DSS}	$V_{DS} = 960\text{ V}, V_{GS} = 0\text{ V}$	-	-	10	μA
Drain-source on-state resistance	$R_{DS(on)}$	$V_{GS} = 20\text{ V}, I_D = 4\text{ A}$	-	250	313	mΩ
		$V_{GS} = 20\text{ V}, I_D = 4\text{ A}, T_J = 150\text{ °C}$	-	380	475	
		$V_{GS} = 18\text{ V}, I_D = 4\text{ A}$	-	287	359	
		$V_{GS} = 18\text{ V}, I_D = 4\text{ A}, T_J = 150\text{ °C}$	-	395	494	
Dynamic						
Input capacitance	C_{iss}	$V_{GS} = 0\text{ V}, V_{DS} = 800\text{ V}, f = 1\text{ MHz}$	-	447	-	pF
Output capacitance	C_{oss}		-	21.2	-	
Reverse transfer capacitance	C_{rss}		-	3.2	-	
Total gate charge	Q_g	$V_{GS} = 18\text{ V}, I_D = 4\text{ A}, V_{DS} = 800\text{ V}$	-	20.3	-	nC
Gate-source charge	Q_{gs}		-	5.5	-	
Gate-drain charge	Q_{gd}		-	7.9	-	
Gate Resistance	R_g	$V_{DS} = 0\text{ V}, f = 1\text{ MHz}$	-	34	-	Ω
Switching Characteristics						
Turn-on delay time	$t_{d(on)}$	$V_{GS} = -5\text{ V} \sim 18\text{ V}, I_D = 4\text{ A}, V_{DS} = 800\text{ V}, R_{g(ext)} = 4.4\text{ Ω}$	-	8.5	-	ns
Rise time	t_r		-	11.5	-	
Turn-off delay time	$t_{d(off)}$		-	8.5	-	
Fall time	t_f		-	14.5	-	
Turn-on switching energy	E_{on}		-	67	-	μJ
Turn-off switching energy	E_{off}		-	5	-	
Reverse Diode Characteristics						
Reverse recovery time	t_{rr}	$V_{GS} = -5\text{ V}, I_{SD} = 4\text{ A}, V_R = 800\text{ V}, di/dt = 1000\text{ A/μs}$	-	7.5	-	ns
Reverse recovery charge	Q_{rr}		-	12	-	nC
Reverse recovery current	I_{rrm}		-	2.8	-	A



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

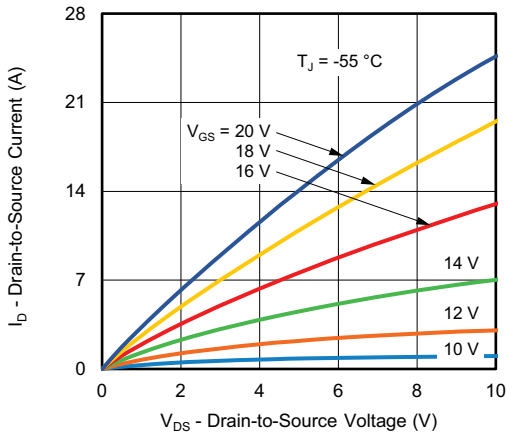


Fig. 1 - Typical Output Characteristics

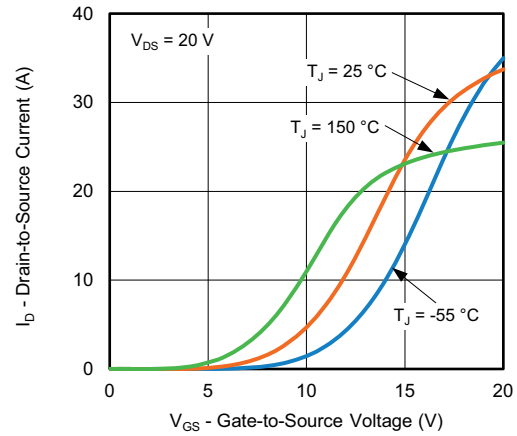


Fig. 4 - Typical Transfer Characteristics

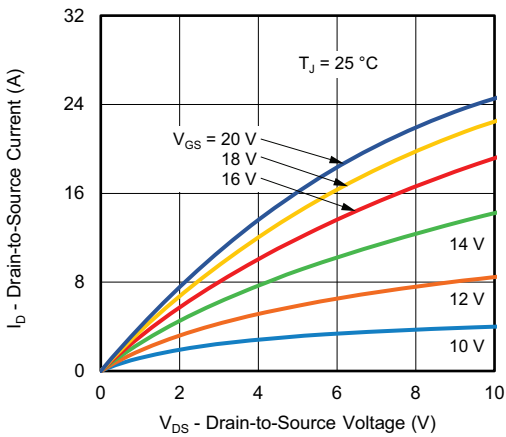


Fig. 2 - Typical Output Characteristics

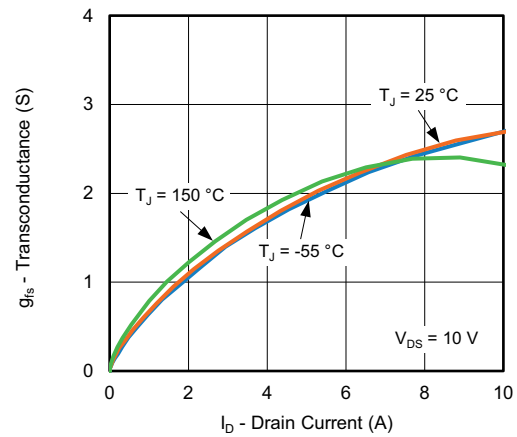


Fig. 5 - Forward Transconductance vs. Drain Current

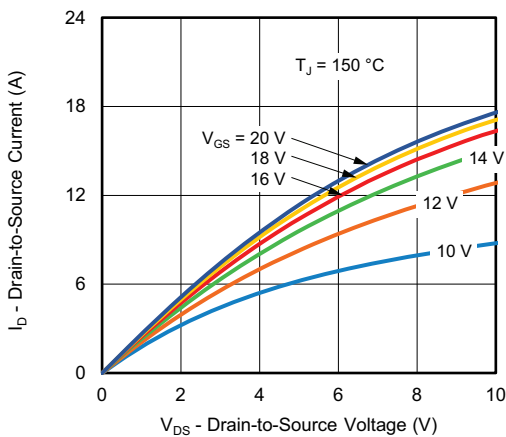


Fig. 3 - Typical Output Characteristics

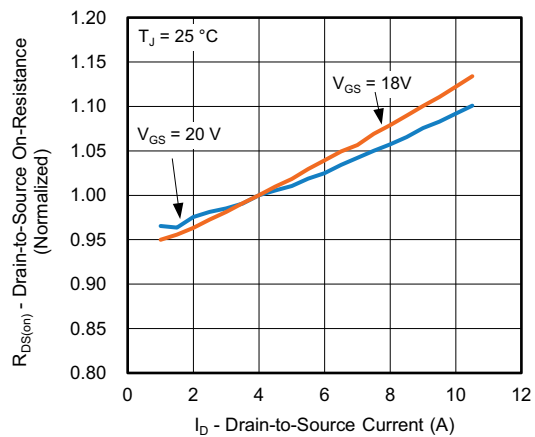


Fig. 6 - Normalized On-Resistance vs. Drain-to-Source Current

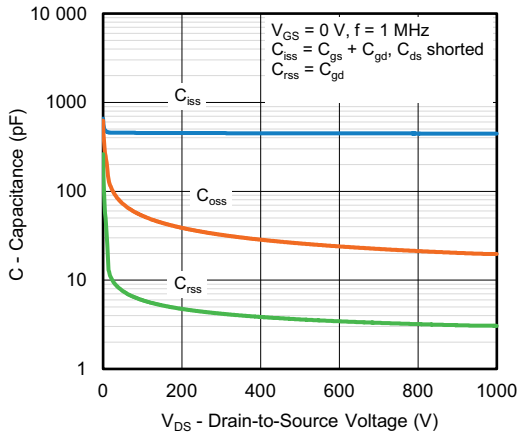


Fig. 7 - Typical Capacitance vs. Drain-to-Source Voltage

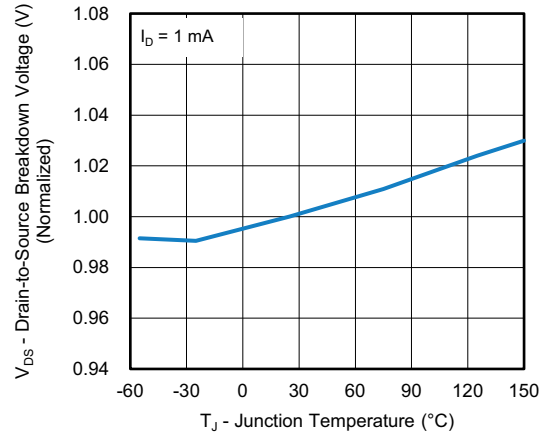


Fig. 10 - Temperature vs. Drain-to-Source Voltage

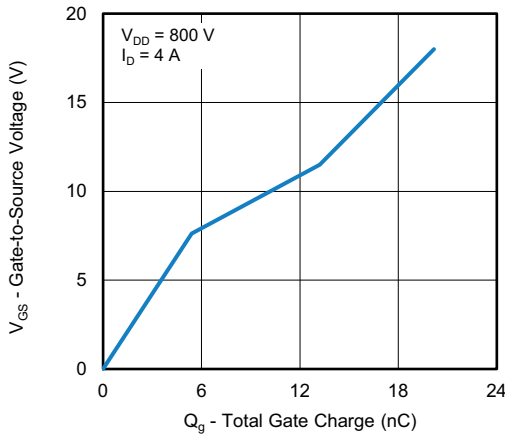


Fig. 8 - Typical Gate Charge vs. Gate-to-Source Voltage

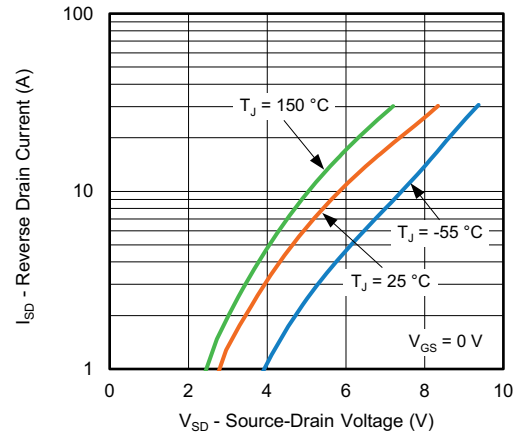


Fig. 11 - Typical Source-Drain Diode Forward Voltage

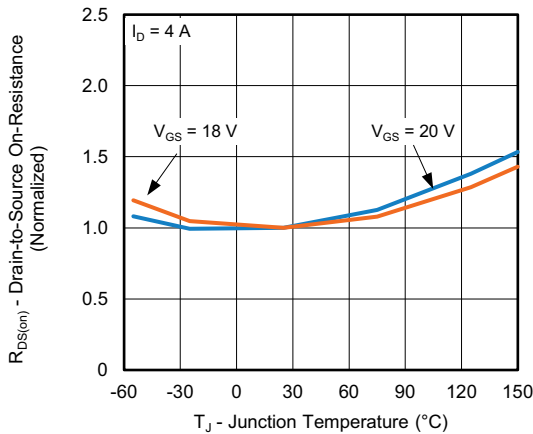


Fig. 9 - Normalized On-Resistance vs. Temperature

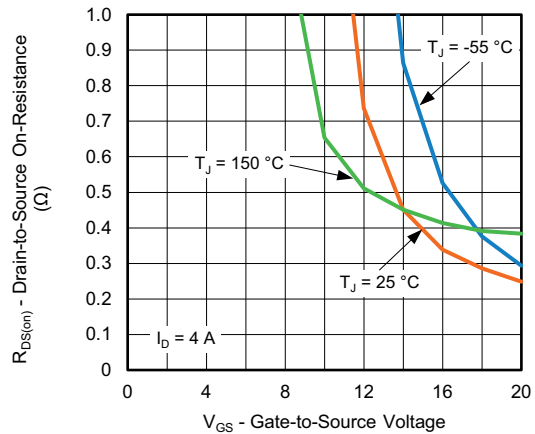


Fig. 12 - On-Resistance vs. Gate-to-Source Voltage

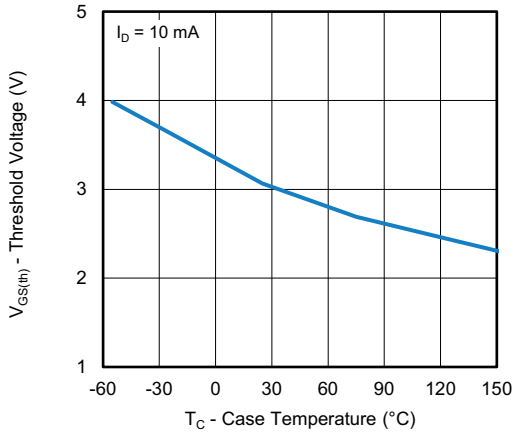


Fig. 13 - Threshold Voltage vs. Case Temperature

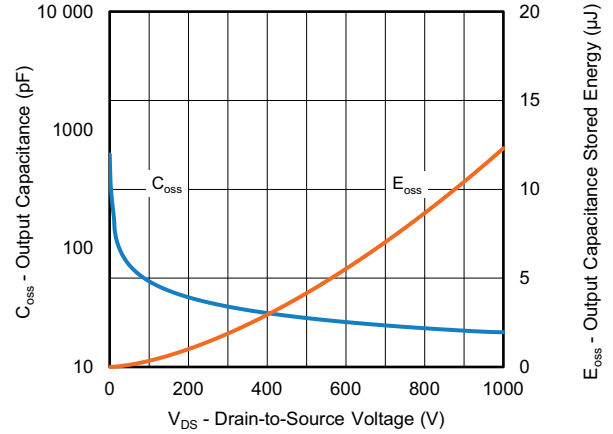


Fig. 15 - Output Capacitance and its Stored Energy vs. Drain-to-Source Voltage

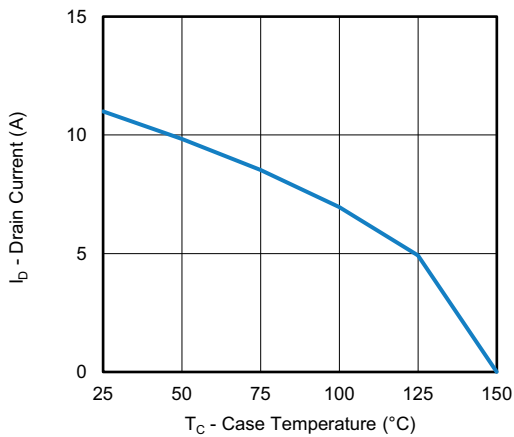


Fig. 14 - Drain Current vs. Case Temperature

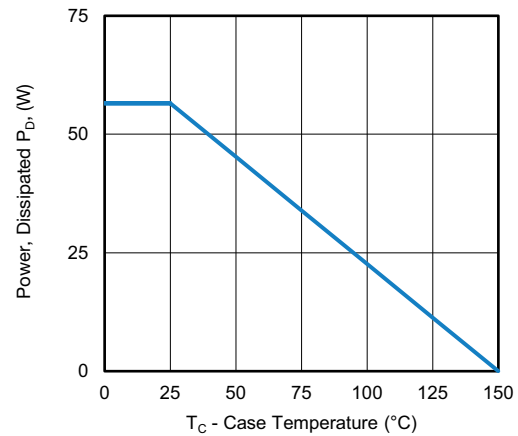


Fig. 16 - Power, Dissipated P_D vs. Case Temperature

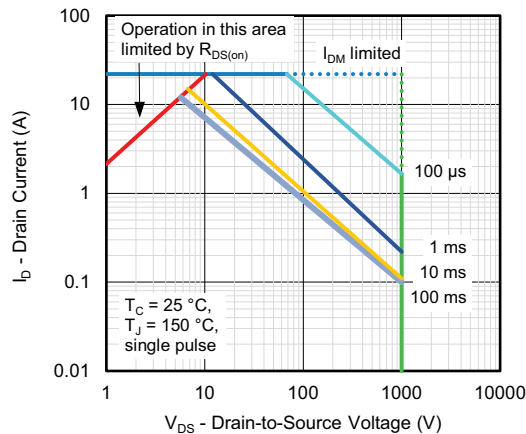


Fig. 17 - Safe Operating Area

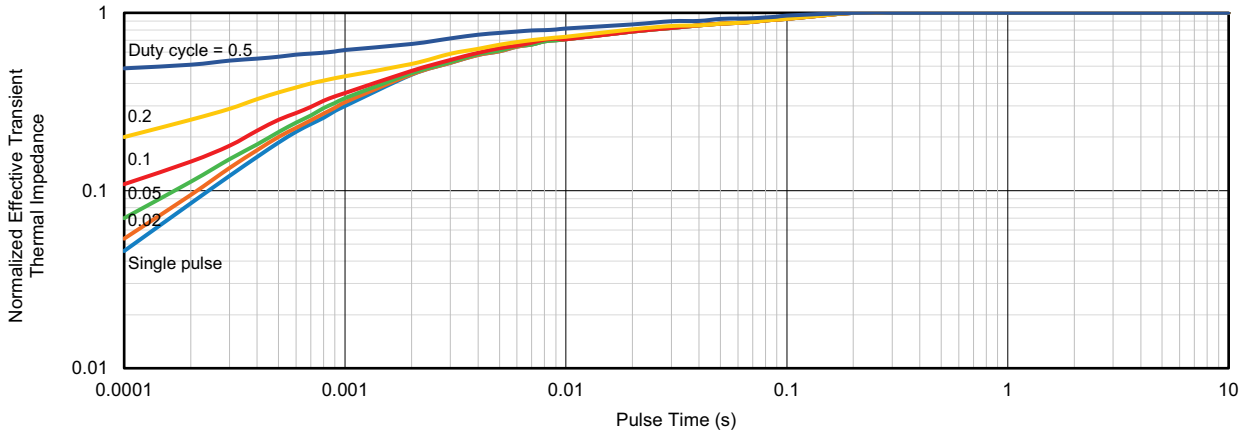


Fig. 18 - Normalized Effective Transient Thermal Impedance

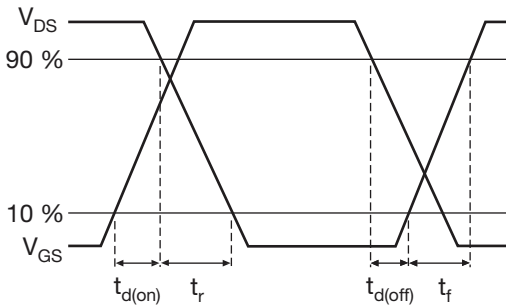


Fig. 19 - Waveforms of Switching Time

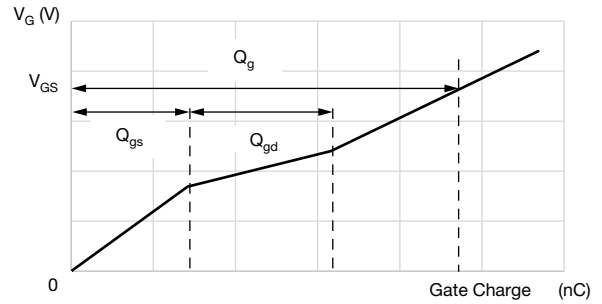


Fig. 22 - Waveforms for Gate Charge

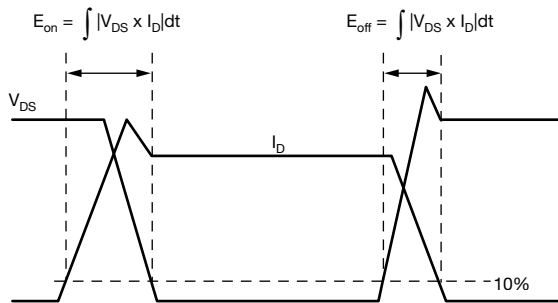


Fig. 20 - Waveforms for Switching Energy

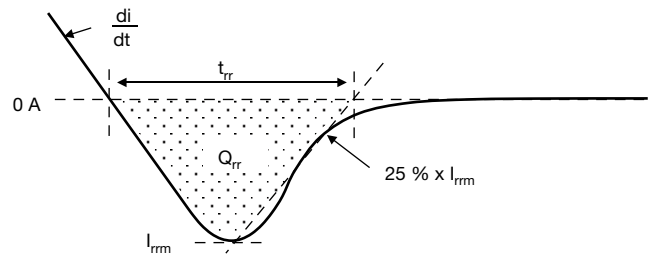


Fig. 23 - Waveforms for Reverse Recovery

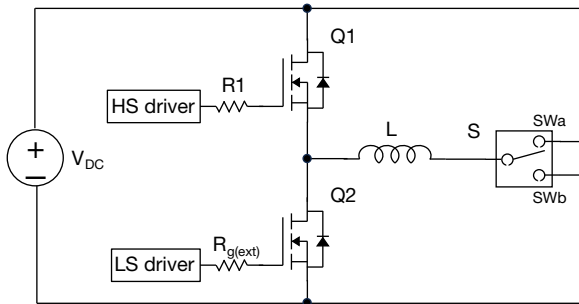


Fig. 21 - Switching and Reverse Diode Characteristics Measurement Circuit

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